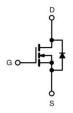
## **Sinai Power Technologies**

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## **N-channel Power MOSFET**

PRODUCT SUMMARY				
V <sub>DS</sub> (V) at T <sub>J</sub> max.	550			
R <sub>DS(on)</sub> max. at 25°C (Ω)	V <sub>GS</sub> =10V	1.5		
Q <sub>g</sub> max. (nC)	42			
Q <sub>gs</sub> (nC)	12			
Q <sub>gd</sub> (nC)	6.5			
Configuration	single			





TO-252

Schematic diagram

### **Features**

- I<sub>D</sub>=5A(Vgs=10V)
- Ultra Low Gate Charge
- Improved dv/dt Capability
- 100% Avalanche Tested
- RoHS compliant

## **Applications**

- Switching Mode Power Supplies (SMPS)
- PWM Motor Controls
- DC to DC Converters
- LED Lighting
- Bridge Circuits

ORDERING INFORMATION				
Device	SPD5N50G			
Device Package	TO-252			
Marking	5N50G			

ABSOLUTE MAXIMUM RATINGS (Tc = 25°C, unless otherwise noted)					
Parameter		Limit	Unit		
Drain to Source Voltage	V <sub>DSS</sub>	500 <sup>(1)</sup>	V		
Continuous Drain Current (@T <sub>C</sub> =25°C)		5 <sup>(1)</sup>	Α		
Continuous Drain Current (@T <sub>C</sub> =100°C)	- I <sub>D</sub>	3.1	Α		
Drain current pulsed (2)	I <sub>DM</sub>	20	Α		
Gate to Source Voltage	V <sub>GS</sub>	30	V		
Single pulsed Avalanche Energy (3)	E <sub>AS</sub> 285		mJ		
Peak diode Recovery dv/dt (4)	dv/dt	6	V/ns		
Total power dissipation (@T <sub>C</sub> =25°C)	Б	112	W		
Derating Factor above 25°C	P <sub>D</sub>	0.88	W/ºC		
Operating Junction Temperature & Storage Temperature	T <sub>STG</sub> , T <sub>J</sub>	-55 to + 150			
Maximum lead temperature for soldering purpose		260	°C		

#### Notes

- 1. Drain current is limited by maximum junction temperature.
- 2. Repetitive rating : pulse width limited by junction temperature.
- 3. L = 15mH,  $I_{AS}$  = 5A,  $V_{DD}$  = 50V,  $R_{G}$ =25 $\Omega$ , Starting at  $T_{J}$  = 25 $^{\circ}$ C
- 4.  $I_{SD} \le 5A$ , di/dt = 100A/us,  $V_{DD} \le BV_{DSS}$ , Starting at  $T_J = 25^{\circ}C$



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THERMAL CHARACTERISTICS				
Parameter	Symbol	Value	Unit	
Thermal resistance, Junction to case	R <sub>thjc</sub>	0.75	°C/W	
Thermal resistance, Junction to ambient	R <sub>thja</sub>	62.5	°C/W	

ELECTRICAL CHARACTERISTICS (Tc = 25°C unless otherwise specified)							
Parameter	Symbol	Test conditions	Min.	Тур.	Max.	Unit	
Off Characteristics							
Drain to source breakdown voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	500			V	
Breakdown voltage temperature coefficient	ΔBV <sub>DSS</sub> / ΔTJ	I <sub>D</sub> =250uA, referenced to 25°C		0.5		V/°C	
Drain to source leakage current		V <sub>DS</sub> =500V, V <sub>GS</sub> =0V			1	uA	
	I <sub>DSS</sub>	V <sub>DS</sub> =400V, T <sub>C</sub> =125°C			50	uA	
Gate to source leakage current, forward	I <sub>GSS</sub>	$V_{GS}$ =30V, $V_{DS}$ =0V			100	nA	
Gate to source leakage current, reverse	IGSS	$V_{GS}$ =-30V, $V_{DS}$ =0V			-100	nA	
On Characteristics							
Gate threshold voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	2		4	V	
Drain to source on state resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =2.5A		1.35	1.5	Ω	
Forward Transconductance	Gfs	$V_{DS} = 30 \text{ V}, I_{D} = 2.5 \text{A}$		3.7		S	
Dynamic Characteristics							
Input capacitance	C <sub>iss</sub>			1271			
Output capacitance	Coss	$V_{GS}$ =0V, $V_{DS}$ =25V, f=1MHz		143		pF	
Reverse transfer capacitance	C <sub>rss</sub>			1			
Turn on delay time	t <sub>d(on)</sub>			27.5			
Rising time	tr	$V_{DS}$ =250V, $I_{D}$ =5A , $R_{G}$ =25 $\Omega$		25		no	
Turn off delay time	t <sub>d(off)</sub>	VDS-200V, ID-0A , RG-2012		127		ns	
Fall time	t <sub>f</sub>			35			
Total gate charge	$Q_g$	V <sub>DS</sub> =400V, V <sub>GS</sub> =10V, I <sub>D</sub> =5A		31.5			
Gate-source charge	Q <sub>gs</sub>			12		nC	
Gate-drain charge	$Q_{gd}$			6.5			

SOURCE TO DRAIN DIODE RATINGS CHARACTERISTICS								
Parameter	Symbol	Test conditions	Min.	Тур.	Max.	Unit		
Continuous source current	Is	Integral reverse p-n Junction			5	Α		
Pulsed source current	I <sub>SM</sub>	diode in the MOSFET			20	Α		
Diode forward voltage drop.	V <sub>SD</sub>	I <sub>S</sub> =5A, V <sub>GS</sub> =0V			1.2	٧		
Reverse recovery time	T <sub>rr</sub>	I <sub>S</sub> =5A, V <sub>GS</sub> =0V,		567		ns		
Reverse recovery Charge	Qrr	dI <sub>F</sub> /dt=100A/us		4		uC		

## Fig1. Output characteristics

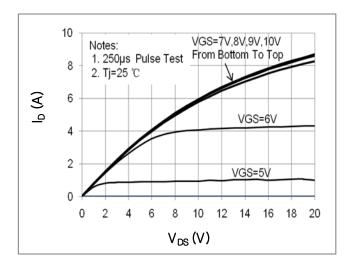


Fig3. Gate charge characteristics

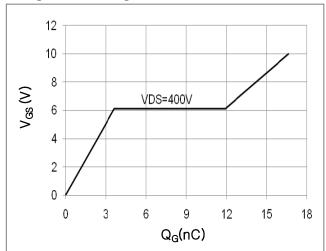


Fig 5. RDS(ON) vs junction temperature

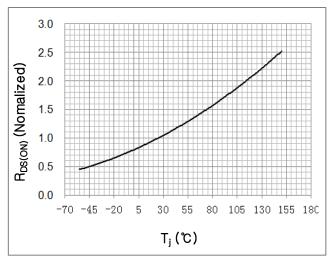


Fig2. Drain-source on-state resistance

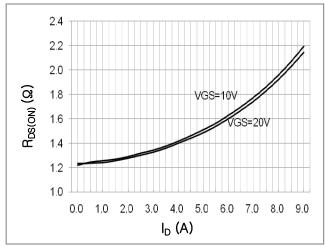
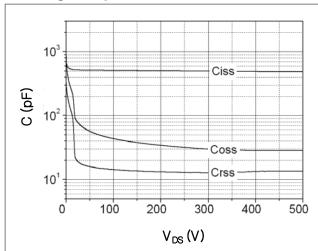


Fig 4. Capacitance Characteristics



BV<sub>DSS</sub> vs junction temperature

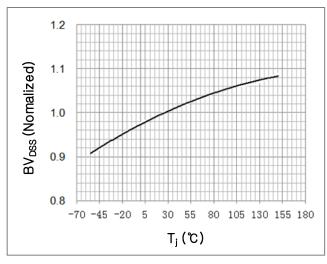


Fig 7. Safe operating area

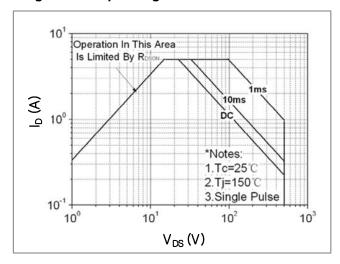


Fig 8. Transient thermal impedance

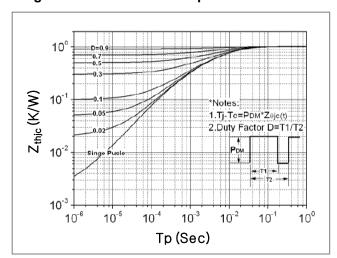


Fig 9. Forward characteristics of reverse diode

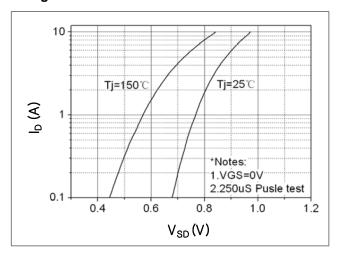
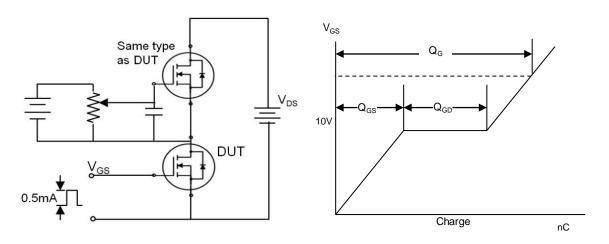
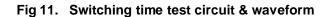


Fig 10. Gate charge test circuit & waveform







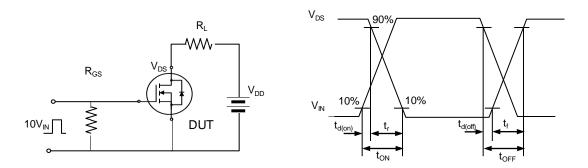


Fig 12. Unclamped Inductive switching test circuit & waveform

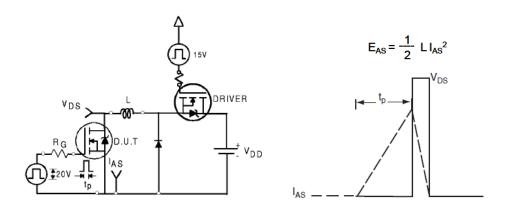
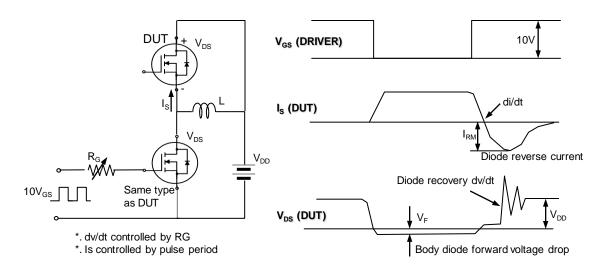


Fig 13. Peak diode recovery dv/dt test circuit & waveform







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