

MICROELECTRONICS CO., LTD.

P1800SB

Thyristor Surge Protective Devices

Revision:A

General Description

P1800SB is a solid state crowbar device designed to protect telecom equipment during hazardous transient conditions. It is a two terminal solid state device capable to drain a surge current pulse to ground when a transient voltage appears in between its two terminals when a specific maximum voltage delimited by the maximum breakover voltage of the device is reached.

Features

- Biderectional crowbar protection
- Continuous reverse voltage :170V
- Low leakage current: IR=10uA max.
- Holding current: IH=150mA min.

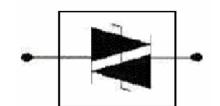
Main applications

- Interface circuit
- Analog line cards

Functional diagram





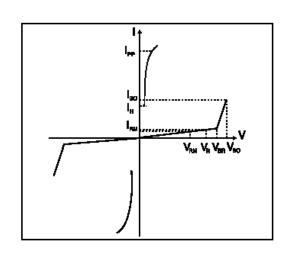


Absolute Ratings (Tamb=25℃)

Symbol	Parameter	Value	Unit	
Ts	Storage temperature rai	-40 to +150	$^{\circ}$	
Tj	Maximum junction temper	150	$^{\circ}$ C	
Ірр	Repetitive peak pulse current	10/1000µs 10/700µs 10/160µs 8/20µs 2/10µs	75 100 150 250 250	А
I _{TSM}	Non repetitive surge peak on-state current (sinusoidal)	t=16.6ms	20	А

Electrical Parameters

Symbol	Parameter				
V_{RM}	Stand-off voltage				
V_{BR}	Breakdown voltage				
V_{BO}	Switching Voltage				
I _{BO}	Breakover current				
I _{RM}	Leakage current at VRM				
I _{PP}	Peak pulse current				
lн	Holding current				
VT	On-state Voltage at I⊤				
Со	Off-state Capacitance				



Electrical Characteristics (T_{amb}=25°C)

Туре	V _{RM}	I _{RM}	V _{BO}	I _{BO}	V _T	Ι _Τ	Co	lн
	Min.		Max.	Max.	Max.		Max.	Min.
	V	μΑ	V	mA	V	Α	pF	mA
P1800SB	170	10	230	800	4	1	60	150

Typical Characteristics

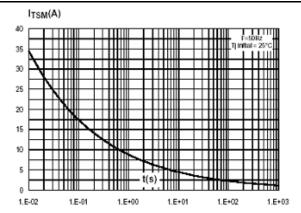


Fig.1:Non repetitive surge peak on-state current versus overload duration

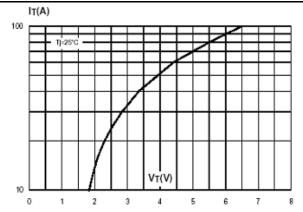


Fig.2:On-state voltage versus on-state current(typical values)

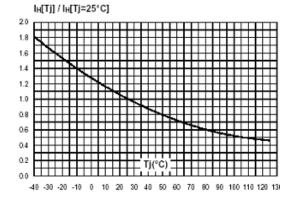


Fig.3:Relative variation of holding current versus junction temperature

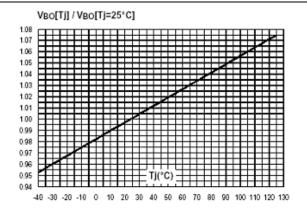


Fig.4:Relative variation of breakover voltage versus junction temperature

P1800SB

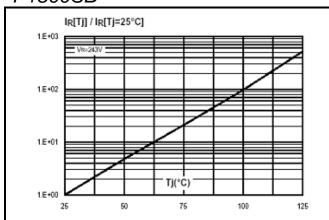


Fig.5:Relative variation of leakage current versus reverse voltage applied(typical values)

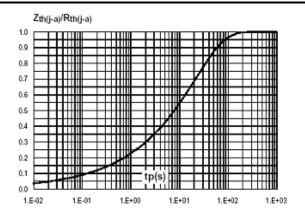
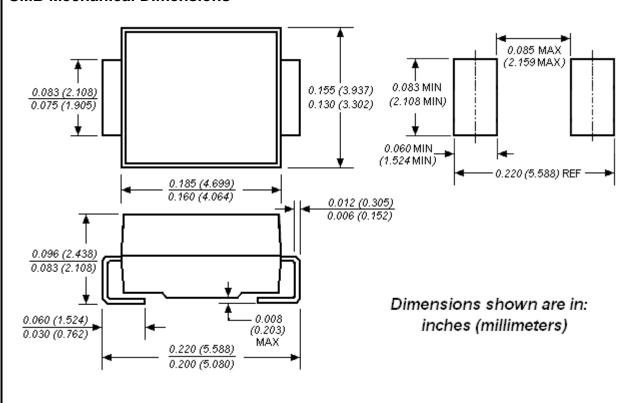


Fig.6:Variation of thermal impedance junction to ambient versus pulse duration(Printed circuit board FR4,Scu=35um,recommended pad layout)

SMB Mechanical Dimensions



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