

SE7401P**20V P-Channel Enhancement-Mode MOSFET**

Revision:B

General Description

The MOSFETs from SINO-IC provide the best combination of fast switching, low on-resistance and cost-effectiveness.

General Description

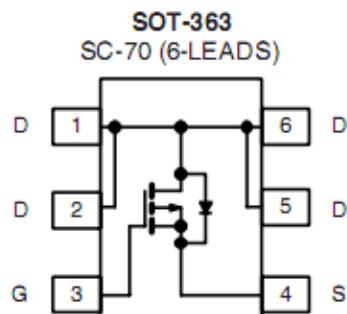
High Density Cell Design For Ultra Low On-Resistance Fully Characterized Avalanche Voltage and Current Improved Shoot-Through FOM

- Simple Drive Requirement
- Small Package Outline
- Surface Mount Device
- Pb-Free package is available

Features

For a single mosfet

- $V_{DS} = -20\text{ V}$
- $R_{DS(ON)} = 120\text{m}\Omega$ @ $V_{GS}=-4.50\text{V}$ @ $I_{DS}=-2.0\text{A}$
- $R_{DS(ON)} = 150\text{m}\Omega$ @ $V_{GS}=-2.50\text{V}$ @ $I_{DS}=-1.8\text{A}$
- $R_{DS(ON)} = 170\text{m}\Omega$ @ $V_{GS}=-1.80\text{V}$ @ $I_{DS}=-1.8\text{A}$

Pin configurations

Top View

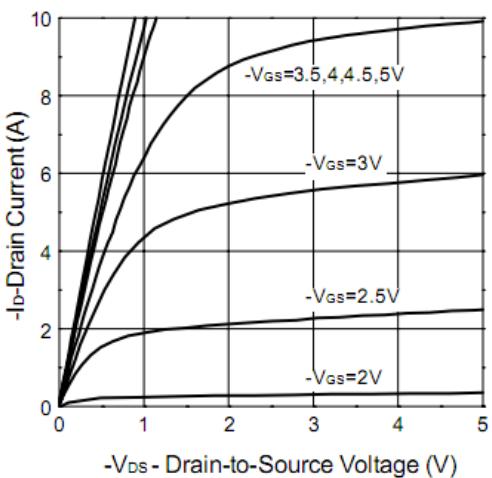
Absolute Maximum Ratings

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 8	V
Drain Current (Note 1)	I_D	-2.2	A
		-7	
Total Power Dissipation	P_D	0.9	W
		0.57	
Operating Junction Temperature Range	T_J	-55 to 150	°C

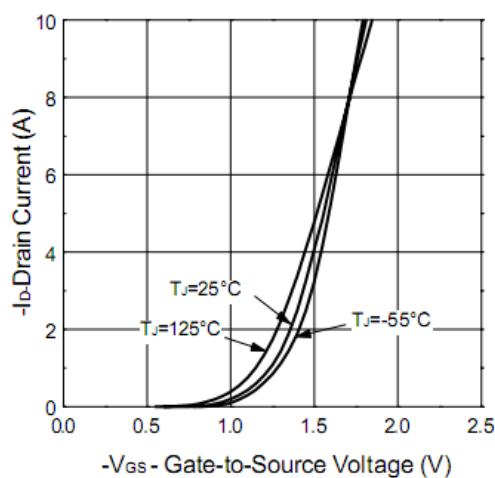
Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)						
Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
OFF CHARACTERISTICS (Note 2)						
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D=-250 \mu\text{A}$, $V_{GS}=0 \text{ V}$	-20			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=20 \text{ V}$, $V_{GS}=0 \text{ V}$			-1	μA
I_{CSS}	Gate-Body leakage current	$V_{DS}=0 \text{ V}$, $V_{GS}=\pm 10 \text{ V}$			± 0.1	μA
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ $I_D=-250 \mu\text{A}$	-0.4		-0.9	V
$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance ²	$V_{GS}=-4.5\text{V}$, $I_D=-2.0 \text{ A}$	-	100	120	$\text{m}\Omega$
		$V_{GS}=-2.5\text{V}$, $I_D=-1.8 \text{ A}$	-	120	150	
		$V_{GS}=-1.8\text{V}$, $I_D=-1.8 \text{ A}$		140	170	
g_{FS}	Forward Transconductance	$V_{DS}=5\text{V}$, $I_D=2\text{A}$		6.5		S
DYNAMIC PARAMETERS						
C_{iss}	Input Capacitance	$V_{GS}=0\text{V}$, $V_{DS}=10\text{V}$, $f=1\text{MHz}$		373		pF
C_{oss}	Output Capacitance			138		pF
C_{rss}	Reverse Transfer Capacitance			52		pF
SWITCHING PARAMETERS						
Q_g	Total Gate Charge ²	$V_{GS}=-4.5\text{V}$, $V_{DS}=-6\text{V}$, $I_D=-2.0\text{A}$		15.2		nC
Q_{gs}	Gate Source Charge			5.5		nC
Q_{gd}	Gate Drain Charge			2.7		nC
$t_{d(on)}$	Turn-On DelayTime ²	$V_{GS}=-4.5\text{V}$, $V_{DD}=-6\text{V}$, $R_L=6\Omega$, $R_G=6\Omega$ $I_D=-1\text{A}$			17.3	ns
$t_{d(off)}$	Turn-Off DelayTime				36.0	
$t_{d(r)}$	Turn-On Rise Time				3.7	
$t_{d(f)}$	Turn-Off Fall Time				3.2	

Typical Characteristics

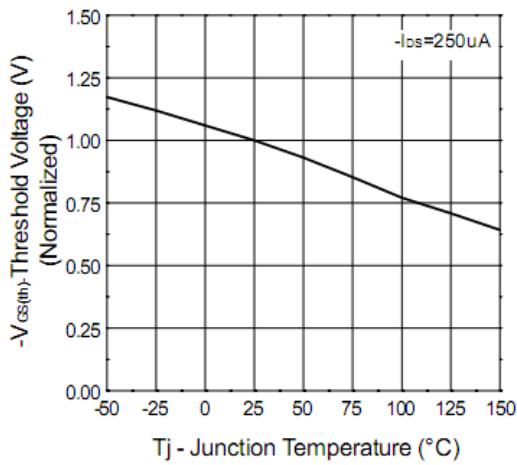
Output Characteristics



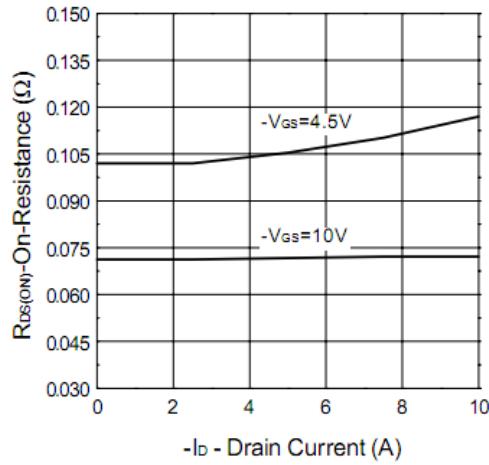
Transfer Characteristics



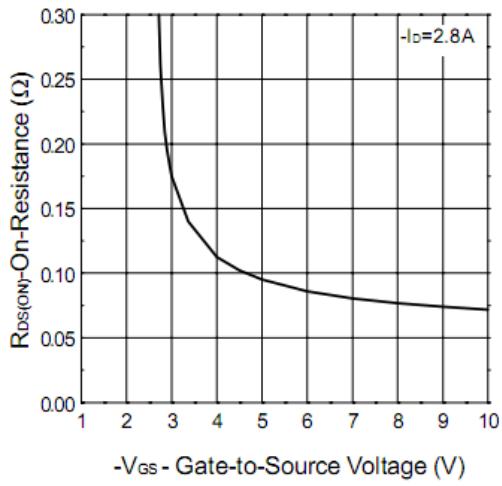
Threshold Voltage vs. Junction Temperature



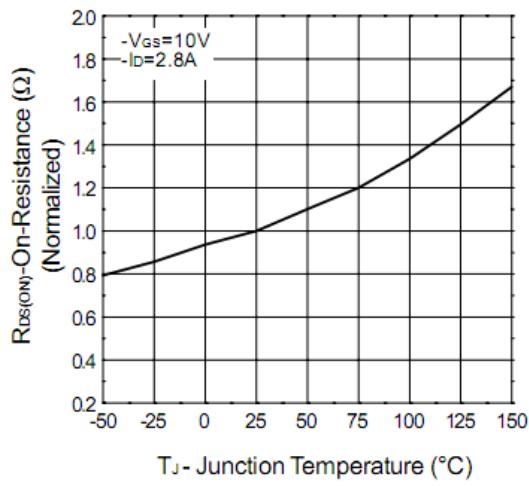
On-Resistance vs. Drain Current



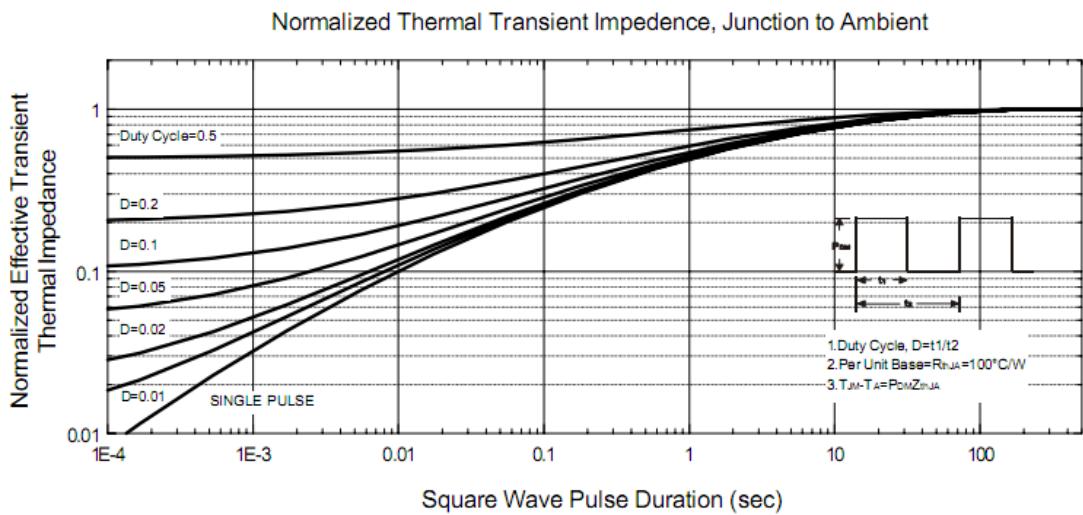
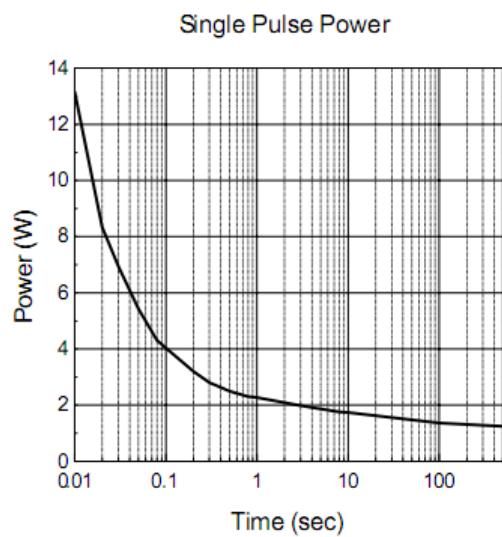
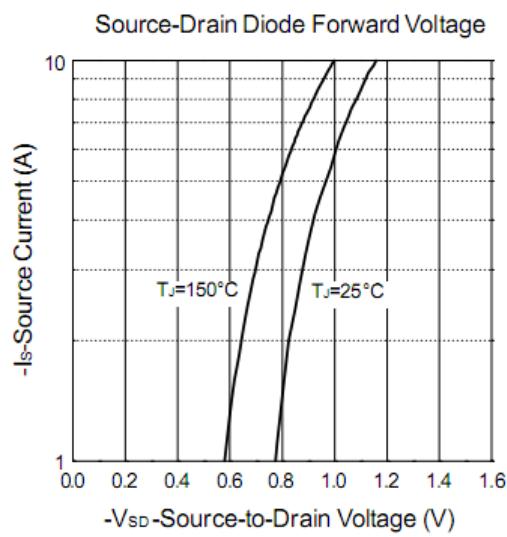
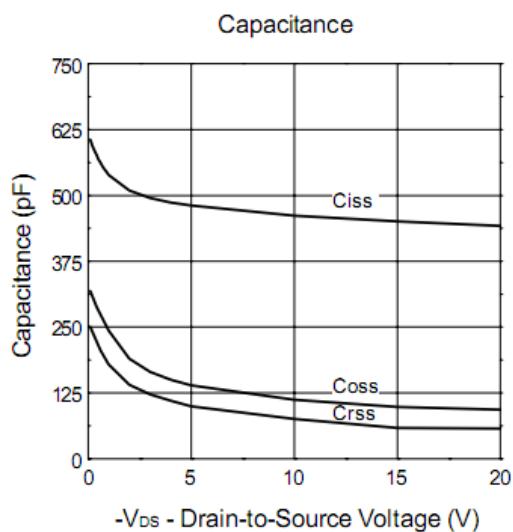
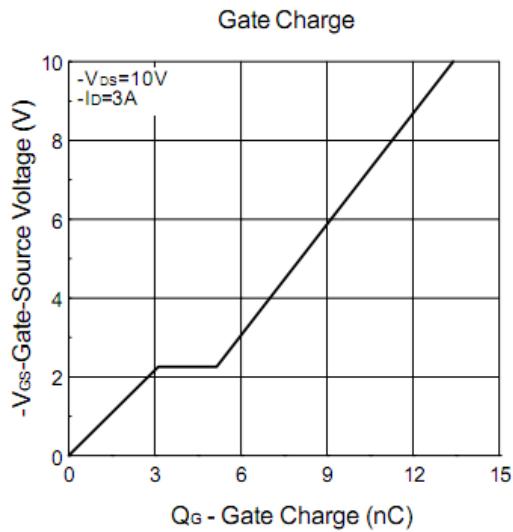
On-Resistance vs. Gate-to-Source Voltage



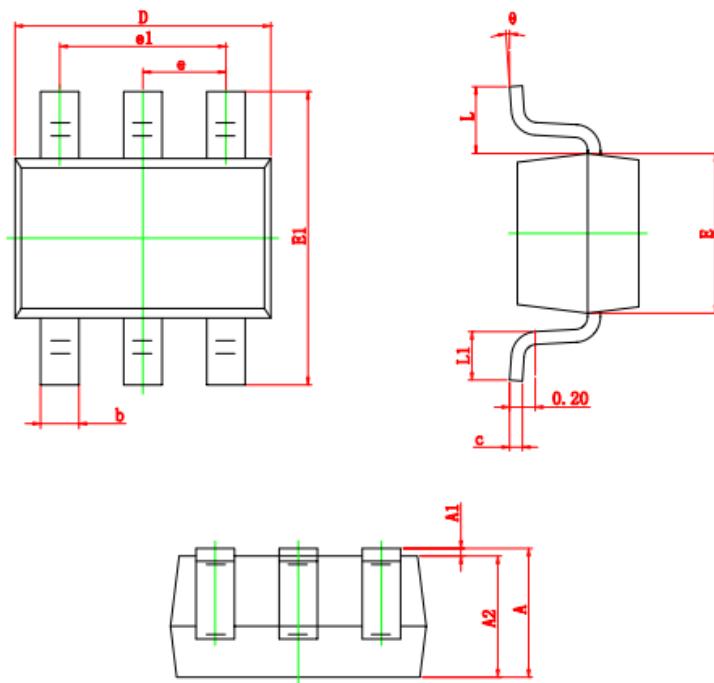
On-Resistance vs. Junction Temperature



Typical Characteristics



Packaging Information(SOT363)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
c	0.080	0.150	0.003	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
e	0.650 TYP		0.026 TYP	
e1	1.200	1.400	0.047	0.055
L	0.525 REF		0.021 REF	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°

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