

SE80100GA
N-Channel Enhancement-Mode MOSFET

Revision: A

General Description

Thigh Density Cell Design For Ultra Low On-Resistance Fully Characterized Avalanche Voltage and Current Improved Shoot-Through FOM

- Simple Drive Requirement
- Small Package Outline
- Surface Mount Device

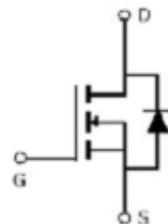
Features

For a single MOSFET

- $V_{DS} = 80V$
- $R_{DS(ON)} = 6.0m\Omega @ V_{GS}=10$

Pin configurations

See Diagram below



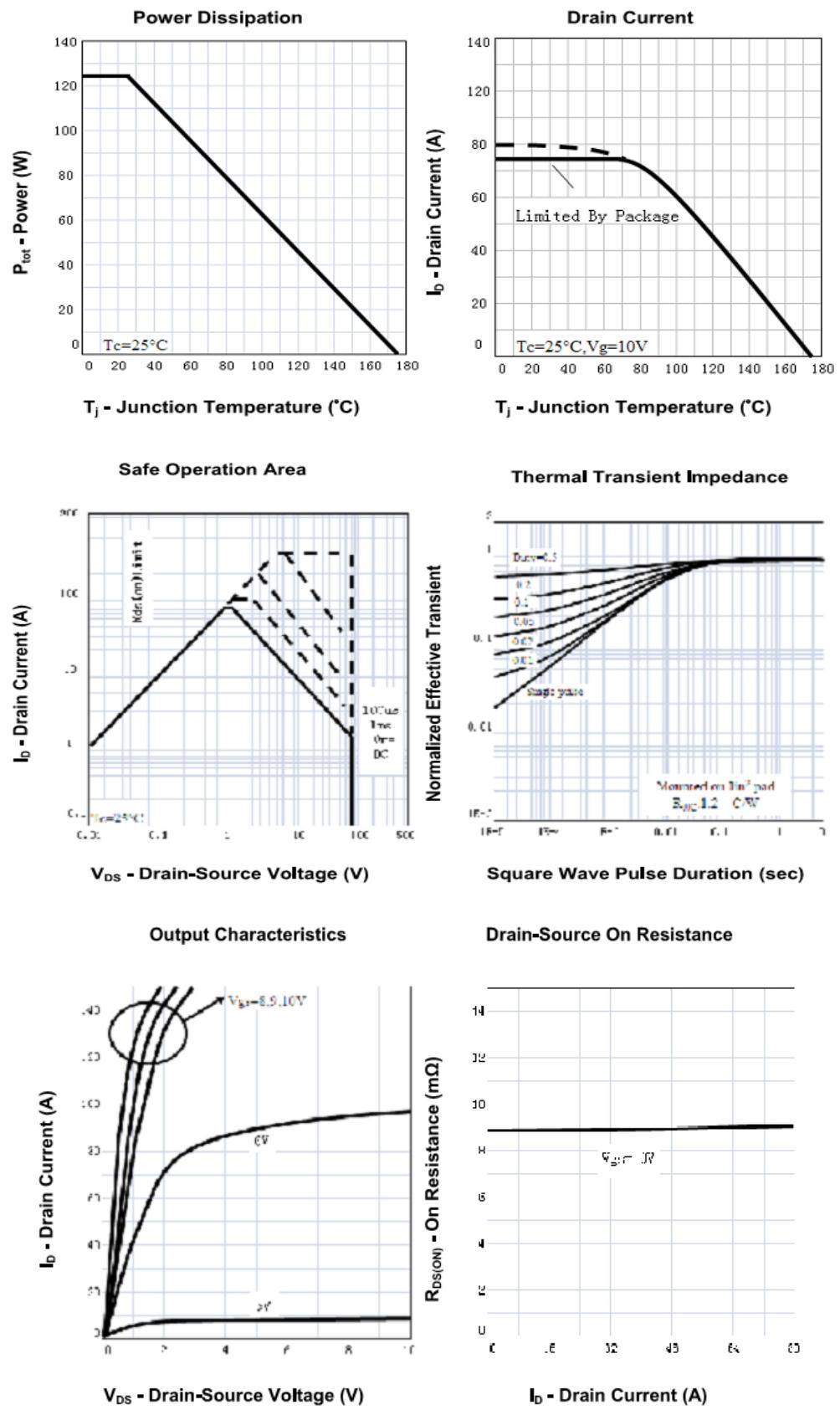
Absolute Maximum Ratings

| Parameter | Symbol | Rating | Units |
|--|------------|------------|-------|
| Drain-Source Voltage | V_{DS} | 80 | V |
| Gate-Source Voltage | V_{GS} | ± 20 | V |
| Drain Current | Continuous | I_D | A |
| Pulsed | | | |
| Total Power Dissipation @ $T_A=25^\circ C$ | P_D | 125 | W |
| Operating Junction Temperature Range | T_J | -55 to 175 | °C |

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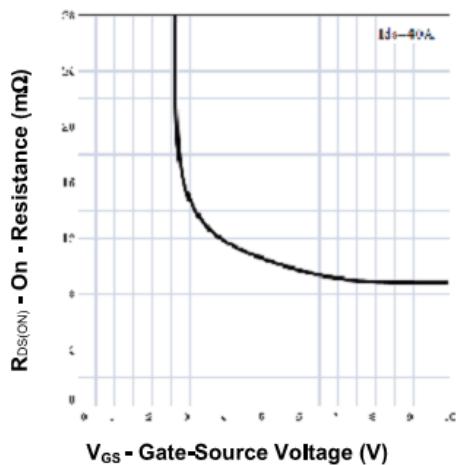
| Electrical Characteristics (TJ=25°C unless otherwise noted) | | | | | | |
|---|--|---|-----|------|-------|-------|
| Symbol | Parameter | Test Conditions | Min | Typ | Max | Units |
| OFF CHARACTERISTICS (Note 2) | | | | | | |
| BV _{DSS} | Drain-Source Breakdown Voltage | I _D =250μA, V _{GS} =0 V | 80 | | | V |
| I _{DSS} | Drain to Source Leakage Current | V _{DS} = 64V, V _{GS} =0V | | | 1 | μA |
| I _{GSS} | Gate-Body Leakage Current | V _{GS} =20V | | | 100 | nA |
| V _{GS(th)} | Gate Threshold Voltage | V _{DS} = V _{GS} , I _D =250μA | 1.5 | | 2.5 | V |
| R _{DS(ON)} | Static Drain-Source On-Resistance ² | V _{GS} =10V, I _D =40A | - | 6.0 | | mΩ |
| DYNAMIC PARAMETERS | | | | | | |
| C _{iss} | Input Capacitance | V _{GS} =0V, V _{DS} =40V, f=1MHz | | 4120 | | pF |
| C _{oss} | Output Capacitance | | | 520 | | pF |
| C _{rss} | Reverse Transfer Capacitance | | | 200 | | pF |
| SWITCHING PARAMETERS | | | | | | |
| Q _g | Total Gate Charge | V _{GS} =10V, V _{DS} =64V, I _D =40A | | 58 | | nC |
| Q _{gs} | Gate Source Charge | | | 15 | | nC |
| Q _{gd} | Gate Drain Charge | | | 19 | | nC |
| t _{d(on)} | Turn-On Delay Time | V _{GS} =10V, V _{DS} =40V, R _{GEN} =4.7Ω I _D =2A | | 34 | | ns |
| t _{d(off)} | Turn-Off Delay Time | | | 103 | | ns |
| t _{d(r)} | Turn-On Rise Time | | | 95 | | ns |
| t _{d(f)} | Turn-Off Fall Time | | | 33 | | ns |
| Thermal Resistance | | | | | | |
| Symbol | Parameter | | Typ | Max | Units | |
| R _{θJC} | Thermal Resistance Junction to Case(t≤10s) | | - | 1.2 | | °C/W |

Typical Characteristics

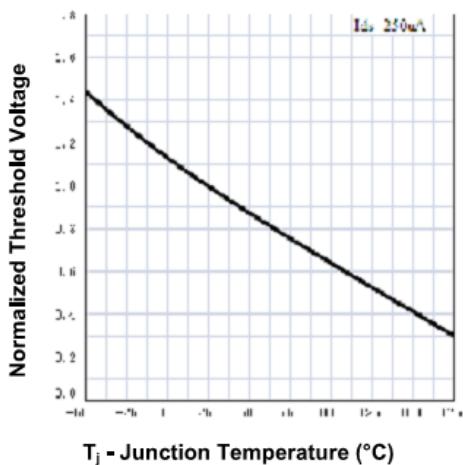


Typical Characteristics

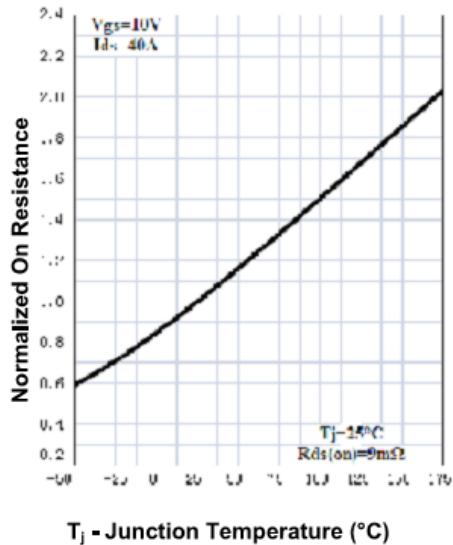
Drain-Source On Resistance



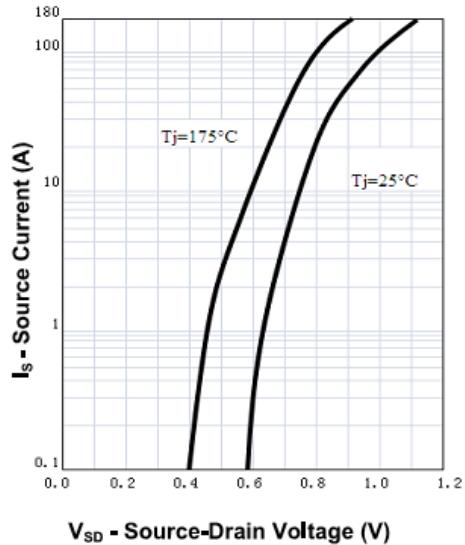
Gate Threshold Voltage



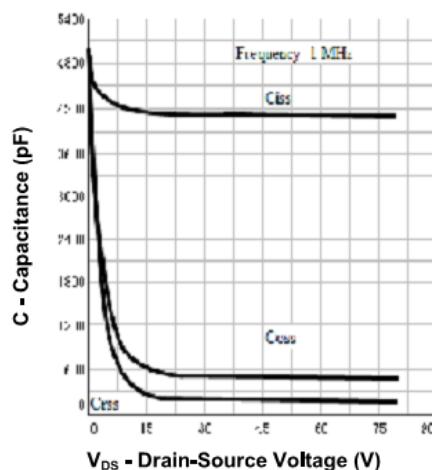
Drain-Source On Resistance



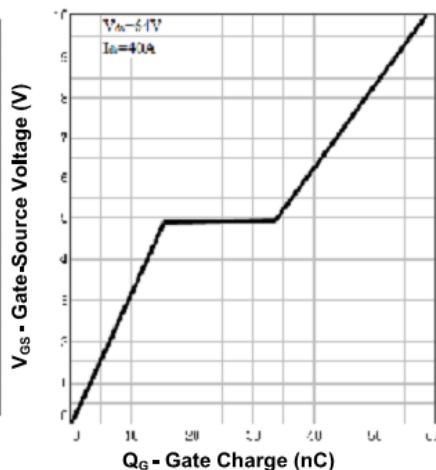
Source-Drain Diode Forward



Capacitance



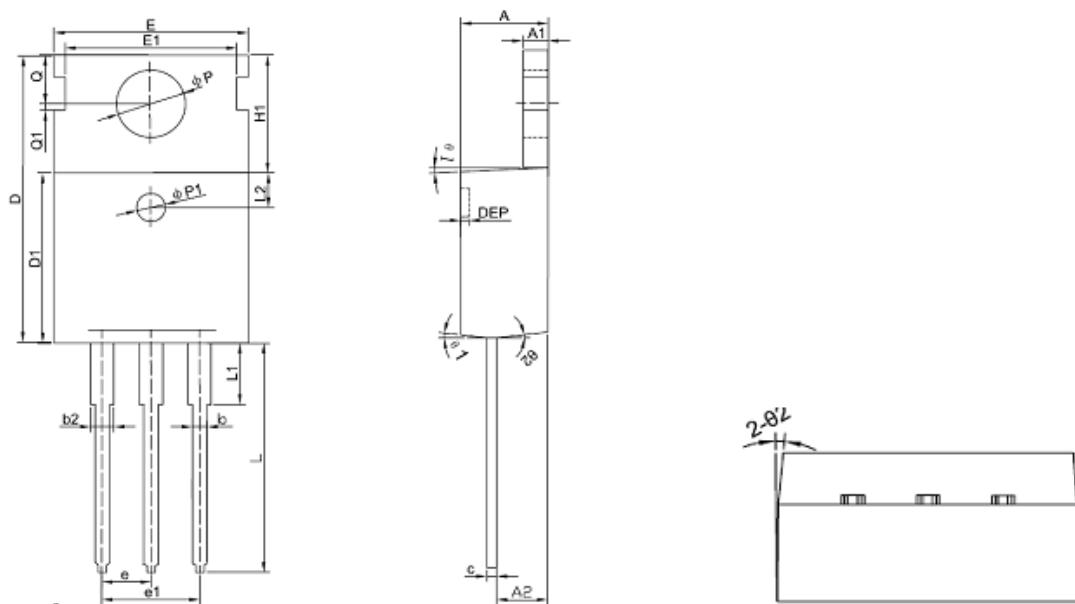
Gate Charge



SE80100GA

Package Outline Dimension

TO-220



| Symbol | Dimension In Millimeters | | | Dimension In Inches | | |
|--------|--------------------------|--------|--------|---------------------|-------|-------|
| | Min | Nom | Max | Min | Nom | Max |
| A | 4.400 | 4.550 | 4.700 | 0.173 | 0.179 | 0.185 |
| A1 | 1.270 | 1.300 | 1.330 | 0.050 | 0.051 | 0.052 |
| A2 | 2.590 | 2.690 | 2.790 | 0.102 | 0.106 | 0.110 |
| b | 0.770 | - | 0.900 | 0.030 | - | 0.035 |
| b2 | 1.230 | - | 1.360 | 0.048 | - | 0.054 |
| c | 0.480 | 0.500 | 0.520 | 0.019 | 0.020 | 0.020 |
| D | 15.100 | 15.400 | 15.700 | - | 0.606 | - |
| D1 | 9.000 | 9.100 | 9.200 | 0.354 | 0.358 | 0.362 |
| DEP | 0.050 | 0.285 | 0.520 | 0.002 | 0.011 | 0.020 |
| E | 10.060 | 10.160 | 10.260 | 0.396 | 0.400 | 0.404 |
| E1 | - | 8.700 | - | - | 0.343 | - |
| ΦP1 | 1.400 | 1.500 | 1.600 | 0.055 | 0.059 | 0.063 |
| e | 2.54BSC | | | 0.1BSC | | |
| e1 | 5.08BSC | | | 0.2BSC | | |
| H1 | 6.100 | 6.300 | 6.500 | 0.240 | 0.248 | 0.256 |
| L | 12.750 | 12.960 | 13.170 | 0.502 | 0.510 | 0.519 |
| L1 | - | - | 3.950 | - | - | 0.156 |
| L2 | 1.85REF | | | 0.073REF | | |
| ΦP | 3.570 | 3.600 | 3.630 | 0.141 | 0.142 | 0.143 |
| Q | 2.730 | 2.800 | 2.870 | 0.107 | 0.110 | 0.113 |
| Q1 | - | 0.200 | - | - | 0.008 | - |
| Θ1 | 5° | 7° | 9° | 5° | 7° | 9° |
| Θ2 | 1° | 3° | 5° | 1° | 3° | 5° |

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SHANGHAI SINO-IC MICROELECTRONICS CO., LTD

Add: Building 3, Room 3401-03, No.200 Zhangheng Road, ZhangJiang Hi-Tech Park, Pudong, Shanghai 201203, China

Phone: +86-21-33932402 33932403 33932405 33933508 33933608

Fax: +86-21-33932401

Email: webmaster@sino-ic.net

Website: <http://www.sino-ic.net>

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