

Dual Enhancement Mode MOSFET (N- and P-Channel)

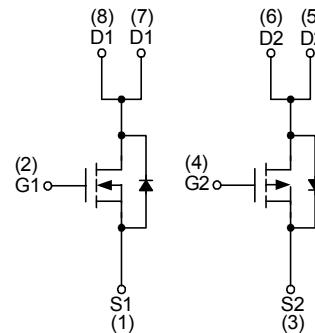
Features

- N-Channel
20V/9.5A,
 $R_{DS(ON)} = 14\text{m}\Omega(\text{max.}) @ V_{GS} = 4.5\text{V}$
 $R_{DS(ON)} = 18\text{m}\Omega(\text{max.}) @ V_{GS} = 2.5\text{V}$
- P-Channel
-20V/-6A,
 $R_{DS(ON)} = 45\text{m}\Omega(\text{max.}) @ V_{GS} = -4.5\text{V}$
 $R_{DS(ON)} = 65\text{m}\Omega(\text{max.}) @ V_{GS} = -2.5\text{V}$
- 100% UIS + R_g Tested
- Reliable and Rugged
- Lead Free and Green Devices Available
(RoHS Compliant)

Pin Description



Top View of SOP-8

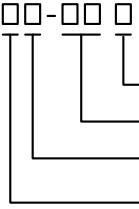


N-Channel MOSFET P-Channel MOSFET

Applications

- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems.

Ordering and Marking Information

SM2001CS  	Package Code K : SOP-8 Operating Junction Temperature Range C : -55 to 150 °C Handling Code TR : Tape & Reel Assembly Material G : Halogen and Lead Free Device
SM2001CS K : 	XXXXX - Lot Code

Note : SINOPOWER lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. SINOPOWER lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020D for MSL classification at lead-free peak reflow temperature. SINOPOWER defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

SINOPOWER reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Channel 1	Channel 2	Unit
Common Ratings ($T_A=25^\circ\text{C}$ Unless Otherwise Noted)				
V_{DSS}	Drain-Source Voltage	20	-20	V
V_{GSS}	Gate-Source Voltage	± 12	± 12	
T_J	Maximum Junction Temperature	150		$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150		
I_S	Diode Continuous Forward Current	$T_A=25^\circ\text{C}$	1.8	-1.2
I_D	Continuous Drain Current	$T_A=25^\circ\text{C}$	9.5	-6
I_{DM}^a	Pulsed Drain Current	$T_A=25^\circ\text{C}$	30	-20
P_D	Power Dissipation	$T_A=25^\circ\text{C}$	2	2
		$T_A=70^\circ\text{C}$	1.3	1.3
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient	$t \leq 10\text{s}$	45	45
		Steady State	62.5	62.5
$R_{\theta JL}$	Thermal Resistance-Junction to Lead	Steady State	20	20
I_{AS}^b	Avalanche Current, Single pulse ($L=0.1\text{mH}$)	16		A
E_{AS}^b	Avalanche Energy, Single pulse ($L=0.1\text{mH}$)	12	11	mJ

Note a : Pulse width limited by max. junction temperature.

Note b : UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature $T_j=25^\circ\text{C}$).

Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

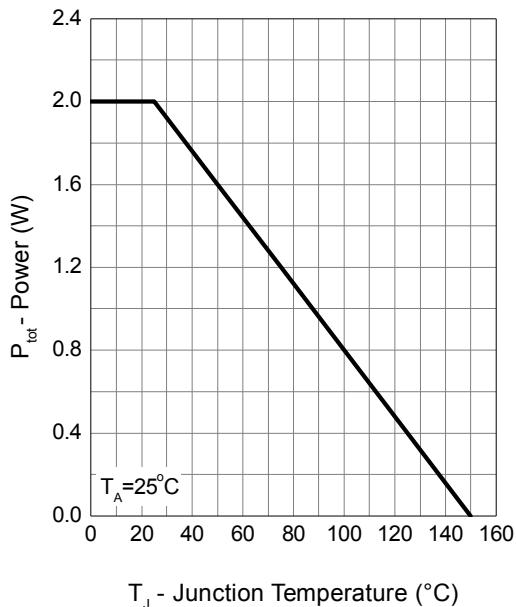
Symbol	Parameter	Test Conditions	Channel 1			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{DS}}=250\mu\text{A}$	20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=16\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	μA
		$T_J=85^\circ\text{C}$	-	-	30	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{DS}}=250\mu\text{A}$	0.5	0.7	1.0	V
I_{GSS}	Gate Leakage Current	$V_{\text{GS}}=\pm 12\text{V}, V_{\text{DS}}=0\text{V}$	-	-	± 100	nA
$R_{\text{DS(ON)}}$	Drain-Source On-state Resistance	$V_{\text{GS}}=4.5\text{V}, I_{\text{DS}}=9\text{A}$	-	12	14	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V}, I_{\text{DS}}=6\text{A}$	-	13.5	18	
Diode Characteristics						
V_{SD}	Diode Forward Voltage	$I_{\text{SD}}=1.5\text{A}, V_{\text{GS}}=0\text{V}$	-	0.67	1.1	V
t_{rr}	Reverse Recovery Time	$I_{\text{DS}}=6\text{A}, dI_{\text{SD}}/dt=100\text{A}/\mu\text{s}$	-	11.8	-	ns
Q_{rr}	Reverse Recovery Charge		-	3.6	-	nC
Dynamic Characteristics						
R_{G}	Gate Resistance	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=0\text{V}, F=1\text{MHz}$	-	0.7	-	Ω
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=10\text{V},$ $\text{Frequency}=1.0\text{MHz}$	-	795	-	pF
C_{oss}	Output Capacitance		-	160	-	
C_{rss}	Reverse Transfer Capacitance		-	118	-	
$t_{\text{d(ON)}}$	Turn-on Delay Time	$V_{\text{DD}}=10\text{V}, R_{\text{L}}=10\Omega,$ $I_{\text{DS}}=1\text{A}, V_{\text{GEN}}=4.5\text{V},$ $R_{\text{G}}=1\Omega$	-	10.2	19	ns
t_{r}	Turn-on Rise Time		-	16.1	30	
$t_{\text{d(OFF)}}$	Turn-off Delay Time		-	16.6	31	
t_{f}	Turn-off Fall Time		-	3.6	7	
Gate Charge Characteristics						
Q_{g}	Total Gate Charge	$V_{\text{DS}}=10\text{V}, V_{\text{GS}}=4.5\text{V},$ $I_{\text{DS}}=6\text{A}$	-	8.7	-	nC
Q_{gs}	Gate-Source Charge		-	1.25	-	
Q_{gd}	Gate-Drain Charge		-	3	-	
Q_{gth}	Threshold Gate Charge		-	0.54	-	

Electrical Characteristics (Cont.) ($T_A = 25^\circ\text{C}$ unless otherwise noted)

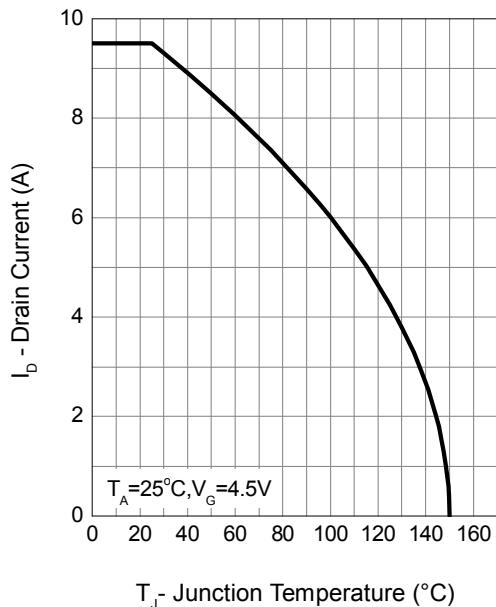
Symbol	Parameter	Test Conditions	Channel 2			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{DS}}=-250\mu\text{A}$	-20	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=-16\text{V}, V_{\text{GS}}=0\text{V}$ $T_J=85^\circ\text{C}$	-	-	-1	μA
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{DS}}=-250\mu\text{A}$	-0.5	-0.7	-1	V
I_{GSS}	Gate Leakage Current	$V_{\text{GS}}=\pm 12\text{V}, V_{\text{DS}}=0\text{V}$	-	-	± 100	nA
$R_{\text{DS(ON)}}$	Drain-Source On-state Resistance	$V_{\text{GS}}=-4.5\text{V}, I_{\text{DS}}=-6\text{A}$	-	36	45	$\text{m}\Omega$
		$V_{\text{GS}}=-2.5\text{V}, I_{\text{DS}}=-4\text{A}$	-	50	65	
Diode Characteristics						
V_{SD}	Diode Forward Voltage	$I_{\text{SD}}=-1.2\text{A}, V_{\text{GS}}=0\text{V}$	-	-0.7	-1	V
t_{rr}	Reverse Recovery Time	$I_{\text{sd}}=-6\text{A}, dI_{\text{SD}}/dt=100\text{A}/\mu\text{s}$	-	12	-	ns
Q_{rr}	Reverse Recovery Charge		-	4.5	-	nC
Dynamic Characteristics						
R_{G}	Gate Resistance	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=0\text{V}, F=1\text{MHz}$	-	3.5	-	Ω
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V},$ $V_{\text{DS}}=-10\text{V},$ Frequency=1.0MHz	-	590	-	pF
C_{oss}	Output Capacitance		-	122	-	
C_{rss}	Reverse Transfer Capacitance		-	92	-	
$t_{\text{d(ON)}}$	Turn-on Delay Time	$V_{\text{DD}}=-10\text{V}, R_{\text{L}}=10\Omega,$ $I_{\text{DS}}=-1\text{A}, V_{\text{GEN}}=-4.5\text{V},$ $R_{\text{G}}=1\Omega$	-	8.4	16	ns
t_{r}	Turn-on Rise Time		-	14.1	26	
$t_{\text{d(OFF)}}$	Turn-off Delay Time		-	16	30	
t_{f}	Turn-off Fall Time		-	10.7	20	
Gate Charge Characteristics						
Q_{g}	Total Gate Charge	$V_{\text{DS}}=-10\text{V}, V_{\text{GS}}=-4.5\text{V},$ $I_{\text{DS}}=-6\text{A}$	-	7.68	-	nC
Q_{gs}	Gate-Source Charge		-	0.66	-	
Q_{gd}	Gate-Drain Charge		-	2.72	-	
Q_{gth}	Threshold Gate Charge		-	0.3	-	

N Channel Typical Operating Characteristics

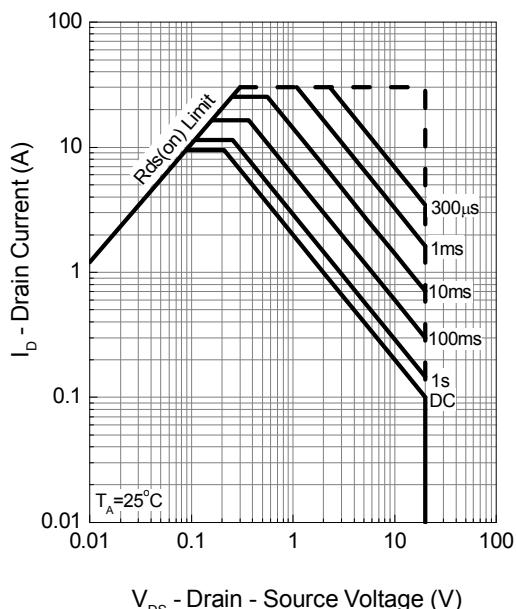
Power Dissipation



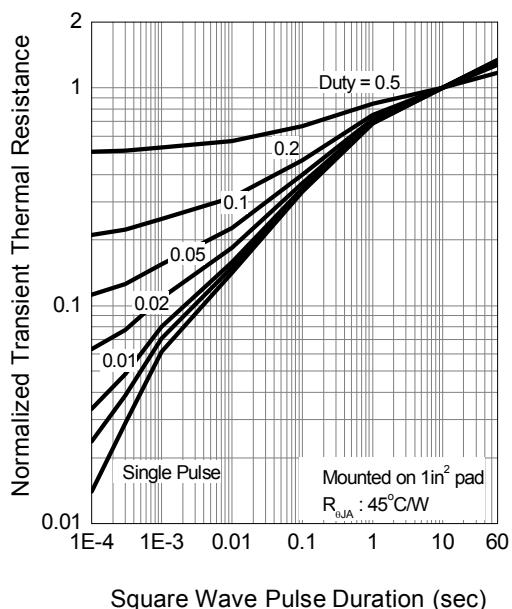
Drain Current



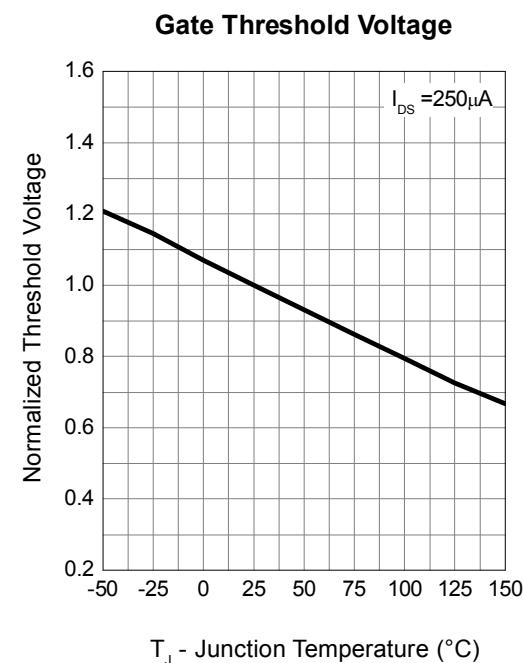
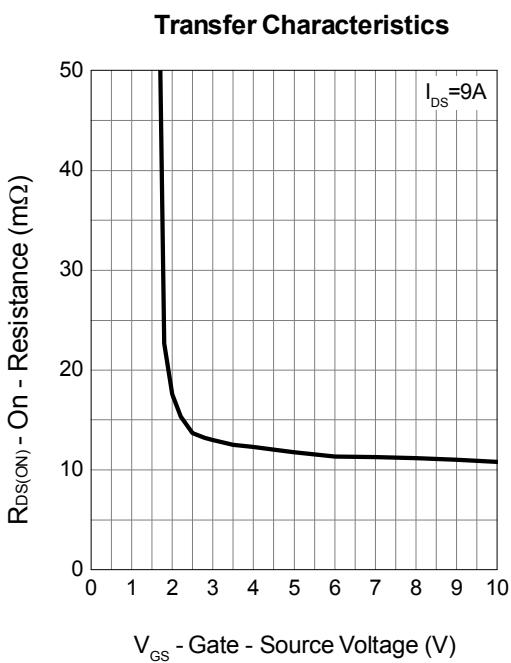
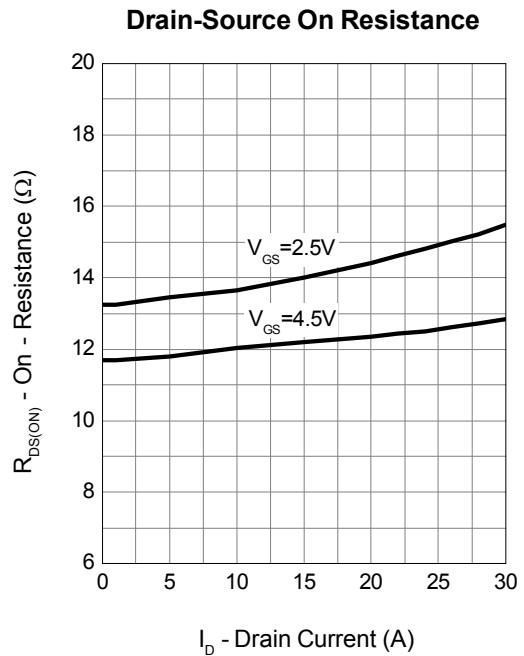
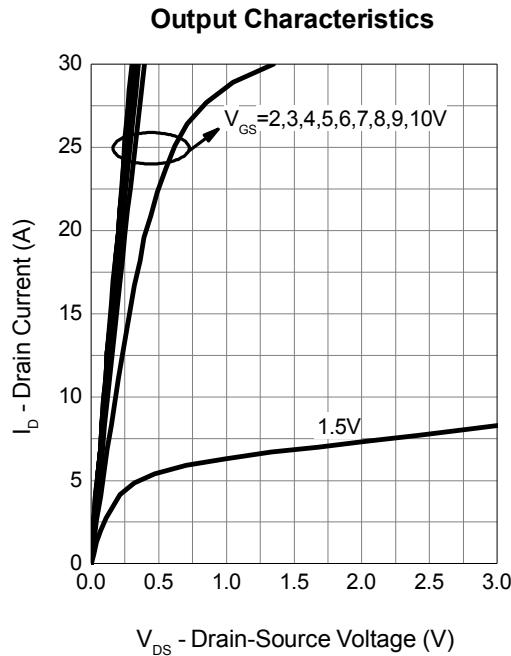
Safe Operation Area



Thermal Transient Impedance

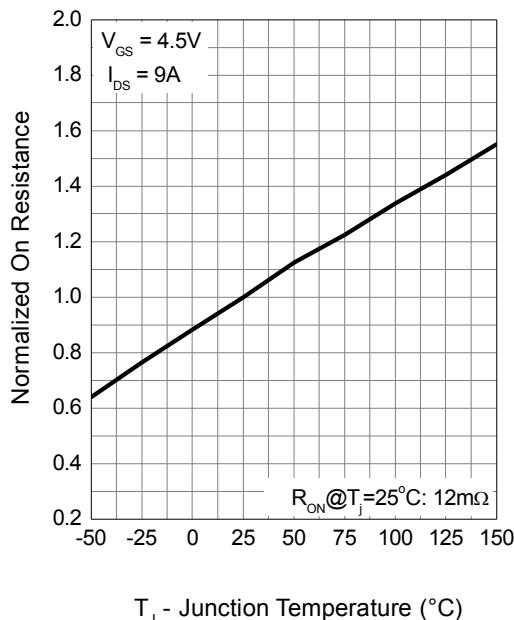


N Channel Typical Operating Characteristics (Cont.)

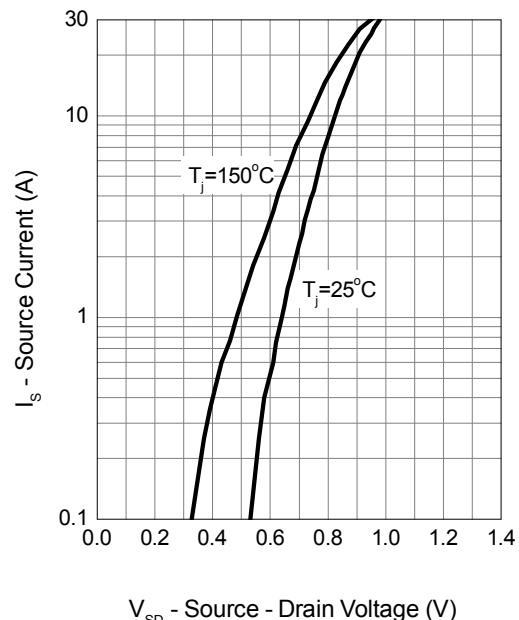


N Channel Typical Operating Characteristics (Cont.)

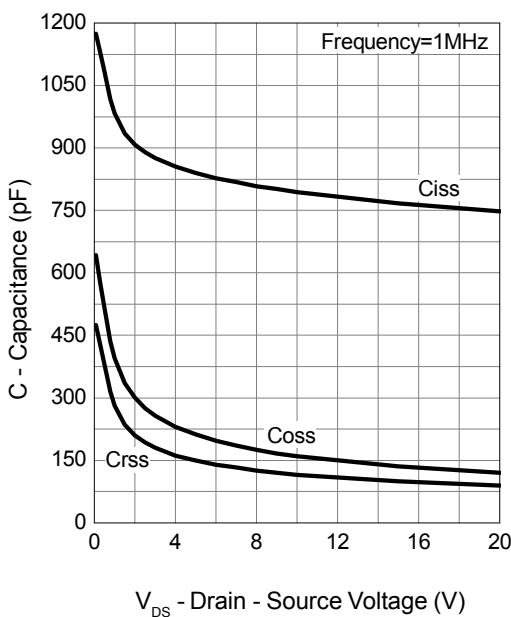
Drain-Source On Resistance



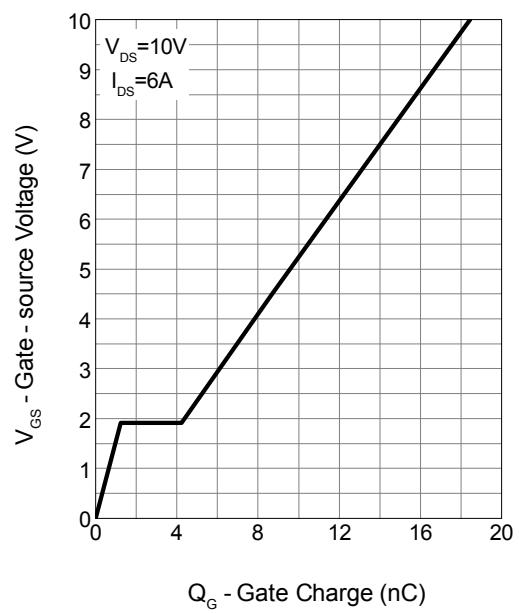
Source-Drain Diode Forward



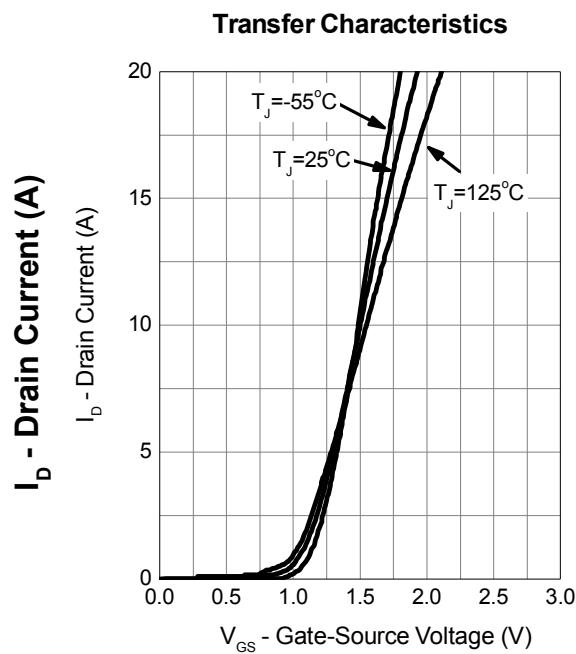
Capacitance



Gate Charge

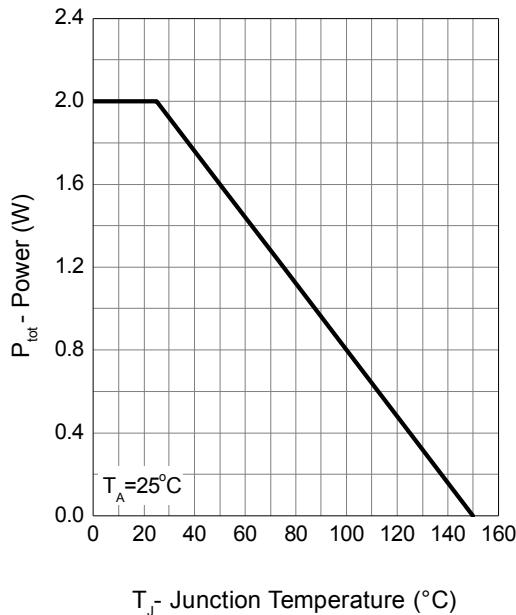


N Channel Typical Operating Characteristics (Cont.)

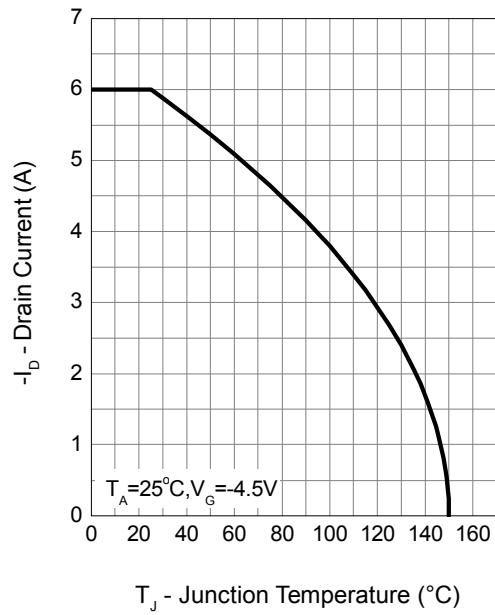


P Channel Typical Operating Characteristics

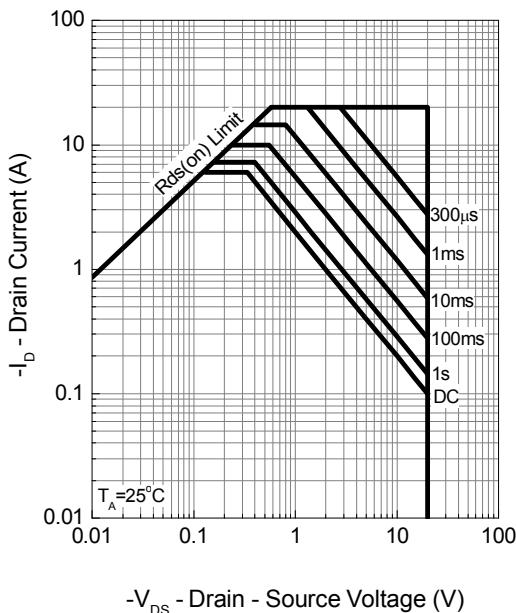
Power Dissipation



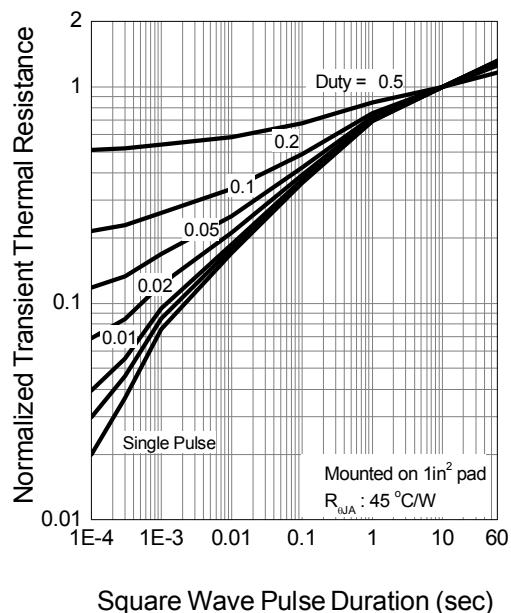
Drain Current



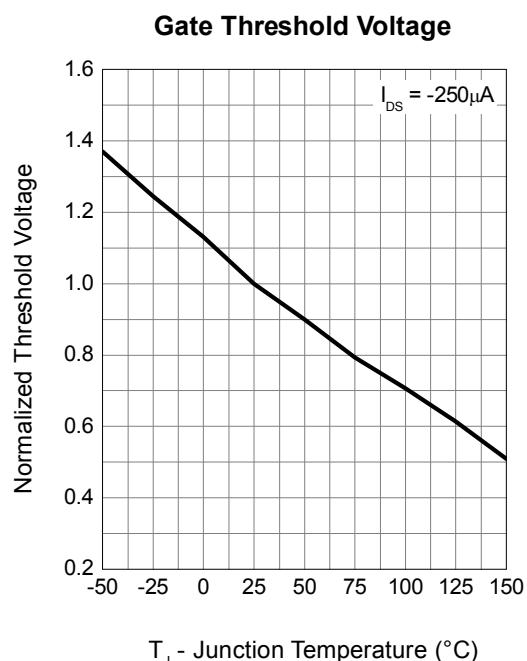
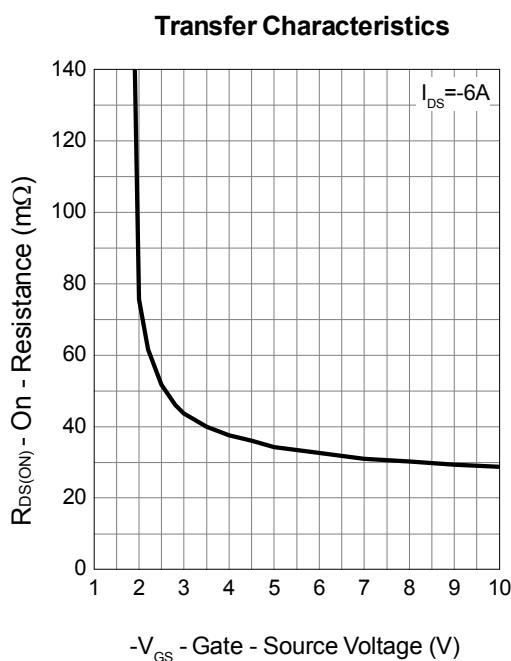
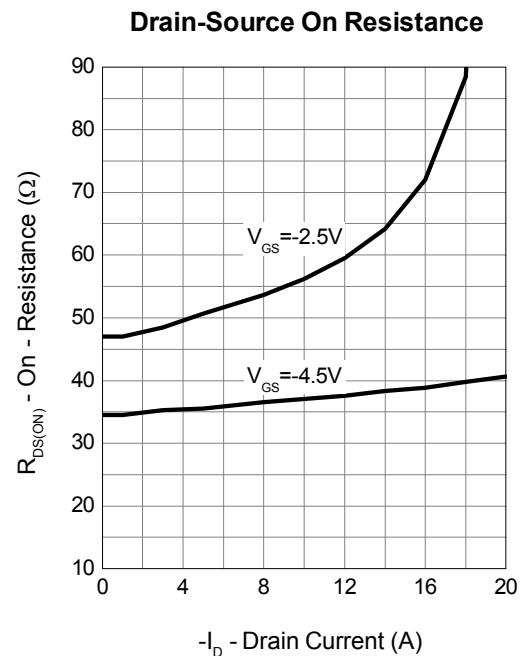
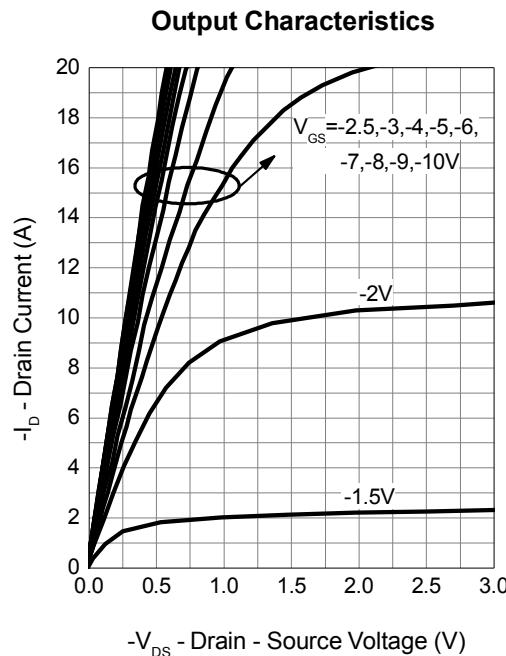
Safe Operation Area



Thermal Transient Impedance

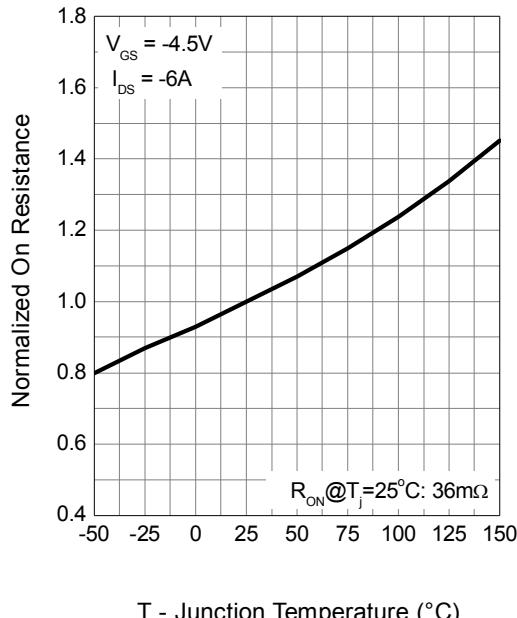


P Channel Typical Operating Characteristics (Cont.)

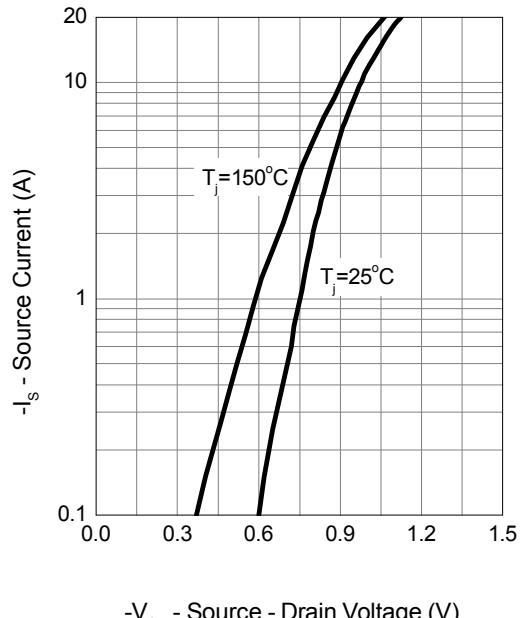


P Channel Typical Operating Characteristics (Cont.)

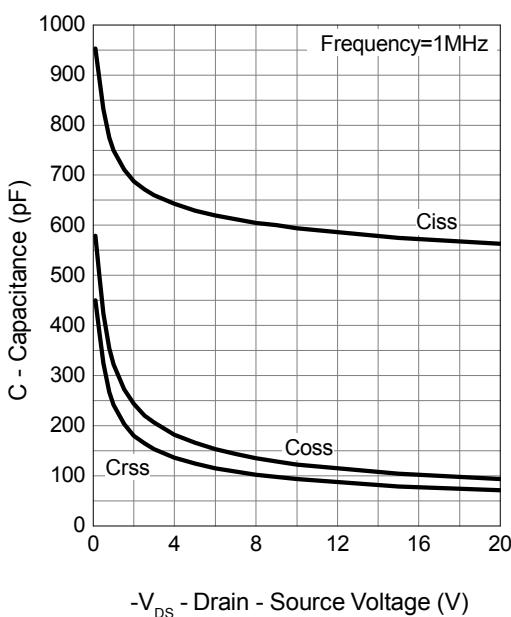
Drain-Source On Resistance



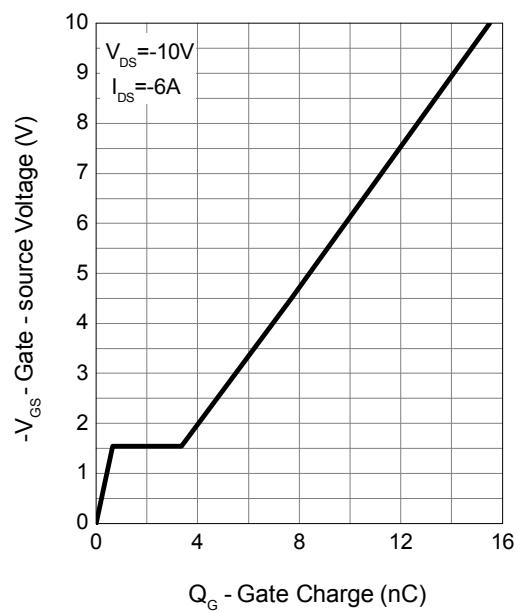
Source-Drain Diode Forward

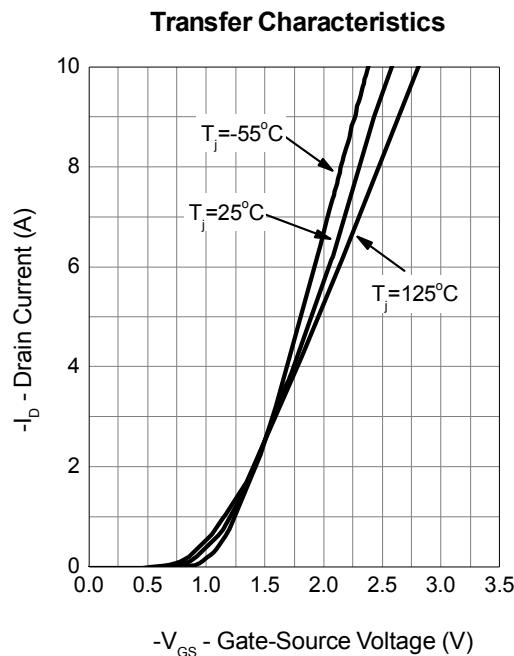


Capacitance



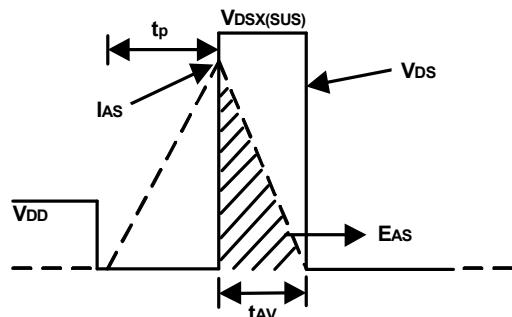
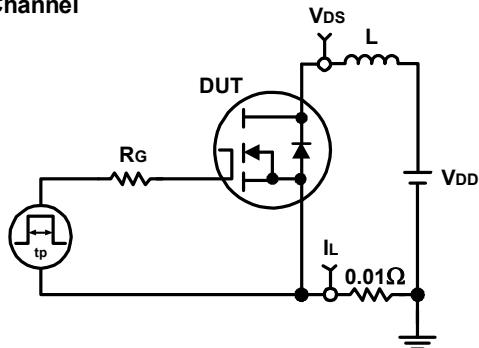
Gate Charge



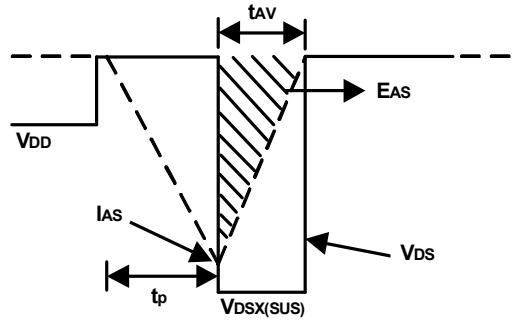
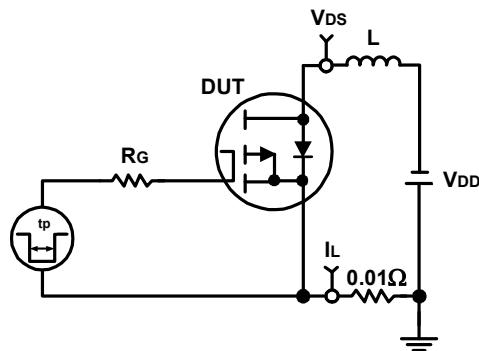
P Channel Typical Operating Characteristics (Cont.)

Avalanche Test Circuit and Waveforms

N Channel

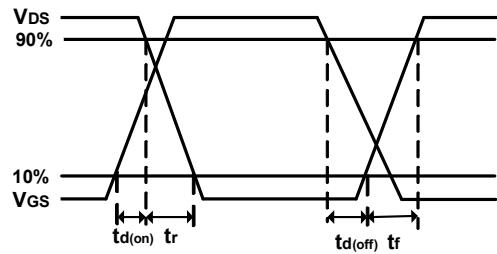
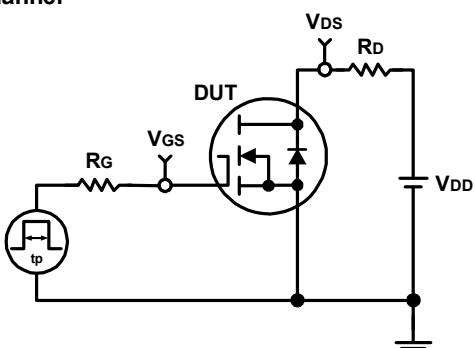


P Channel

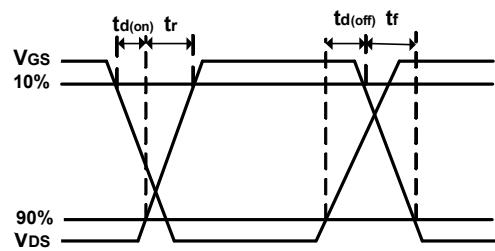
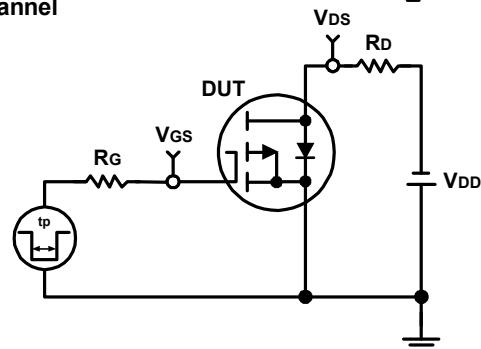


Switching Time Test Circuit and Waveforms

N Channel



P Channel



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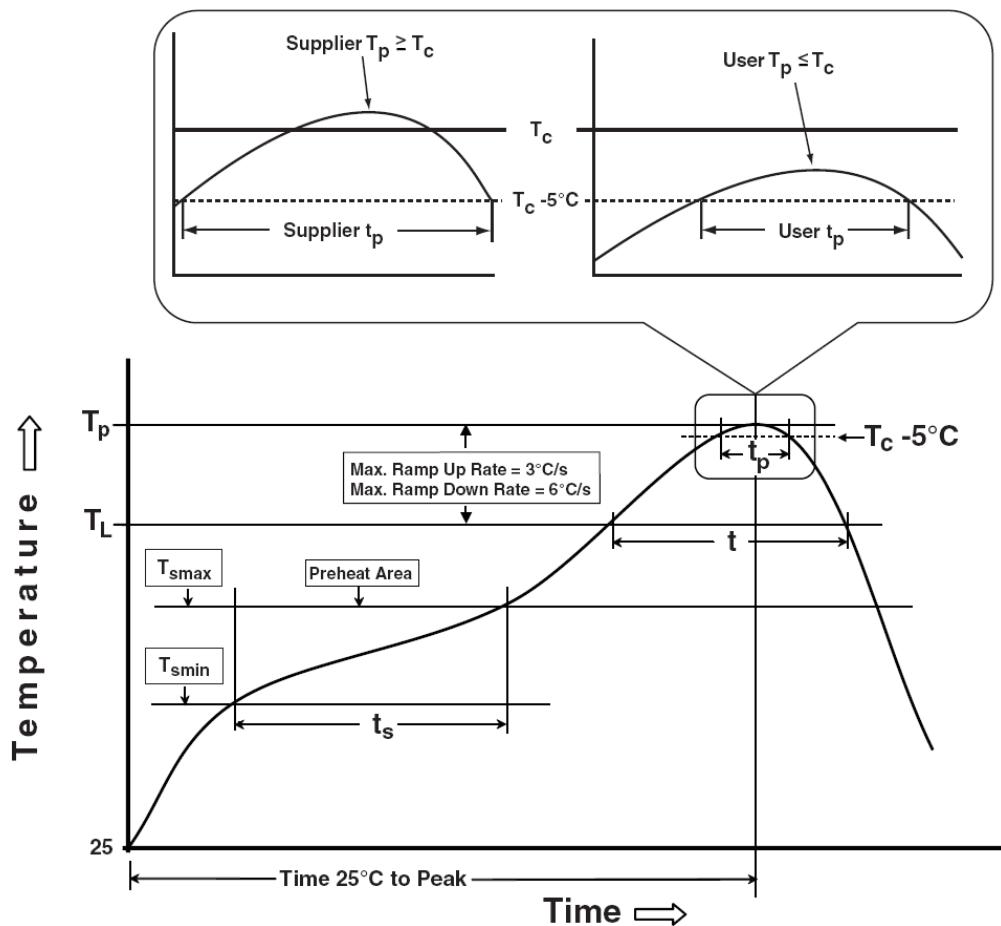
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Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak		
Temperature min (T_{smin})	100 °C	150 °C
Temperature max (T_{smax})	150 °C	200 °C
Time (T_{smin} to T_{smax}) (t_s)	60-120 seconds	60-120 seconds
Average ramp-up rate (T_{smax} to T_p)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L)	183 °C	217 °C
Time at liquidous (t_L)	60-150 seconds	60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_p)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.

* Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum.
 ** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.

Table 1. SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm ³ ≤350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (Tc)

Package Thickness	Volume mm ³ ≤350	Volume mm ³ 350-2000	Volume mm ³ ≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	1000 Hrs, 80% of VDS max @ Tjmax
HTGB	JESD-22, A108	1000 Hrs, 100% of VGS max @ Tjmax
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -65°C~150°C

Customer Service

Sinopower Semiconductor, Inc.

5F, No. 6, Dusing 1St Rd., Hsinchu Science Park,
 Hsinchu, 30078, Taiwan
 TEL: 886-3-5635818 Fax: 886-3-5635080

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[DMN2080UCB4-7](#) [DMN61D9UWQ-13](#) [US6M2GTR](#) [DMN31D5UDJ-7](#) [SSM6P54TU,LF](#) [DMP22D4UFO-7B](#) [IPS60R3K4CEAKMA1](#)
[DMN1006UCA6-7](#) [DMN16M9UCA6-7](#) [STF5N65M6](#) [IRF40H233XTMA1](#) [IPSA70R950CEAKMA1](#) [IPSA70R2K0CEAKMA1](#) [STU5N65M6](#)
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