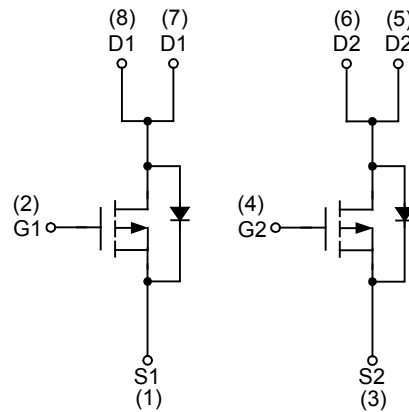
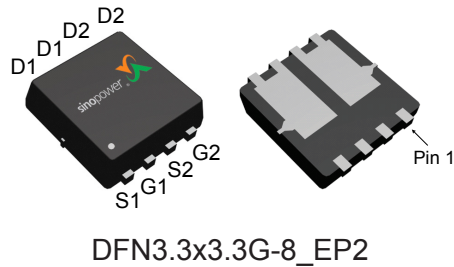


## Dual P-Channel Enhancement Mode MOSFET

### Features

- 20V/-19A\*
  - $R_{DS(ON)}=29m\Omega(max.)@V_{GS}=-4.5V$
  - $R_{DS(ON)}=40m\Omega(max.)@V_{GS}=-2.5V$
  - $R_{DS(ON)}=60m\Omega(max.)@V_{GS}=-1.8V$
- 100% UIS +  $R_g$  Tested
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)

### Pin Description



P-Channel MOSFET

### Applications

- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems.

### Ordering and Marking Information

<p>SM3429BS <span style="font-family: monospace;">□□□-□□□</span></p> <div style="margin-left: 40px;"> <p>└─ Assembly Material</p> <p>└─ Handling Code</p> <p>└─ Temperature Range</p> <p>└─ Package Code</p> </div>	<p>Package Code QA : DFN3.3x3.3G-8_EP2</p> <p>Operating Junction Temperature Range C : -55 to 150 °C</p> <p>Handling Code TR : Tape &amp; Reel</p> <p>Assembly Material G : Halogen and Lead Free Device</p>
<p>SM3429BS QA :</p> <div style="border: 1px solid black; padding: 5px; display: inline-block; margin-left: 20px;"> <p>SM 3429B XXXXX ●</p> </div>	<p>XXXXX - Lot Code</p>

Note : SINOPOWER lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. SINOPOWER lead-free products meet or exceed the leadfree requirements of IPC/JEDEC J-STD-020D for MSL classification at lead-free peak reflow temperature. SINOPOWER defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

SINOPOWER reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.

**Absolute Maximum Ratings** ( $T_A=25^\circ\text{C}$  Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit	
<b>Common Ratings</b>				
$V_{DSS}$	Drain-Source Voltage	-20	V	
$V_{GSS}$	Gate-Source Voltage	$\pm 12$		
$T_J$	Maximum Junction Temperature	150	$^\circ\text{C}$	
$T_{STG}$	Storage Temperature Range	-55 to 150		
$I_S$	Diode Continuous Forward Current	$T_C=25^\circ\text{C}$	-9*	A
$I_D$	Continuous Drain Current	$T_C=25^\circ\text{C}$	-19*	A
		$T_C=100^\circ\text{C}$	-12*	
$I_{DM}^a$	Pulsed Drain Current	$T_C=25^\circ\text{C}$	-76	
$P_D$	Maximum Power Dissipation	$T_C=25^\circ\text{C}$	15.6	W
		$T_C=100^\circ\text{C}$	6.25	
$R_{\theta JC}$	Thermal Resistance-Junction to Case	8	$^\circ\text{C/W}$	
$I_D$	Continuous Drain Current	$T_A=25^\circ\text{C}$	-4.7	A
		$T_A=70^\circ\text{C}$	-3.7	
$P_D$	Maximum Power Dissipation	$T_A=25^\circ\text{C}$	0.96	W
		$T_A=70^\circ\text{C}$	0.61	
$R_{\theta JA}^c$	Thermal Resistance-Junction to Ambient	Steady state	130	$^\circ\text{C/W}$
$I_{AS}^b$	Avalanche Current, Single pulse	$L=0.1\text{mH}$	-18	A
$E_{AS}^b$	Avalanche Energy, Single pulse	$L=0.1\text{mH}$	16	mJ

Note \* : Calculated continuous current based on maximum allowable junction temperature. Bonding wire limitation current is 8A.

Note a : Pulse width limited by max. junction temperature.

Note b : UIS tested and pulse width limited by maximum junction temperature  $150^\circ\text{C}$  (initial temperature  $T_J=25^\circ\text{C}$ ).

Note c : Surface Mounted on  $1\text{in}^2$  pad area.

**Electrical Characteristics** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

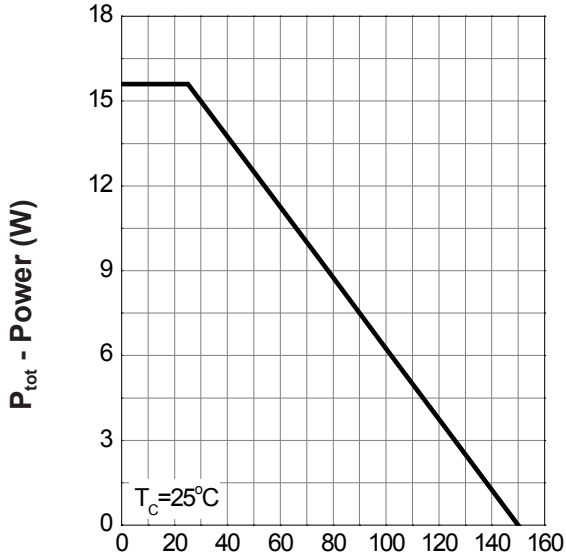
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_{DS}=-250\mu A$	-20	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=-16V, V_{GS}=0V$	-	-	-1	$\mu A$
		$T_J=85^\circ C$	-	-	-30	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_{DS}=-250\mu A$	-0.5	-0.7	-1.0	V
$I_{GSS}$	Gate Leakage Current	$V_{GS}=\pm 12V, V_{DS}=0V$	-	-	$\pm 100$	nA
$R_{DS(ON)}^d$	Drain-Source On-state Resistance	$V_{GS}=-4.5V, I_{DS}=-4.4A$	-	23	29	m $\Omega$
		$V_{GS}=-2.5V, I_{DS}=-3.4A$	-	30	40	
		$V_{GS}=-1.8V, I_{DS}=-2.4A$	-	42	60	
<b>Diode Characteristics</b>						
$V_{SD}^d$	Diode Forward Voltage	$I_{SD}=-1A, V_{GS}=0V$	-	-0.7	-1	V
$t_{rr}$	Reverse Recovery Time	$I_{SD}=-4.4A, di_{SD}/dt=100A/\mu s$	-	24	-	ns
$Q_{rr}$	Reverse Recovery Charge		-	6	-	nC
<b>Dynamic Characteristics<sup>e</sup></b>						
$R_G$	Gate Resistance	$V_{GS}=0V, V_{DS}=0V, F=1MHz$	-	6	-	$\Omega$
$C_{iss}$	Input Capacitance	$V_{GS}=0V,$ $V_{DS}=-10V,$ Frequency=1.0MHz	-	1155	1500	pF
$C_{oss}$	Output Capacitance		-	205	-	
$C_{rss}$	Reverse Transfer Capacitance		-	165	-	
$t_{d(ON)}$	Turn-on Delay Time	$V_{DD}=-10V, R_L=10\Omega,$ $I_{DS}=-1A, V_{GEN}=-10V,$ $R_G=1\Omega$	-	8	15	ns
$t_r$	Turn-on Rise Time		-	13	24	
$t_{d(OFF)}$	Turn-off Delay Time		-	88	158	
$t_f$	Turn-off Fall Time		-	38	69	
<b>Gate Charge Characteristics<sup>e</sup></b>						
$Q_g$	Total Gate Charge	$V_{DS}=-10V, V_{GS}=-4.5V,$ $I_{DS}=-4.4A$	-	13.3	18.6	nC
$Q_{gs}$	Gate-Source Charge		-	2.4	-	
$Q_{gd}$	Gate-Drain Charge		-	3.8	-	

Note d : Pulse test ; pulse width $\leq 300\mu s$ , duty cycle $\leq 2\%$ .

Note e : Guaranteed by design, not subject to production testing.

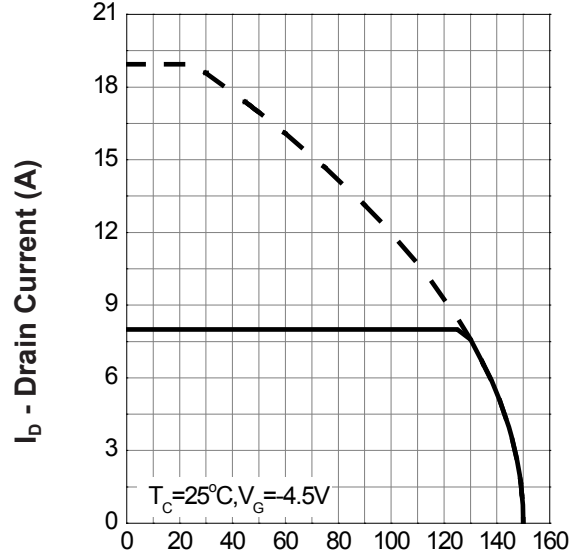
Typical Operating Characteristics

Power Dissipation



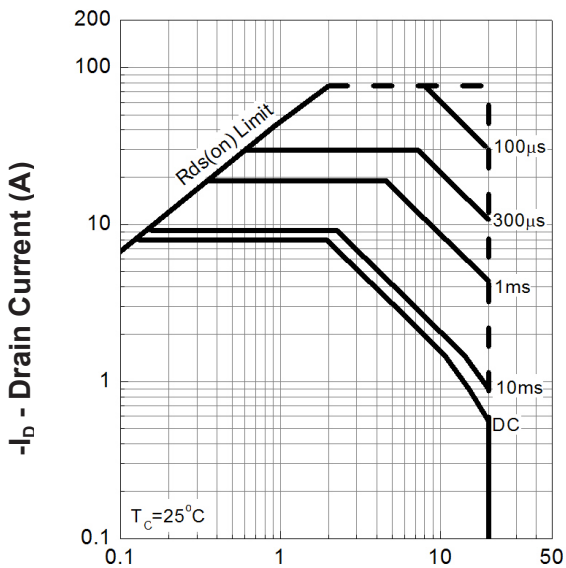
$T_j$  - Junction Temperature ( $^{\circ}\text{C}$ )

Drain Current



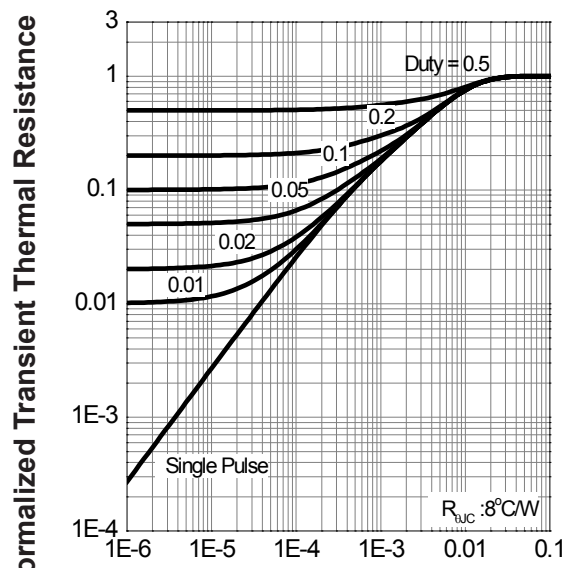
$T_j$  - Junction Temperature ( $^{\circ}\text{C}$ )

Safe Operation Area



$-V_{DS}$  - Drain - Source Voltage (V)

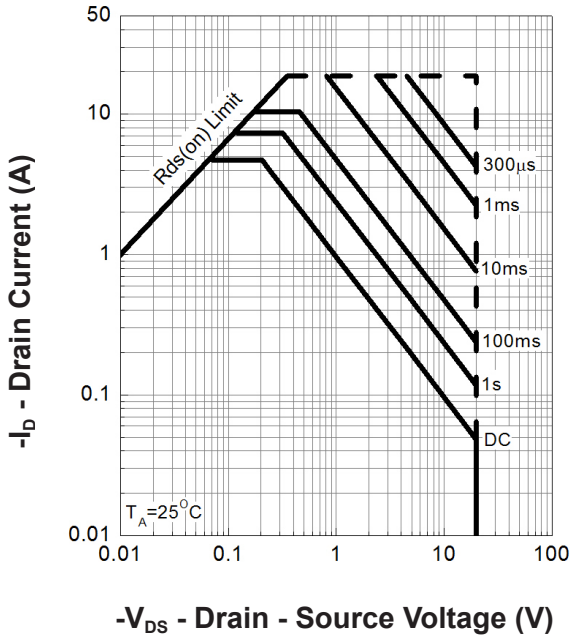
Thermal Transient Impedance



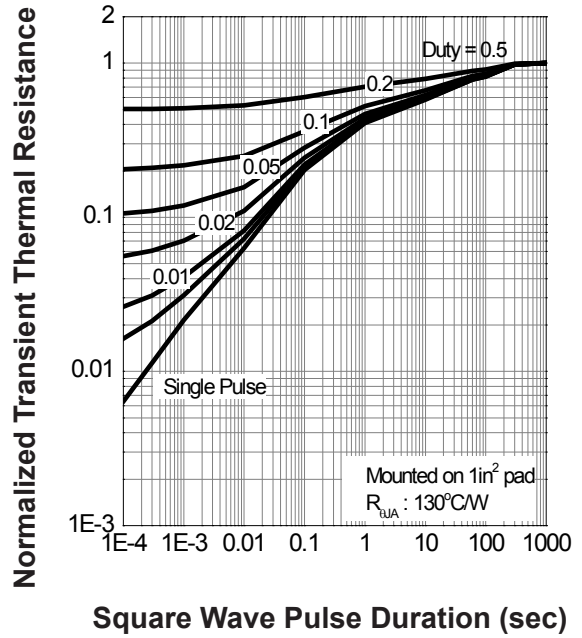
Square Wave Pulse Duration (sec)

Typical Operating Characteristics(Cont.)

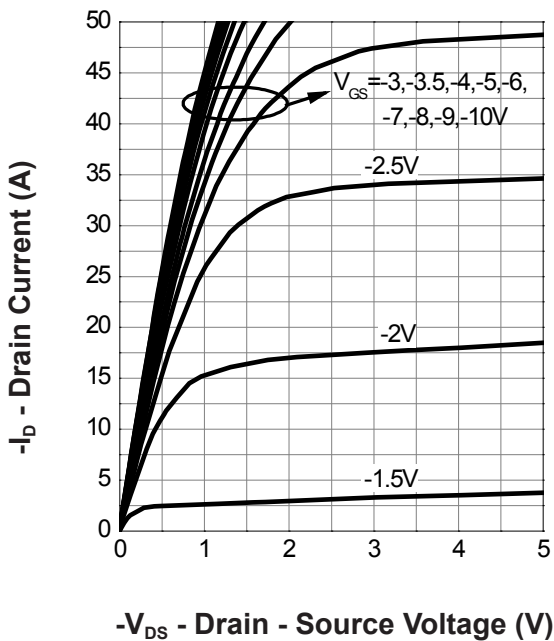
Safe Operation Area



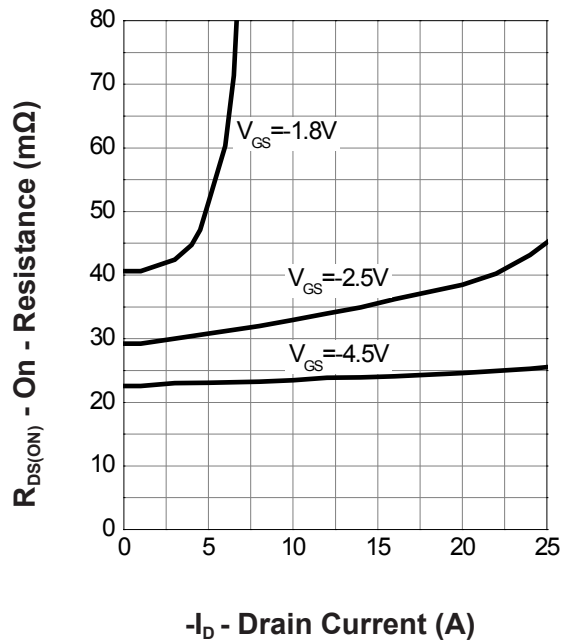
Thermal Transient Impedance



Output Characteristics

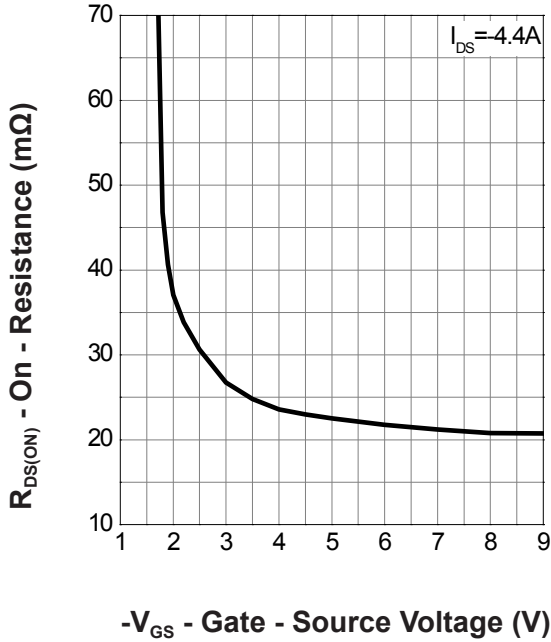


Drain-Source On Resistance

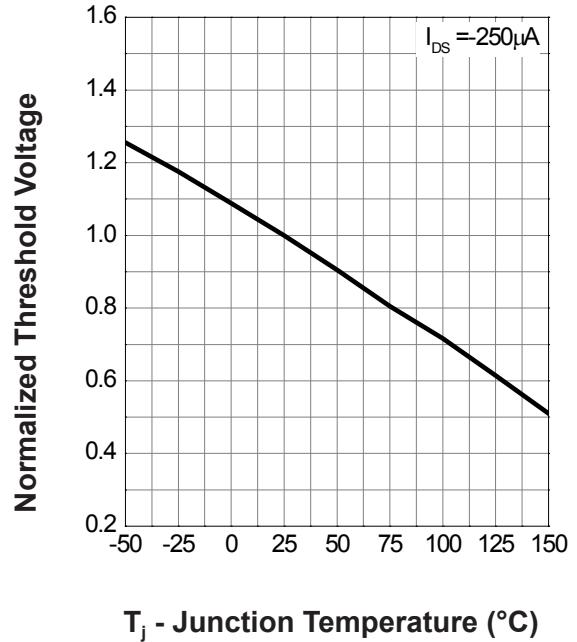


Typical Operating Characteristics(Cont.)

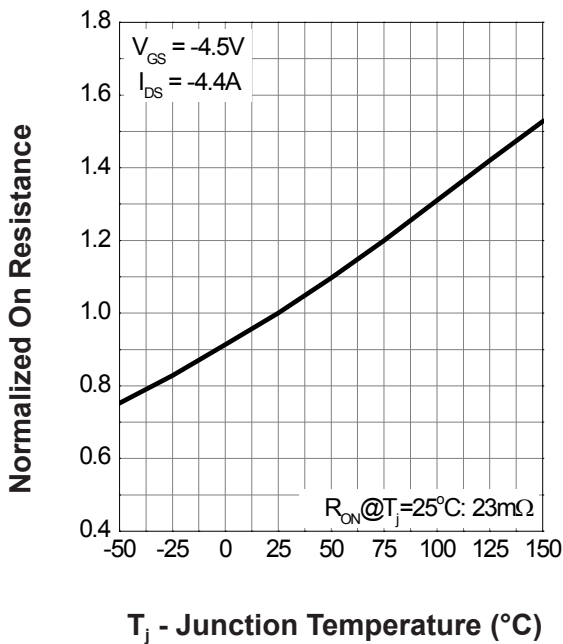
Gate-Source On Resistance



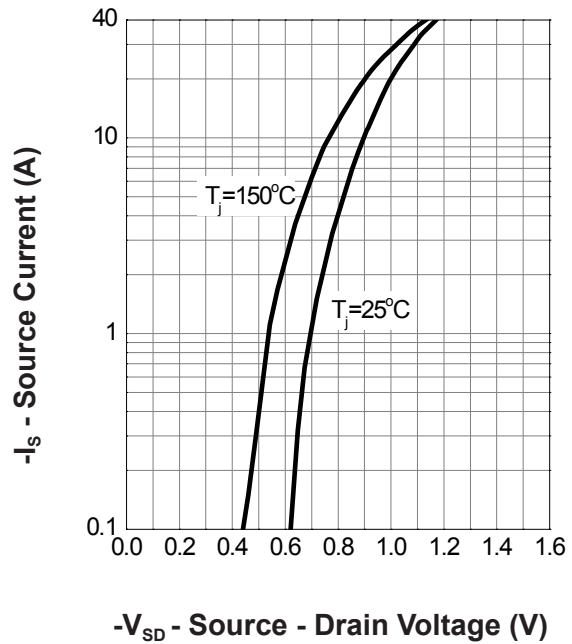
Gate Threshold Voltage



Drain-Source On Resistance

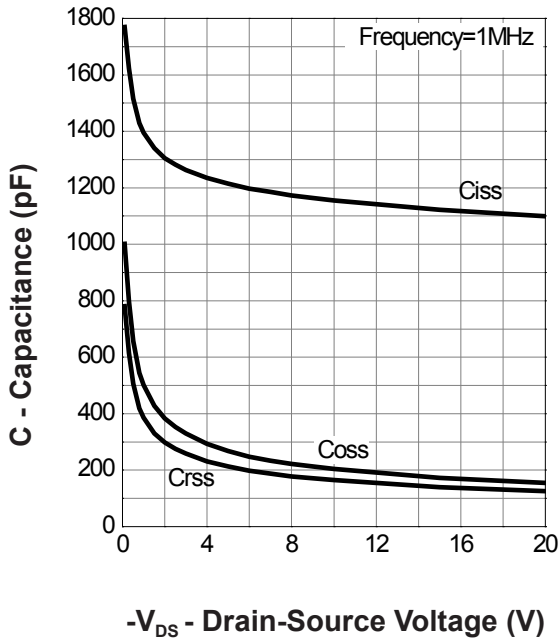


Source-Drain Diode Forward

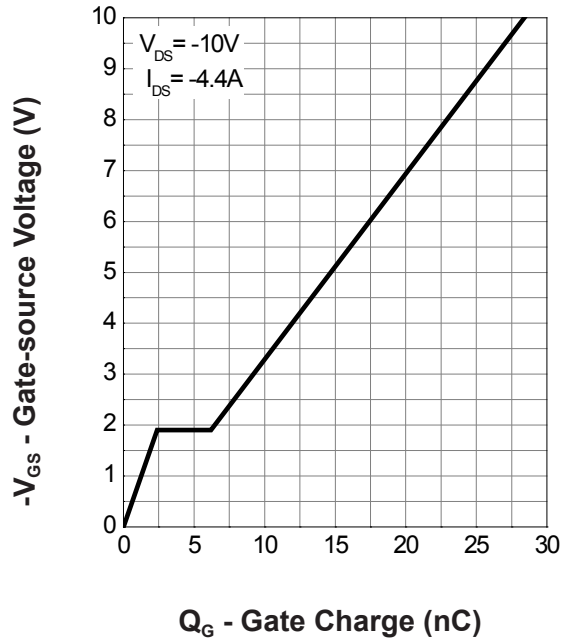


Typical Operating Characteristics(Cont.)

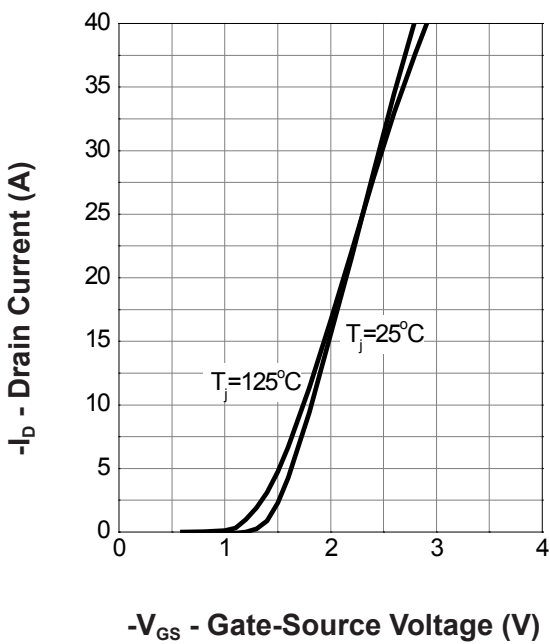
Capacitance



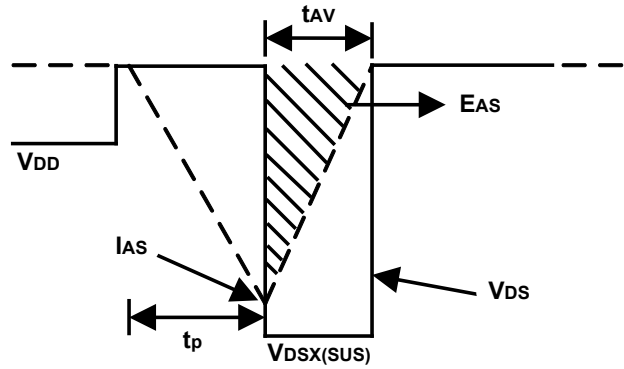
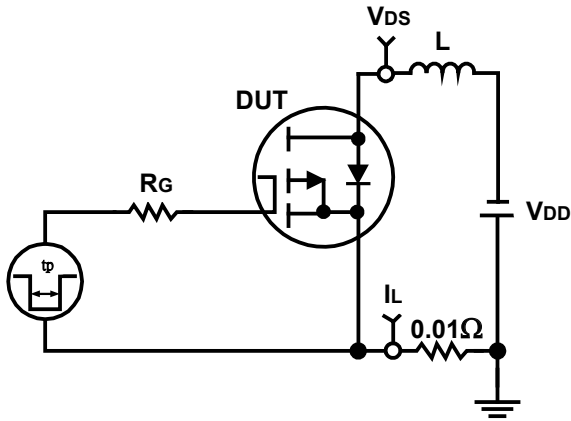
Gate Charge



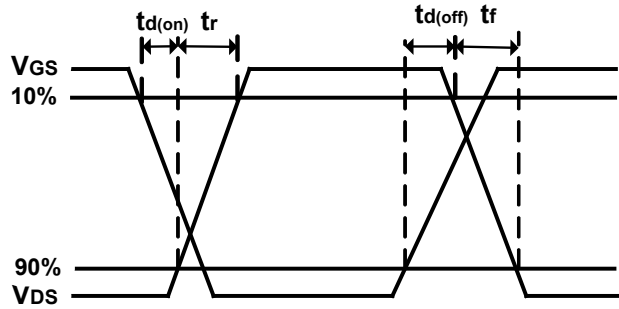
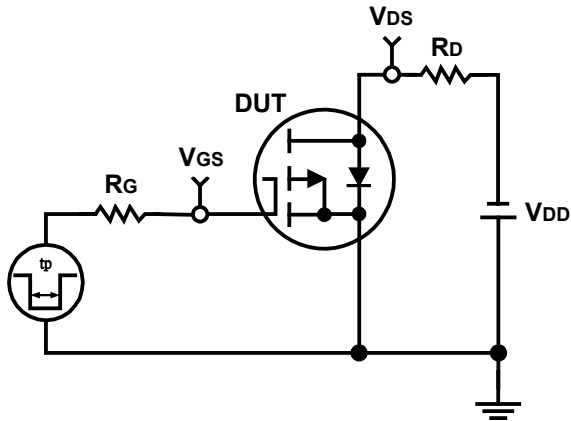
Transfer Characteristics



Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms





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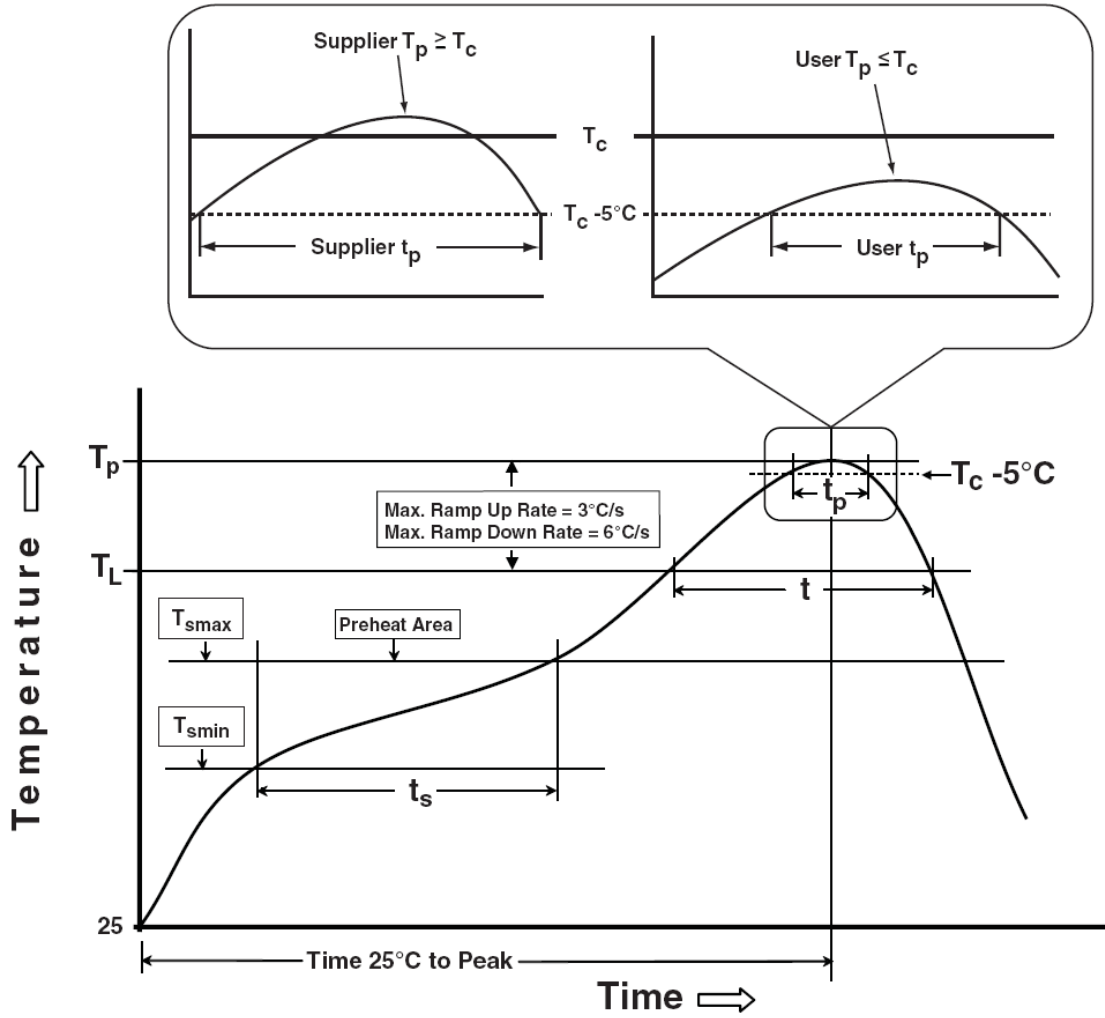
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Classification Profile



## Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
<b>Preheat &amp; Soak</b>		
Temperature min ( $T_{smin}$ )	100 °C	150 °C
Temperature max ( $T_{smax}$ )	150 °C	200 °C
Time ( $T_{smin}$ to $T_{smax}$ ) ( $t_s$ )	60-120 seconds	60-120 seconds
Average ramp-up rate ( $T_{smax}$ to $T_p$ )	3 °C/second max.	3°C/second max.
Liquidous temperature ( $T_L$ )	183 °C	217 °C
Time at liquidous ( $t_L$ )	60-150 seconds	60-150 seconds
Peak package body Temperature ( $T_p$ )*	See Classification Temp in table 1	See Classification Temp in table 2
Time ( $t_p$ )** within 5°C of the specified classification temperature ( $T_c$ )	20** seconds	30** seconds
Average ramp-down rate ( $T_p$ to $T_{smax}$ )	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature ( $T_p$ ) is defined as a supplier minimum and a user maximum.		
** Tolerance for time at peak profile temperature ( $t_p$ ) is defined as a supplier minimum and a user maximum.		

Table 1. SnPb Eutectic Process – Classification Temperatures ( $T_c$ )

Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures ( $T_c$ )

Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> 350-2000	Volume mm <sup>3</sup> >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

## Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	1000 Hrs, 80% of VDS max @ $T_{jmax}$
HTGB	JESD-22, A108	1000 Hrs, 100% of VGS max @ $T_{jmax}$
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -65°C~150°C

## Customer Service

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TEL: 886-3-5635818 Fax: 886-3-5635080

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