

FEATURES

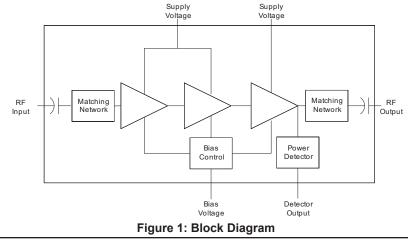
- InGaP HBT Technology
- -47 dBc ACPR @ + 10 MHz, +24.5 dBm
- 30 dB Gain
- High Efficiency
- Low Transistor Junction Temperature
- Internally matched for a 50 Ω System
- Low Profile Miniature Surface Mount Package; Halogen Free and RoHS Compliant
- Multi-Carrier Capability

APPLICATIONS

- LTE, WCDMA and HSDPA Air Interfaces
- Picocell, Femtocell, Home Nodes
- Customer Premises Equipment (CPE)
- Data Cards and Terminals

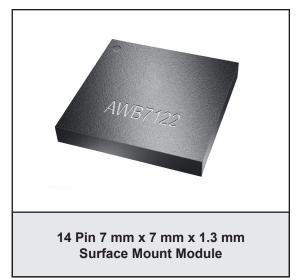
PRODUCT DESCRIPTION

The AWB7122 is a highly linear, fully matched, power amplifier module designed for picocell, femtocell, and customer premises equipment (CPE) applications. Its high power efficiency and low adjacent channel power levels meet the extremely demanding needs of small cell infrastructure architectures. Designed for LTE, WCDMA, HSDPA air interfaces operating in the 1805 MHz to 1880 MHz band, the AWB7122 delivers up to +24.5 dBm of LTE (E-TM1.1) power with an ACPR of -47 dBc. It operates from a convenient +4.2 V supply and provides 30 dB of gain. The device is manufactured using an advanced InGaP HBT MMIC technology offering state-of-the-art reliability, temperature stability, and ruggedness. The self-contained 7 mm x 7 mm x 1.3 mm surface mount package incorporates RF matching networks optimized for output power, efficiency, and linearity in a 50 Ω system.



AWB7122

1805 MHz to 1880 MHz Small-Cell Power Amplifier Module DATA SHEET



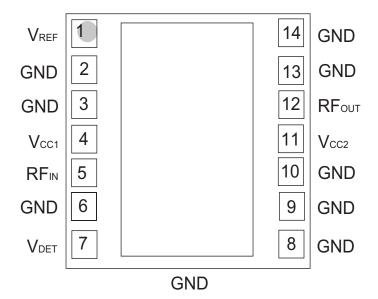


Figure 2: Pinout (X-ray Top View)

| PIN | NAME | DESCRIPTION | |
|-----|------------------|-------------------|--|
| 1 | VREF | Reference Voltage | |
| 2 | GND | Ground | |
| 3 | GND | Ground | |
| 4 | V _{CC1} | Supply Voltage | |
| 5 | RFℕ | RF Input | |
| 6 | GND | Ground | |
| 7 | Vdet | Detector Voltage | |
| 8 | GND | Ground | |
| 9 | GND | Ground | |
| 10 | GND | Ground | |
| 11 | Vcc2 | Supply Voltage | |
| 12 | RFout | RF Output | |
| 13 | GND | Ground | |
| 14 | GND | Ground | |

Table 1: Pin Description

ELECTRICAL CHARACTERISTICS

| Table 2: Absolute Minimum and Maximum Ratings | | | | | |
|--|----------------------|------|-------------------|--|--|
| PARAMETER | MIN | MAX | UNIT | | |
| Supply Voltage (Vcc) | 0 | +5 | V | | |
| Reference Voltage (VREF) | 0 | +3.5 | V | | |
| RF Output Power (Pout) | - | +28 | dBm, modulated | | |
| RF Input Power (PIN) | - | +10 | dBm, CW | | |
| ESD Rating Human Body Model ⁽¹⁾ Charged Device Model ⁽²⁾ | Class 1C Class IV | - | | | |
| MSL Rating (3) | 4 | - | | | |
| Junction Temperature (TJ) | - | +150 | °C | | |
| Storage Temperature (Tstg) | -40 | +150 | °C | | |

Table 2: Absolute Minimum and Maximum Ratings

Functional operation is not implied under these conditions. Exceeding any one or a combination of the Absolute Maximum Rating Conditions may cause permanent damage to the device. Exposure to absolute ratings for extended periods of time may adversely affect reliability.

Notes:

JEDEC JS-001-2010.
JEDEC JESD22-C101D.
260 °C peak reflow.

| PARAMETER | MIN | ТҮР | MAX | UNIT | COMMENTS |
|----------------------------|------------|------------|---------------|------|---------------------------|
| Operating Frequency (f) | 1805 | - | 1880 | MHz | |
| Supply Voltage (Vcc) | +3.2 | +4.2 | +4.5 | V | |
| Reference Voltage (VREF) | +2.80 0 | +2.85 - | +2.90 +0.5 | V | PA "on" PA "shut down" |
| RF Output Power (Pour) (1) | - | +24.5 | - | dBm | |
| Case Temperature (Tc) (2) | -40 | - | +85 | °C | |

Table 3: Operating Ranges

The device may be operated safely over these conditions; however, parametric performance is guaranteed only over the conditions defined in the electrical specifications. *Notes:*

(1) Typ RF Output Power is used during production test.

(2) Case Temperature references the board temperature at the ground paddle on the backside of the package.

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AWB7122

| PARAMETER MIN TYP MAX UNIT COMMENTS | | | | | | |
|---|-------------|-------------------|-------------------|------|---|--|
| PARAIVIETER | | 116 | WIAA | UNIT | COMMENTS | |
| Gain ⁽²⁾ | 30 | 32 | 36 | dB | | |
| ACPR ^{(1), (2), (3)} @ 10 MHz @ 20 MHz | - | -47 -57 | -45 -55 | dBc | | |
| Power-Added Efficiency (1), (2), (3) | 14.5 | 16 | - | % | | |
| Thermal Resistance | - | 23.8 | - | °C/W | Junction to case | |
| Supply Current (1), (2), (3) | - | 390 | 463 | mA | Total through Vcc pins | |
| Quiescent Current (lcq) | - | 135 | 175 | mA | | |
| Reference Current | - | 6.5 | 10 | mA | through VREF pin | |
| Leakage Current | - | 1.5 | 5 | μA | Vcc = +4.5 V, Vref = 0 V | |
| Harmonics 2fo 3fo 4fo | - - - | -56 -65 -65 | -48 -57 -57 | dBc | | |
| Input Return Loss | 10 | 14 | - | dB | | |
| P1dB | - | 32 | - | dBm | | |
| Spurious Output Level (all spurious outputs) | - | - | -60 | dBc | Pour ≤ +24.5 dBm In-band load VSWR < 5:1 Out-of-band load VSWR < 10:1 Applies over all voltage and temperature operating ranges | |
| Load mismatch stress with no permanent degradation or failure | 8:1 | - | - | VSWR | V _{CC} = +4.2 V, P _{IN} = 0 dBm Applies over full operating temperature range | |

Table 4: Electrical Specifications (Tc = +25 °C, Vcc = +4.2 V, V_{REF} = +2.85 V, 50 Ω system)

Notes:

(1) ACPR and Efficiency measured at 1842.5 MHz.

(2) $P_{OUT} = +24.5 \, dBm$.

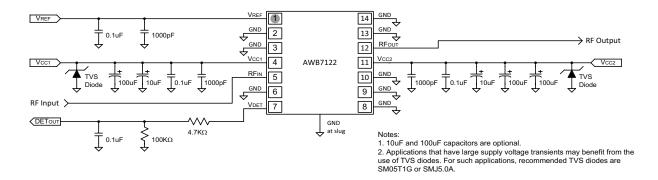
(3) LTE E-TM1.1 (10 MHz)

APPLICATION INFORMATION

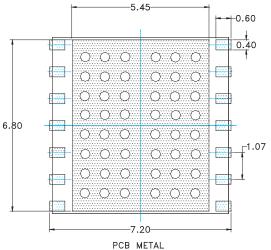
To ensure proper performance, refer to all related Application Notes.

Shutdown Mode

The power amplifier may be placed in a shutdown mode by applying logic low levels (see Operating Ranges table) to the VREF voltage.

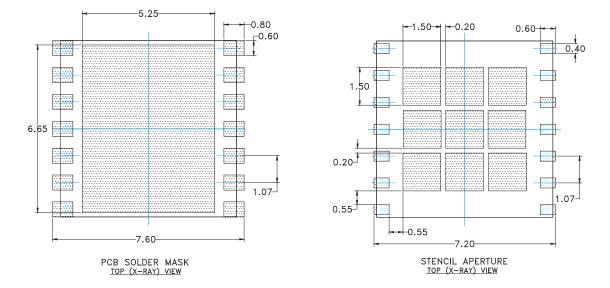






TOP (X-RAY) VIEW ONLY PACKAGE I/O'S AND GROUND REQUIREMENTS SHOWN. NOTES:

- (1) UNLESS SPECIFIED DIMENSIONS ARE SYMMETRICAL ABOUT CENTER LINES SHOWN.
- (2) DIMENSIONS IN MILLIMETERS.
- (3) VIAS SHOWN IN PCB METAL VIEW ARE FOR REFERENCE ONLY. NUMBER & SIZE OF THERMAL VIAS REQUIRED DEPENDENT ON HEAT DISSIPATION REQUIREMENT AND THE PCB PROCESS CAPABILITY.





PACKAGE OUTLINE

1.32

0.400

0.10

7.00

5.45

0.275

7.00

6.80

1.067

0.400

1.42

0.425

_

7.10

_

7.10

_

_

А 1.22

ь 0.375

с _

D 6.90

D1 _

D2

Е 6.90

E1

e _

L.

_

_

0.375

0.048

0.0148

_

0.272

0.272

_

0.052

0.0157

0.004

0.276

0.215

0.0108

0.276

0.268

0.0420

0.425 0.0148 0.0157 0.0167

0.056

0.0167

0.280

0.280

_

14X

_

_

_

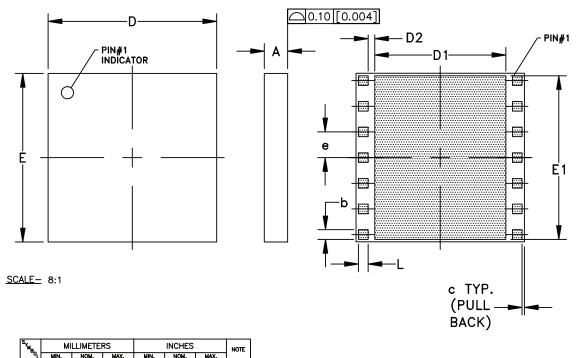
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_

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6X

14X

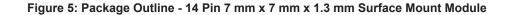


| NOTES: |
|--------|
|--------|

- 1. CONTROLLING DIMENSIONS: MILLIMETERS
- 2.

3.

CONTROLLING DIMENSIONS: MILLIMETERS UNLESS SPECIFIED TOLERANCE=±0.076[0.003]. PADS (INCLUDING CENTER) SHOWN UNIFORM SIZE FOR REFERENCE ONLY. ACTUAL PAD SIZE AND LOCATION WILL VARY WITHIN MIN. AND MAX. DIMENSIONS ACCORDING TO SPECIFIC LAMINATE DESIGN.



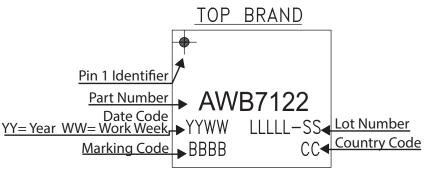


Figure 6: Branding Specification

AWB7122

COMPONENT PACKAGING

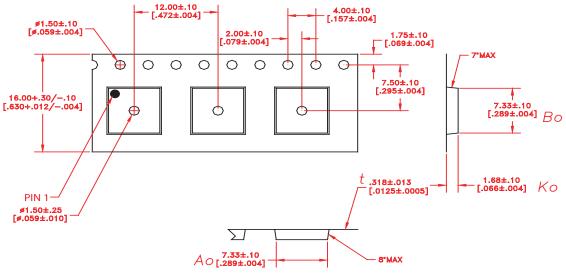


Figure 7: Tape & Reel Packaging

Table 5: Tape & Reel Dimensions

| PACKAGE TYPE TAPE WIDTH | | POCKET PITCH | REEL CAPACITY | MAX REEL DIA |
|-------------------------|-------|--------------|------------------|--------------|
| 7 mm x 7 mm x 1.3 mm | 16 mm | 12 mm | 2500 | 13" |

ORDERING INFORMATION

| ORDER NUMBER | TEMPERATURE RANGE | PACKAGE DESCRIPTION | COMPONENT PACKAGING |
|-----------------|----------------------|---|-------------------------------------|
| AWB7122P7 | -40 °C to +85 °C | RoHS-compliant 14 Pin 7 mm x 7 mm x 1.3 mm Surface Mount Module | Loose in Bag |
| AWB7122P8 | -40 °C to +85 °C | RoHS-compliant 14 Pin 7 mm x 7 mm x 1.3 mm Surface Mount Module | Tape and Reel, 2500 pieces per Reel |
| AWB7122P9 | -40 °C to +85 °C | RoHS-compliant 14 Pin 7 mm x 7 mm x 1.3 mm Surface Mount Module | Partial Reel |

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