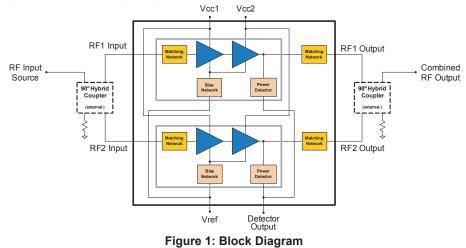


PRODUCT DESCRIPTION

The AWB7224 is a fully matched, Multi-Chip-Module (MCM) designed for picocell, femtocell, and customer premises equipment (CPE) applications. Consisting of two parallel path high linearity, high efficiency power amplifiers, the device meets the extremely demanding needs of small cell infrastructure architectures. Designed for LTE, WCDMA and HSDPA air interfaces operating in the 728 MHz to 768 MHz bands, the AWB7224 delivers up to +27 dBm of LTE (E-TM1.1) power through an external 90-degree hybrid coupler,

with an ACPR of -47 dBc. The device operates from a convenient +4.5 V supply and provides 29 dB of RF gain. The AWB7224 is manufactured using an advanced InGaP HBT MMIC technology offering state-of-the-art reliability, temperature stability, and ruggedness. Its 7 mm x 7 mm x 1.3 mm surface mount package incorporates RF matching networks optimized for output power, efficiency, and linearity in a 50 Ω system.



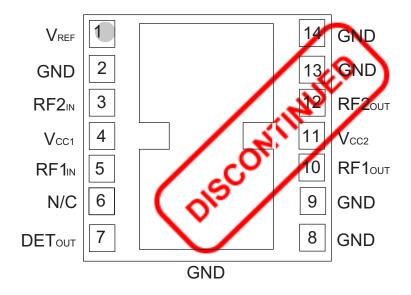


Figure 2: Pinout (X-ray Top View)

PIN	NAME	DESCRIPTION
1	VREF	Reference Voltage
2	GND	Ground
3	RF2ℕ	RF2 Input
4	V _{CC1}	Supply Voltage
5	RF1ℕ	RF1 Input
6	N/C	No Connection
7	DETout	Detector Output
8	GND	Ground
9	GND	Ground
10	RF1out	RF1 Output
11	Vcc2	Supply Voltage
12	RF2out	RF2 Output
13	GND	Ground
14	GND	Ground

Table	1:	Pin	Description
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ELECTRICAL CHARACTERISTICS

Table 2: Absolute Minimum and Maximum Ratings								
PARAMETER	MIN	MAX	UNIT					
Supply Voltage (Vcc)	0	+5	≻ v J					
Reference Voltage (VREF)	0	+3.5						
RF Output Power (Pour) (1)	J.	+30	dBm, modulated					
RF Input Power (PIN)	çÖ.	10	dBm, CW					
ESD Rating Human Body Model ⁽²⁾ Charged Device Model ⁽³⁾	Class IC Class IV	-						
MSL Rating (4)	3	-						
Junction Temperature (TJ)	-	+150	°C					
Storage Temperature (Tstg)	-40	+150	°C					

Functional operation is not implied under these conditions. Exceeding any one or a combination of the Absolute Maximum Rating Conditions may cause permanent damage to the device. Exposure to absolute ratings for extended periods of time may adversely affect reliability.

Notes:

At output of external 90° hybrid coupler.
JEDEC JS-001-2010.
JEDEC JESD22-C101D.
260 °C peak reflow.

PARAMETER	MIN	ТҮР	MAX	UNIT	COMMENTS
Operating Frequency (f)	728	-	768	MHz	
Supply Voltage (Vcc)	+3.6	+4.5	+4.65	V	
Reference Voltage (VREF)	+2.80 0	+2.85 -	+2.90 +0.5	V	PA "on" PA "shut down"
RF Output Power (Pour) (1)	-	+27	-	dBm	
Case Temperature (Tc) (2)	-40	-	+85	°C	

Talata	۰.	0		D
lable	3:	U	perating	Ranges

The device may be operated safely over these conditions; however, parametric performance is guaranteed only over the conditions defined in the electrical specifications. *Notes:*

(1) Typ RF Output Power is used during production test.

(2) Case Temperature references the board temperature at the ground paddle on the backside of the package.

PARAMETER	MIN	ТҮР	MAX	UNIT	COMMENTS
Gain (2)	27	29	34	dB	728 - 768 MHz
ACPR ^{(1), (2), (3)} @ 10 MHz @ 20 MHz	-	-47 -57	-45 -54	dBc	JE
Power-Added Efficiency (1), (2), (3)	11.5	13	/-	%	
Thermal Resistance (RJc) (4)	-	17	2	°C/W	Junction to Case
Supply Current ^{(1), (2), (3)}	-	825	968	mA	total through Vcc pins
Quiescent Current (Icq)	200	275	340	mA	
Reference Current	7	10	13	mA	through VREF pin
Leakage Current	-	3	10	μA	Vcc = +5 V, Vref = 0 V
Harmonics ⁽²⁾ 2fo 3fo, 4fo	-	-53 -65	-43 -53	dBc	
Input Return Loss (2)	15	20	-	dB	
Output Return Loss (2)	15	20	-	dB	
P1dB	-	+35.5	-	dBm	CW tone
Spurious Output Level ⁽²⁾ (all spurious outputs)	-	-	-60	dBc	Pout ≤ +27 dBm In-band load VSWR < 5:1 Out-of-band load VSWR < 10:1 Applies over all voltage and temperature operating ranges
Load mismatch stress with no per- manent degradation or failure ⁽²⁾	8:1	-	-	VSWR	Vcc = +4.5 V, Pout = +27 dBm Applies over full operating temperature range

Table 4: Electrical Specifications (Tc = +25 °C, Vcc = +4.5 V, VREF = +2.85 V, 50 Ω system)

Notes:

(1) ACPR and Efficiency measured at 748 MHz.

(2) $P_{OUT} = +27 \, dBm$, using specified external 90° hybrid couplers.

(3) LTE E-TM1.1 (10 MHz).

APPLICATION INFORMATION

To ensure proper performance, refer to all related Application Notes.

Shutdown Mode

The power amplifier may be placed in a shutdown mode by applying logic low levels (see Operating Ranges table) to the VREF voltage.

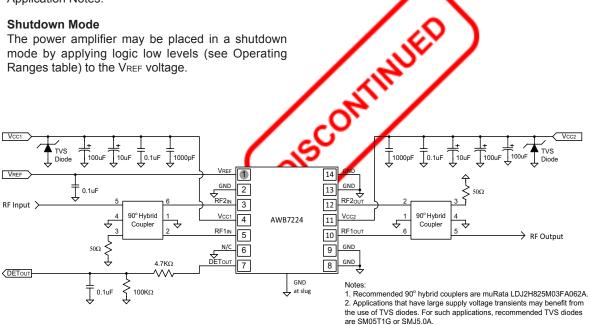
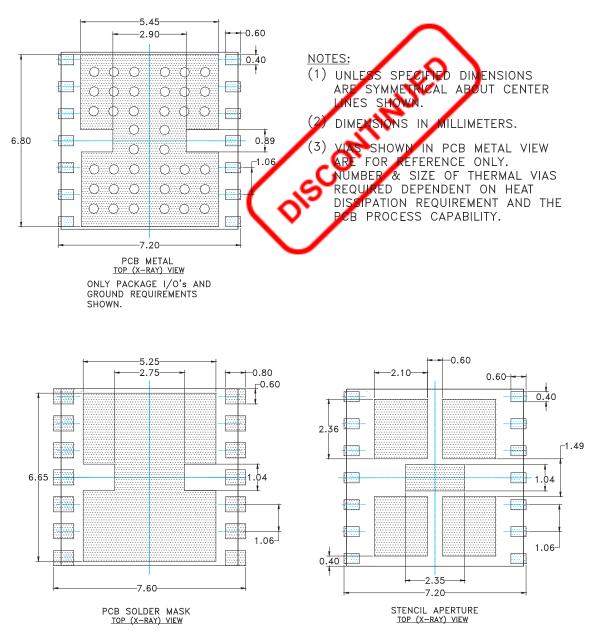


Figure 3: Application Circuit Schematic





PACKAGE OUTLINE

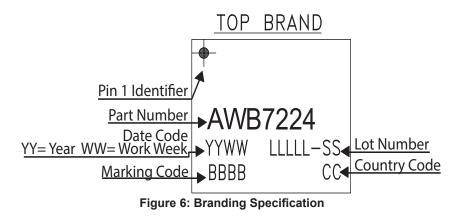


ľ™ _B .	мі	LLIMETER	RS .		INCHES			
[™] ®₀_	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.		
A	1.17	1.27	1.37	0.046	0.05	0.054	1	
ь	0.375	0.400	0.425	0.0148	0.0157	0.0167	14X	
c	-	0.10	-	-	0.004	-	-	
D	6.90	7.00	7.10	0.272	0.276	0.280	-	
D1	-	5.40	1	-	0.213	-	1	
D2	-	0.30	-	-	0.0118	-		
D3	-	2.90	1	-	0.114	-		
E	6.90	7.00	7.10	0.272	0.276	0.280	-	
E1	-	6.80	-	-	0.268	-	-	
E2	-	0.89	-	-	0.035	-		
е	-	1.067	I	-	0.0420	-	6X	
L	0.375	0.400	0.425	0.0148	0.0157	0.0167	14X	

NOTES:

CONTROLLING DIMENSIONS: MILLIMETERS
UNLESS SPECIFIED TOLERANCE=±0.076[0.003].
PADS (INCLUDING CENTER) SHOWN UNIFORM SIZE FOR <u>REFERENCE</u> ONLY. ACTUAL PAD SIZE AND LOCATION WILL VARY WITHIN MIN. AND MAX. DIMENSIONS ACCORDING TO SPECIFIC LAMINATE DESIGN.

Figure 5: Package Outline - 14 Pin 7 mm x 7 mm x 1.3 mm Surface Mount Module



COMPONENT PACKAGING

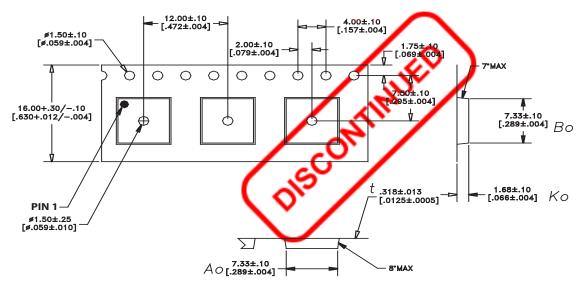


Figure 7: Tape & Reel Packaging

Table 5: Tape & Reel Dimensions

PACKAGE TYPE	TAPE WIDTH	POCKET PITCH	REEL CAPACITY	MAX REEL DIA
7 mm x 7 mm x 1.3 mm 16 mm		12 mm	2500	13"

ORDERING INFORMATION

ORDER NUMBER	TEMPERATURE RANGE	PACKAGE DESCRIPTION	COMPONENT PACKAGING
AWB7224P7	-40 °C to +85 °C	RoHS-compliant 14 Pin 7 mm x 7 mm x 1.3 mm Surface Mount Module	Loose in Bag
AWB7224P8	-40 °C to +85 °C	RoHS-compliant 14 Pin 7 mm x 7 mm x 1.3 mm Surface Mount Module	Tape and Reel, 2500 pieces per Reel
AWB7224P9	-40 °C to +85 °C	RoHS-compliant 14 Pin 7 mm x 7 mm x 1.8 mm Surface Mount Module	Partial Reel

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