

DATA SHEET

OLS100: Hermetic Surface-Mount Phototransistor Optocoupler

Features

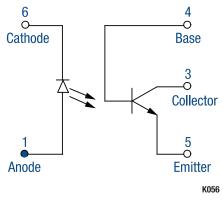
- Current transfer ratio guaranteed over –55 °C to +125 °C ambient temperature range
- Electrical isolation: 1500 VDC
- High Current Transfer Ratio (CTR):
 - 75% minimum over temperature
 - At low input current, 100% at $I_F = 1$ mA over temperature
- · High reliability and rugged construction
- \bullet CTR-comparable to Darlington output, but with low saturation $V_{\text{CE}}=0.15~\text{V}$
- Similar to 4N2X, 4N4X type optocouplers
- High reliability screenings on this product are available

Description

The OLS100 is designed especially for applications that require optical isolation with a high CTR and low saturation VCE. Each OLS100 device consists of an LED and N-P-N silicon phototransistor, mounted and coupled in a custom hermetic surface-mount Leadless Chip Carrier (LCC) package.

The low input current makes the OLS100 well-suited for direct Complementary Metal Oxide Semiconductor (CMOS) to Low Power Schottky Transistor (LSTTL) to Transistor Logic (TTL) interfaces.

The OLS100 device can be mounted using reflow soldering or conductive epoxies.





A functional block diagram of the OLS100 is shown in Figure 1. The absolute maximum ratings of the OLS100 are provided in Table 1. Electrical specifications are provided in Table 2.

Typical performance characteristics of the OLS100 are illustrated in Figures 2 through 5. A typical switching test circuit is shown in Figure 6, and package dimensions for the OLS100 are provided in Figure 7.

Table 1. OLS100 Absolute Maximum Ratings (Note 1)

Parameter	Symbol	Minimum	Maximum	Units
Coupled				
Input to output isolation voltage (Note 2)	VDC	-1500	+1500	V
Storage temperature range	Tstg	-65	+150	°C
Operating temperature range	ТА	-55	+125	°C
Mounting temperature range (3 minutes maximum)	Тмтс		+240	°C
Input Diode				
Average input current	Idd		40	mA
Peak forward current (\leq 1 ms duration)	lF		60	mA
Reverse voltage	VR		3	V
Power dissipation	Pd		70	mW
Output Detector				
Collector to emitter voltage	VCE		35	V
Emitter to collector voltage	VEC		7	V
Collector to base voltage	Vcb		70	V
Power dissipation (Note 3)	Pd		300	mW

Note 1: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

Note 2: Measured between pins 1, 2, and 6 shorted together, and pins 3, 4, and 5 shorted together. TA = 25 °C and duration = 1 s.

Note 3: Derate linearly at 3 mW/°C above 25 °C.

CAUTION: Although this device is designed to be as robust as possible, electrostatic discharge (ESD) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

Parameter	Symbol	Test Condition	Min	Typical	Max	Units
Current transfer ratio (Note 2)	CTR	$I_F = 10 \text{ mA}, V_{CE} = 5 \text{ V}$	100	200		%
		$I_F = 1 \text{ mA}, V_{CE} = 5 \text{ V}$	100	200		%
Saturation voltage	VCE_SAT	IF = 10.0 mA, Ic = 2.0 mA		0.15	0.3	V
Breakdown voltage:						
Collector to emitter	BVCEO	Ice = 100 μ A, Ta = 25 °C	30			V
Collector to base	ВУсво	ICB = 10 μ A, TA = 25 °C	70			V
Emitter to collector	BVECO	Iec = 100 μ A, Ta = 25 °C	5			V
Leakage current (collector to emitter)	Iceo	$V_{CE}=20$ V, $T_{A}=25$ °C			100	nA
		$V_{CE}=20~V,~T_{A}=100~^{\circ}C$			100	μΑ
Input:						
Forward voltage	VF	I⊧ = 10.0 mA	0.9	1.5	1.9	V
Reverse current	IR	$V_{R} = 3.0 V$			100.0	μA
Output leakage current (Note 3)	lı_o	$\label{eq:RH} \begin{array}{l} {\sf R}_{\sf H} \leq \!\! 50\%, {\sf T}_{\sf A} = 25 \ ^{\circ}{\sf C}, \\ {\sf V}_{\sf I_0} = 1500.0 \ {\sf V}_{\sf DC} \end{array}$			1.0	μA
Output capacitance (Note 3)	C I_0	f = 1 MHz			5	pF
Time:						
Turn on	ton	$V_{CC} = 10 \text{ V}, \text{ RL} = 100 \Omega$ Ic = 2 mA, TA = 25 °C		5	15	μs
Turn off	toff	Vcc = 10 V, RL = 100 Ω Ic = 2 mA, TA = 25 °C		5	15	μs

Table 2. OLS100 Electrical Specifications (Note 1) ($T_A = -55$ °C to +125 °C, Unless Otherwise Noted)

Note 1: Performance is guaranteed only under the conditions listed in this table.

Note 2: CTR is defined as the ratio of output collector current (Ic) to the forward LED current (IF) multiplied by 100%.

Note 3: Measured between pins 1, 2, and 6 shorted together, and pins 3, 4, and 5 shorted together. TA = 25 °C and duration = 1 s.

Typical Performance Characteristics (Top = -55 °C to +125 °C, Unless Otherwise Noted)

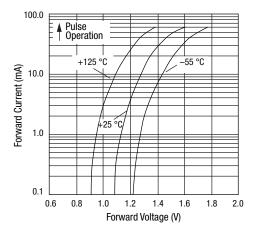


Figure 2. Forward Current vs Forward Voltage

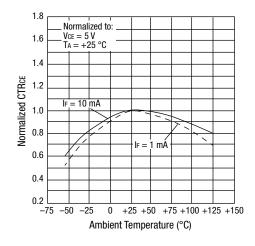


Figure 4. Normalized CTRcE vs Temperature

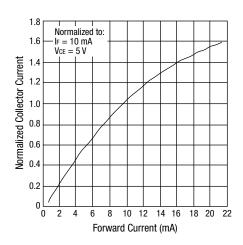


Figure 3. Normalized Collector Current vs Forward Current

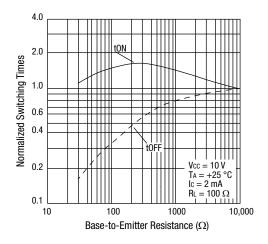
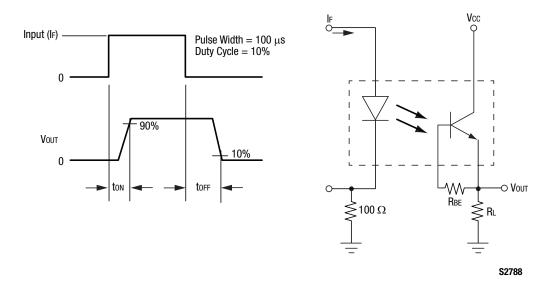


Figure 5. Normalized Switching Times vs Base-to-Emitter Resistance





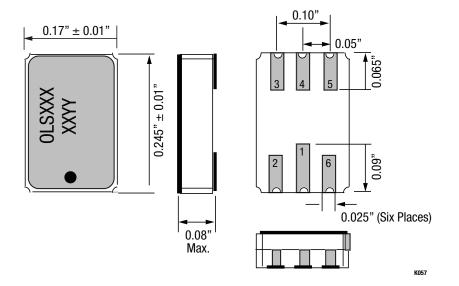


Figure 7. OLS100 Package Dimensions

Ordering Information

Model Name	Manufacturing Part Number
OLS100: Hermetic Surface-Mount Phototransistor Optocoupler	0LS100

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