

### **Applications**

- IEEE802.11b DSSS WLAN
- IEEE802.11g OFDM WLAN
- IEEE802.11a OFDM WLAN
- IEEE802.11n WLAN
- Access Points, PCMCIA, PC cards

#### Features

- All RF ports matched to 50  $\Omega$
- Integrated 2.4 GHz PA, 5 GHz PA, TX Filter, T switches and diplexers
- Integrated Power Detector for each TX Chain
- 21 dBm O/P Power, 802.11b, 11 Mbps, ACPR
  35 dBc
- 18 dBm @ 3.0 % EVM, 802.11g 54 Mbits
- 16.5 dBm @ 3.0 % EVM, 802.1
  a, 54 M
- Single supply voltage: 3.3 V ± 10%
- Lead free, Halogen free, RoHS compliant, MSL 3
- 5mm x 5mm x 0.9mm, QFN Package

# **Ordering Information**

Part No.	Package	Remark
SE2594L	32 pin QFN	Samples
SE2594L-R	32 pin QFN	Tape and Reel
SE2594L-EK1	N/A	Evaluation kit

# **Product Description**

The SE2594L is a complete 802.11a/b/g/n WLAN RF front-end module providing all the functionality of the power amplifiers, filtering, power detector, T/R switch, diplexers and associated matching. The SE2594L provides a complete 2.4 GHz and 5 GHz WLAN RF solution from the output of the transceiver to the antenna in an ultra compact form factor.

Designed for ease of use, all RF ports are matched to 50  $\Omega$  to simplify PCB layout and the interface to the transceiver RFIC. The SE2594L also includes a transmitter power detector for each band and transmit chain with 20 dB of dynamic range for each transmit chain. Each transmit chain has a separate digital enable control for transmitter power ramp on/off control. The power ramp rise/fall time is less than 0.7 µsec.

The device also provides a notch filter from 3.260-3.267 GHz and 3.28-3.89 GHz prior to the input of each 2.4 GHz and 5 GHz power amplifiers, respectively.

The SE2594L packaged in 5mm x 5mm x 0.9mm, Halogen free, Lead free, ROHS compliant, MSL 3 QFN package.



# **Functional Block Diagram**

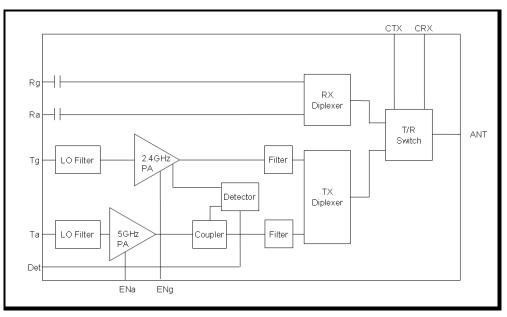


Figure 1: SE2594L Functional Block Diagram



DATA SHEET SE2594L: Dual-Band 802.11a/b/g/n Wireless LAN Front-End Preliminary Information

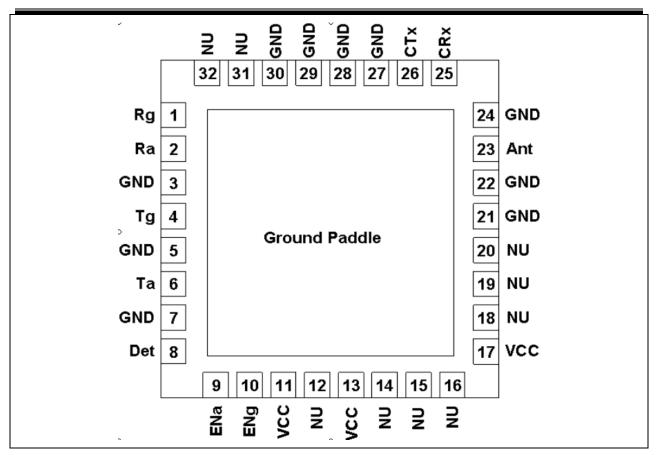


Figure 2: SE2594L Pin Out (Top View Through Package)

# **Pin Out Description**

Pin No.	Name	Description	Pin No.	Name	Description
1	Rg	2.4 GHz RF Receive Output, DC blocked	17	VCC	Supply Voltage
2	Ra	5 GHz RF Receive Output, DC blocked	18	NU	Not Used
3	GND	Ground	19	NU	Not Used
4	Тg	2.4 GHz RF Transmit Input, DC short to GND	20	NU	Not Used
5	GND	Ground	21	GND	Ground
6	Та	5 GHz RF Transmit Input, DC short to GND	22	GND	Ground
7	GND	Ground	23	Ant	Antenna
8	Det	2.4/5 GHz Power Detector Output	24	GND	Ground
9	ENa	5 GHz Power Amplifier Enable	25	CRx	Receive Switch Control
10	ENg	2.4 GHz Power Amplifier Enable	26	CTx	Transmit Switch Control
11	VCC	Supply Voltage	27	GND	Ground
12	NU	No Used	28	GND	Ground
13	VCC	Supply Voltage	29	GND	Ground
14	NU	Not Used	30	GND	Ground

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15	NU	Not Used	31	NU	Not Used
16	NU	Not Used	32	NU	Not Used

# **Absolute Maximum Ratings**

These are stress ratings only. Exposure to stresses beyond these maximum ratings may cause permanent damage to, or affect the reliability of the device. Avoid operating the device outside the recommended operating conditions defined below. This device is ESD sensitive. Handling and assembly of this device should be at ESD protected workstations.

Symbol	Definition	Min.	Max.	Unit
Vcc	Supply Voltage	-0.3	4.0	V
PU	ENa, ENg	-0.3	4.0	V
TXrf	Ta, Tg, ANT terminated in 6:1 load or better	-	12.0	dBm
TA	Operating Temperature Range	-40	85	°C
Тѕтс	Storage Temperature Range	-40	150	°C

### **Recommended Operating Conditions**

Symbol	Parameter	Min.	Тур.	Max.	Unit
Vcc	Supply Voltage	3.0	3.3	3.6	V
TA	Ambient Temperature	-40	25	85	°C

#### **DC Electrical Characteristics**

Conditions: Vcc = 3.3 V, T<sub>A</sub> = 25 °C, as measured on Skyworks Solutions' SE2594L-EV1 evaluation board (deembedded to device), all unused ports terminated with 50 ohms, unless otherwise noted

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
lcc-g	Total 802.11g Transmit Supply Current	P <sub>OUT</sub> = 18 dBm, 54 Mbps OFDM signal, 64 QAM ENg = 3.3 V, ENa = 0 V	-	150	-	mA
Ісс-в	Total 802.11b Transmit Supply Current	P <sub>OUT</sub> = 21 dBm, 11 Mbps CCK signal, BT = 0.45, ENg = 3.3 V, ENa = 0 V	-	180	-	mA
ICC-A	Total 802.11a Transmit Supply Current	$P_{OUT} = 16 \text{ dBm}, 54 \text{ Mbps}$ OFDM signal, 64 QAM, ENa = 3.3 V, ENg = 0 V	-	220	250	mA
ICC_OFF	Total Supply Current	No RF, ENg = ENa = 0 V	-	2	100	μA



# Logic Characteristics

Conditions: Vcc = 3.3 V, T<sub>A</sub> = 25 °C, as measured on Skyworks Solutions' SE2594L-EV1 evaluation board (deembedded to device), all unused ports terminated with 50 ohms, unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Venh	Logic High Voltage for ENg, ENa (Module On)	-	1.8	-	Vcc	V
Venl	Logic Low Voltage ENg, ENa (Module Off)		$\mathbf{S}$	-	0.5	V
Ienh	Input Current Logic High Voltage (ENg, ENa)			350	400	μA
IENL	Input Current Logic Low Voltage (ENg, ENa)	ON!	-	0.2	-	μA

# Switch Characteristics

Conditions: Vcc = 3.3 V, T<sub>A</sub> = 25 °C, as measured on Skyworks Solutions' SE2594L-EV1 evaluation board (deembedded to device), all unused ports terminated with 50 ohms, unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Vctl_on	Control Voltage (On State)	-	3.0	-	3.6	V
Vctl_off	Control Voltage (OFF State)	-	0.0	-	0.2	V
SWon	Low Loss Switch Control Voltage	High State = Vctl_on - Vctl_off	2.8	-	Vcc	V
SWOFF	High Loss Switch Control Voltage	Low State = VCTL_OFF - VCTL_OFF	0	-	0.3	V
ICTL_ON	Switch Control Bias Current (RF Applied)	On pin (CTx, CRx) being driven high. RF Applied	-	-	100	μA
ICTL_ON	Switch Control Bias Current (No RF)	On pin (CTx, CRx) being driven high. No RF	-	-	30	μA
Сст∟	Control Input Capacitance	-	-	-	100	pF

# Switch Control Logic Table

СТх	CRx	Tg, Ta – ANT	Rg, Ra – ANT	
SWON	SWOFF	ON	OFF	
SWOFF	SWON	OFF	ON	
SWOFF	SWOFF	OFF	OFF	
All Othe	er States	Unsupported Switch State		



# 2.4 GHz AC Electrical Characteristics

### 2.4 GHz Transmit Characteristics

Conditions:	Vcc = 3.3 V, ENg = CTx = 3.3 V, ENa = CRx = 0 V, TA = 25 °C, as measured on Skyworks Solutions'
	SE2594L-EV1 evaluation board, all unused ports terminated with 50 ohms, unless otherwise noted.

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
Fin	Frequency Range	1 6	2400	-	2500	MHz
P802.11g	Output power	54 Mbps OFDM signal, 64QAM, EVM = 3.0 %	/	18	-	dBm
P802.11b	Output power	11 Mbps CCK signal, BT = 0.45 ACPR(±11MHz offset) < -35 ACPR(±22MHz offset) < -55	-	21	-	dBm
P <sub>1dB</sub>	P1dB		23	24	-	dBm
<b>S</b> 21	Small Signal Gain		25	-	30	dB
$\Delta S_{21}$	Small Signal Gain Variation Over Band	$\sim$	-	1.0	2.0	dB
S211.6	Gain at Ref-VCO ÷ 2	1600 MHz	-	-	21	dB
S213.2	Gain at Ref-VCO	3216.00 to 3256.00 MHz 3262.00 to 3263.21 MHz 3269.33 to 3276.00 MHz 3282.67 to 3312.00 MHz	-	-	9 4 9 17	dB
2f,3f	Harmonics	Pout ≤ 21 dBm, 1Mbps, CCK	-	-	-45.2	dBm/MHz
tdr, tdf	Delay and rise/fall Time	50 % of VEN edge and 90/10 % of final output power level	-	-	0.4	μs
S11	Input Return Loss	-	10	15	-	dB
STAB	Stability	CW, Pout = 21 dBm 0.1 GHz – 21 GHz Load VSWR = 6:1	All non-harmonically related outputs less than -42 dBm/MHz			
Ru	Ruggedness	Tg = 12dBm, ANT load varies over 10:1 VSWR, ENg = 0 or 3.3 V	No Irreversible damage			



### 2.4 GHz Receive Characteristics

Conditions: Vcc = 3.3 V, CRx = 3.3 V, ENg = ENa = CTx = 0 V, T<sub>A</sub> = 25 °C, as measured on Skyworks Solutions' SE2594L-EV1 evaluation board (de-embedded to device), all unused ports terminated with 50 ohms, unless otherwise noted.

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
Fout	Frequency Range	. /	2400	-	2500	MHz
RXı∟	Insertion Loss		5	1.5	1.9	dB
RXrl	Return Loss	<u> </u>	10	15	-	dB
TRISOL-2	Rx Leakage	CTx = SWON, CRx = SWOFF, Device transmitting (ENg = 3.3 V) 18.0 dBm @ ANT, Power measured @ RX_OUT	-	-	0	dBm
ANTRISOL	Antenna to Rx isolation	Small signal input into ANT, Device not transmitting, Power measured @ Rg, CTx (Ant to Rx Iso) = SWON, CRx = SWOFF	18	-	28	dB



# **5 GHz AC Electrical Characteristics**

### 5 GHz Transmit Characteristics

Conditions:	Vcc = 3.3 V, ENa and CTx = 3.3 V, ENg = CRx = 0 V, TA = 25 °C, as measured on Skyworks Solutions'
	SE2594L-EV1 evaluation board, all unused ports terminated with 50 ohms, unless otherwise noted.

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
Fin	Frequency Range	/ .	4900	-	5875	MHz
P802.11a	Nominal Output Power	54 Mbps OFDM signal, 64 QAM, EVM = 3.0 %	× /	16.5	-	dBm
P <sub>1dB</sub>	P1dB		21	22.5	-	dBm
<b>S</b> 21	Small Signal Gain		23	-	30	dB
	Small Signal Gain Variat	tion Over 40 MHz Channel	-	-	0.5	dB
<b>ΔS</b> 21	Small Signal Gain Variation Over sub- bands	4.9 – 5.1 GHz 5.15 – 5.7 GHz 5.7 – 5.85 CHz	-	1	3	dB
S211.9	Gain at Ref-VCO ÷ 2	1942 MHz	-	-	14	dB
S213.2	Gain at Ref-VCO	3200 to 3900 MHz	-	-	5.5	dB
2f,3f	Harmonics @16dBm, 54Mbps, 802.11a	4900 – 5850 MHz	-	-	-48.2	dBm/MHz
tdr, tdf	Delay and rise/fall Time	50 % of V <sub>EN</sub> edge and 90/10 % of final output power level	-	-	0.4	μs
S11	Input Return Loss	-	-	6	-	dB
STAB	Stability	64 QAM, Pout = 16 dBm 0.1 GHz – 21 GHz Load VSWR = 6:1	All non-harmonically related outputs less than -42 dBm/MHz			
Ru	Ruggedness	TXa = 12dBm, ANT load varies over 10:1 VSWR, ENa = 0 or 3.3 V	No Irreversible damage			



#### **5 GHz Receive Characteristics**

Conditions: Vcc = 3.3 V, CRx = 3.3 V, ENg = ENa = CTx = 0 V, T<sub>A</sub> = 25 °C, as measured on Skyworks Solutions' SE2594L-EV1 evaluation board (de-embedded to device), all unused ports terminated with 50 ohms, unless otherwise noted.

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
Fout	Frequency Range	. /	4900	-	5875	MHz
RXı∟	Insertion Loss		5	2	2.5	dB
RXrl	Return Loss	<u> </u>	10	15	-	dB
TALEAK	Tx Power Leakage	Pout = 16 dBm, ENa = 3.3 V, CTx = 3.3 V CRx = 0 Y	_	-	0	dBm
ATTa	Antenna to Rx isolation	Small signal input into ANT, Device not transmitting, Power measured @ RXRF, CTx (Ant to Rx Iso) = SWON, CRx = SWOFF	16	-	27	dB



# 2.4 GHz Power Detector Characteristic

Conditions: Vcc = ENg =CTx = 3.3 V, CRx = 0 V, T<sub>A</sub> = 25 °C, as measured on Skyworks Solutions' SE2594L-EV1 evaluation board (de-embedded to device), all unused ports terminated with 50 Ω, unless otherwise noted.

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
Fout	Frequency Range	/	2400	-	2500	MHz
PDR	Power detect range, peak power	Measured at ANT	<b>~</b> _	-	22	dBm
PDZout	DC Output impedance			2400	-	Ω
PDV <sub>P21</sub>	Output Voltage, Pour = 21dBm	/ MI	-	0.85	-	V
PDV <sub>p18</sub>	Output Voltage, Pour = 18dBm		-	0.68	-	V
PDVpnoRF	Output Voltage, Pour		-	0.30	-	V
LPF-3dB	Power detect low pass filter -3dB corner frequency	Load = high impedance Typ: 1 MΩ	-	1500	-	KHz

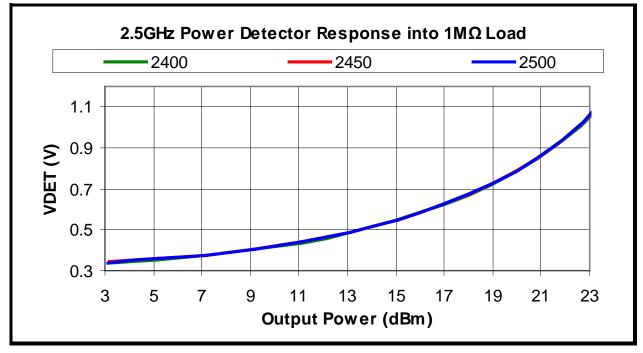


Figure 3: SE2594L Power Detector vs. Output Power over Frequency (CW Signal)



# **5 GHz Power Detector Characteristic**

Conditions:  $V_{CC} = ENa = CTx = 3.3 V$ , CRx = 0 V,  $T_A = 25 °C$ , as measured on Skyworks Solutions' SE2594L-EV1 evaluation board (de-embedded to device), all unused ports terminated with 50  $\Omega$ , unless otherwise noted.

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
Fout	Frequency Range	/ /	4900	-	5850	MHz
PDR	Power detect range, peak power	Measured at ANT	0	-	21	dBm
PDZout	DC Output impedance		<u> </u>	2400	-	Ω
PDV <sub>p18</sub>	Output Voltage, Pour = 18dBm		-	0.80	-	V
PDV <sub>p16</sub>	Output Voltage, Pout = 16dBm	CHEV/	-	0.70	-	V
PDVNORF	Output Voltage, Pour =	$\sim$	-	0.30	-	V
LPF-3dB	Power detect low pass filter -3dB corner frequency	Load = high impedance Typ: 1 MΩ	-	1500	-	KHz

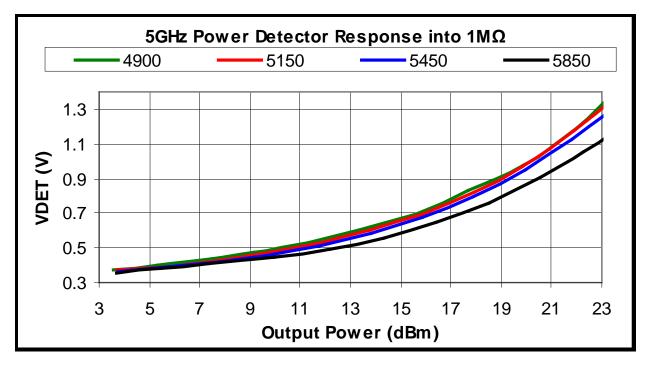


Figure 4: Preliminary SE2594L Power Detector vs. Output Power over Frequency (CW Signal)



# Package Drawing

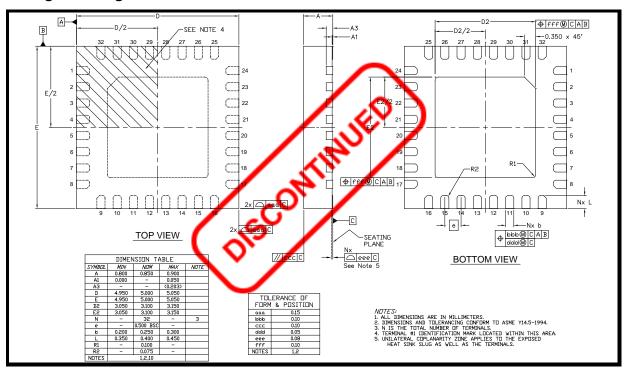
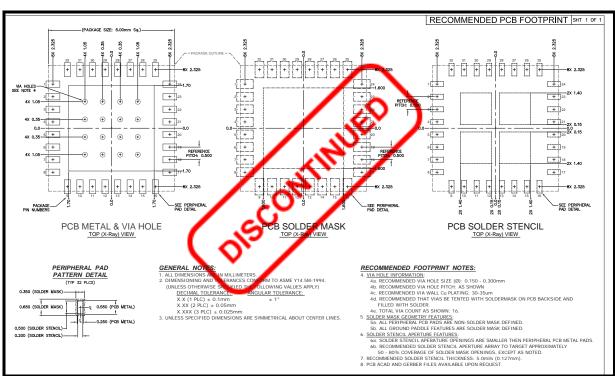


Figure 5: Package Drawing: Topside





**Recommended Land and Solder Patterns** 

Figure 6: Recommended Land and Solder Patterns



# **Package Handling Information**

Because of its sensitivity to moisture absorption, instructions on the shipping container label must be followed regarding exposure to moisture after the container seal is broken, otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly. The SE2594L is capable of withstanding a Pb free solder reflow. Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. If the part is manually attached, precaution should be taken to insure that the device is not subjected to temperatures above its rated peak temperature for an extended period of time. For details on both attachment techniques, precautions, and handling procedures recommended, please refer to:

- "Quad Flat No-Lead Module Solder Reflow & Rework Information", Document Number QAD-00045 "Handling, Packing, Shipping and Use of Moisture Sensitive QFN", Document Number QAD-00044

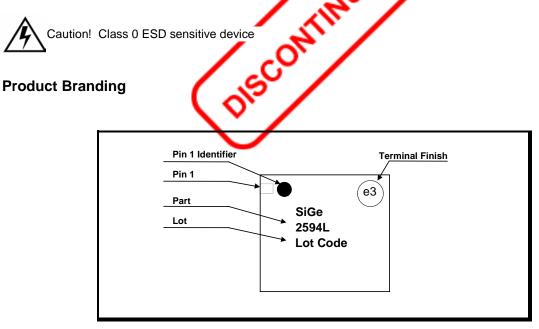
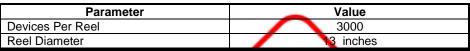


Figure 7: SE2594L Branding Information



#### Tape and Reel Information

Production quantities of this product are shipped in a standard tape-and-reel format. Specific tape and reel dimensions and sizing is shown in Table 1 and Figure .



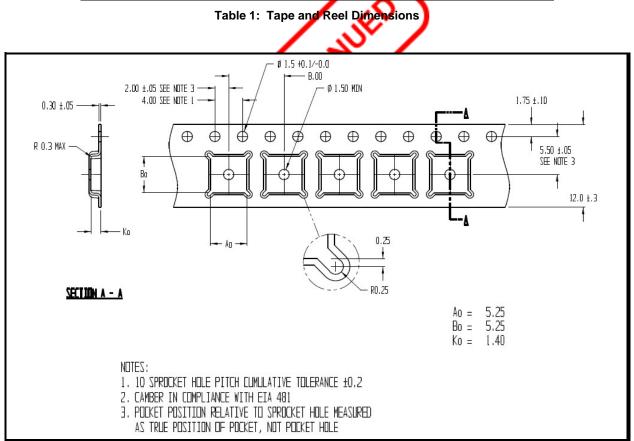


Figure 8: Detailed Tape and Reel Information (All diminensions in Millimeters)



Revision	Date	Notes
1.0	Aug-20-2008	Created
1.1	Apr-9-2009	Added VCC to Pin 13 Corrected terminal finish indicator in Branding Information
1.2	Dec-8-2009	Updated per Design Validation Test.
1.3	Jan-11-2010	Updated ICC_OFF specification
1.4	Feb-11-2011	Updated for Industrial temperature range
1.5	Jun-30-2011	Corrected Recommended Operating Condition to Industrial Temperature Range
1.6	Mar-20-2012	Corrected top marking terminal finish
1.7	Mar-28-2012	Updated with Skyworks logo and disclaimer statement

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