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#### **PRELIMINARY DATA SHEET**

# SKY13272-340LF: 4 x 2 Switch Matrix 250 MHz–2.15 GHz

#### **Applications**

• DBS switching applications, cable modems, cable TV

#### **Features**

- Four inputs, two outputs
- Any input can be directed to either output
- Only four control lines required
- High interport isolation: 40 dB typ. up to 0.95 GHz
- Miniature QFN-20 4 x 4 mm package
- Available lead (Pb)-free and RoHS-compliant per JEDEC J-STD-020

#### **Description**

The SKY13272-340LF is a four-input to two-output switch in a low-cost QFN-20 4 x 4 mm package. The SKY13272-340LF enables 16 states, directing any of the four inputs to either of the two outputs. States are selected by four positive voltage control inputs. The switch can operate over the temperature range of -40 °C to +85 °C.



Skyworks offers lead (Pb)-free, RoHS (Restriction of Hazardous Substances)-compliant packaging.

#### **Electrical Specifications**

V<sub>CTL</sub> = 0 V/5V, T = 25 °C, P<sub>INPUT</sub> = -20 dBm, Z<sub>0</sub> = 50  $\Omega$ , unless otherwise noted.

Parameter	Conditions	Frequency	Min.	Тур.	Max.	Unit
Insertion loss		0.25–0.95 GHz		7.5	8	dB
		0.95–2.15 GHz		8.5	9	dB
Insertion loss flatness		0.25–0.95 GHz		1	1.5	dB
		0.95–2.15 GHz		1	1.5	dB
Isolation		0.25–0.95 GHz	38	40		dB
		0.95–2.15 GHz	28	31		dB
Return loss	1,  2,  3,  4	0.25–2.15 GHz	10	15		dB
	LO, RO		8	10		dB

### Functional Block Diagram and Pin Out

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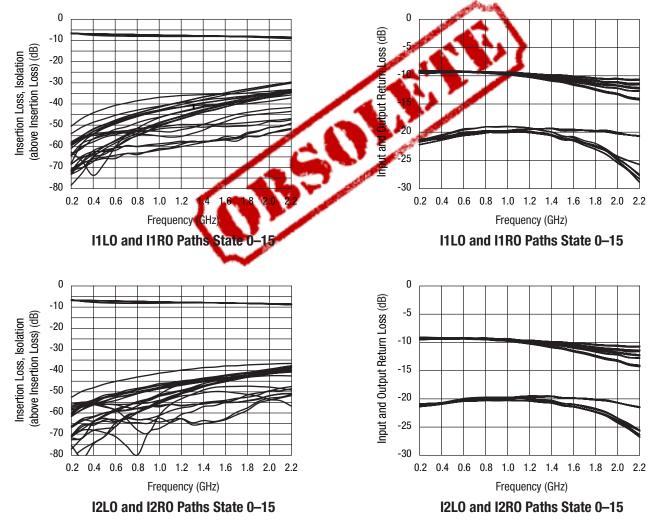
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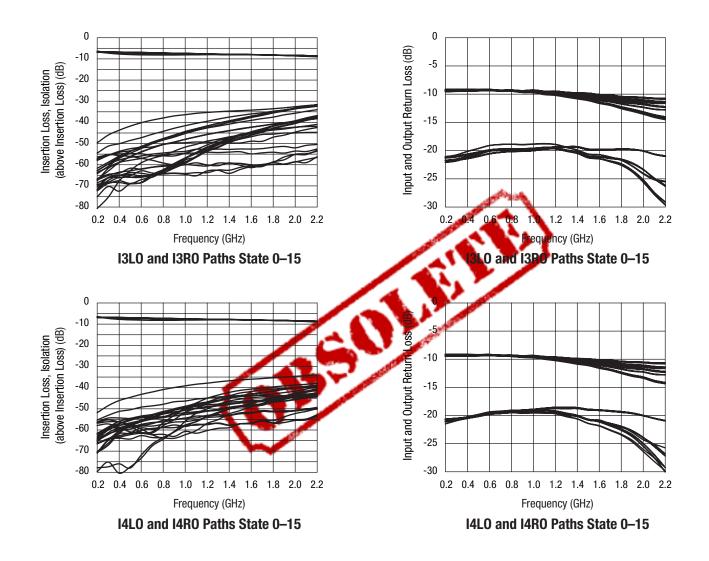
#### **Operating Characteristics**

#### T = 25 °C, Z<sub>0</sub> = 50 $\Omega$ , unless otherwise noted

Parameter	Condition	Frequency Min.		Тур.	Max.	Unit
Input power for 1 dB compression	$V_{CC} = 5 V$	0.25–2.15 GHz		15		dBm
Control voltages	$V_{LOW}=0$ V to 0.2 V @ 50 $\mu A$ maximum $V_{HIGH}=2.5$ V to $V_{CC}$ @ 50 $\mu A$ maximum					

#### **Typical Performance Data**





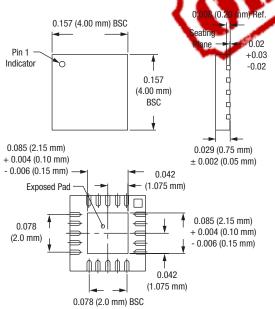
#### **Absolute Maximum Ratings**

Characteristic	Value		
RF input power	15 dBm		
Supply voltage	6 V		
Control voltage	$0 \le V_C \le 6 V$		
Operating temperature	-40 °C to +85 °C		
Storage temperature	-65 °C to +150 °C		

Performance is guaranteed only under the conditions listed in the specifications table and is not guaranteed under the full range(s) described by the Absolute Maximum specifications. Exceeding any of the absolute maximum/minimum specifications may result in permanent damage to the device and will void the warranty.

**CAUTION:** Although this device is designed to be as robust as possible, ESD (Electrostatic Discharge) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions must be employed at all times.

#### QFN-20 (4 x 4)



#### Truth Table<sup>(1)</sup>

		Controls				
		LO (Left Output)		RO (Right Output)		
State	Signal Path (Insertion Loss Path) <sup>(1)</sup>	V <sub>1</sub>	V <sub>2</sub>	V <sub>3</sub>	V4	
0	$11 \rightarrow L0, 11 \rightarrow R0$	0	0	0	0	
1	$\text{I1} \rightarrow \text{L0}, \text{I2} \rightarrow \text{R0}$	0	0	0	1	
2	$I1 \rightarrow L0, I3 \rightarrow R0$	0	0	1	0	
3	$I1 \rightarrow L0, I4 \rightarrow R0$	0	0	1	1	
4	$I2 \rightarrow L0, I1 \rightarrow R0$	0	1	0	0	
5	$12 \rightarrow L0, 12 \rightarrow R0$	0	1	0	1	
6	$12 \rightarrow L0, 13 \rightarrow R0$	0	1	1	0	
7	l2 → L0, 14 → R0	0	1	1	1	
8	I3 $ ightarrow$ LO, I1 $ ightarrow$ RO	1	0	0	0	
9	13 $ ightarrow$ LO, 12 $ ightarrow$ RO	1	0	0	1	
10	I3 → LO, I3 → RO	1	0	1	0	
	I3 $\rightarrow$ L0, I4 $\rightarrow$ R0	1	0	1	1	
12	$14 \rightarrow L0, 11 \rightarrow R0$	1	1	0	0	
13	I4 $\rightarrow$ L0, I2 $\rightarrow$ R0	1	1	0	1	
	$14 \rightarrow L0, 13 \rightarrow R0$	1	1	1	0	
15	$14 \rightarrow L0, 14 \rightarrow R0$	1	1	1	1	

1. All other paths are in isolation state. "1" = 5 V. "0" = 0 V.

#### **Recommended Solder Reflow Profiles**

Refer to the "Recommended Solder Reflow Profile" Application Note.

#### **Tape and Reel Information**

Refer to the "Discrete Devices and IC Switch/Attenuators Tape and Reel Package Orientation" Application Note.



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