## SKYWORKS

## DATA SHEET

## SKY13358-388LF: 0.1-3.0 GHz SP5T Switch

## Applications

- 2G/3G cellular infrastructure


## Features

- Broadband frequency range: 0.1 to 3.0 GHz
- Low insertion loss: 0.8 dB typical @ 2.2 GHz
- High isolation: 24 dB typical @ 2.2 GHz
- CMOS/TTL compatible control voltages: 0/1.5-3.5 V
- Small, QFN (16-pin, $2.3 \times 2.3 \mathrm{~mm}$ ) Pb-free package (MSL $260^{\circ} \mathrm{C}$ per JEDEC J-STD-020)


Figure 1. SKY13358-388LF Block Diagram all applicable legislation and are halogen-free. For additional information, referto Skyworks Definition of Green ${ }^{T M}$, document number SQ04-0074.


Figure 2. SKY13358-388LF Pinout - 16-Pin QFN (Top View)

Table 1. SKY13358-388LF Signal Descriptions

| Pin\# | Name | Description | Pin \# | Name | Description |
| :---: | :--- | :--- | :---: | :--- | :--- |
| 1 | GND | Ground | 9 | GND | Ground |
| 2 | RF1 | RF output port 1. Must be DC-blocked for <br> proper operation. | 10 | RF5 | RF output port 5. Must be DC-blocked for <br> proper operation. |
| 3 | GND | Ground | 11 | GND | Ground |
| 4 | RFC | RF common input (antenna) port. Must be <br> DC-blocked for proper operation. | 12 | RF4 | RF output port 4. Must be DC-blocked for <br> proper operation. |
| 5 | VDD | DC power supply. | 13 | GND | Ground |
| 6 | V1 | DC control voltage 1. See Table 4. | 14 | RF3 | RF output port 3. Must be DC-blocked for <br> proper operation. |
| 7 | V2 | DC control voltage 2. See Table 4. |  | GND | Ground |
| 8 | V3 | DC control voltage 3. See Table 4. | 16 | RF output port 2. Must be DC-blocked for <br> proper operation. |  |

## Functional Description

The SKY13358-388LF is comprised of a CMOS decoder that enables three control lines and five RF ports. T internally connected to a GaAs pHEMT RF switch. Depending on the logic voltage level applied to the control pins, the RFC pin is connected to one of five switched RF outputs (RE1, RF2, RF3 RF4, or RF5) by a low insertion loss path, while maintaining a high isolation path to the alternate ports.

Startup time is defined as the time from when VDD is applied to when the switch is active. Once the startup time has passed, the control voltages V1 and V2 can be applied. RF power should not be applied during the startup time or damage to the device could result.

The recommended startup sequence is:
Step 1: Apply VDD.
Step 2: Apply V1, V2, and V3.
Step 3: Apply RF input.

The device must be turned off in the reverse order.
When VDD is not applied, the device is considered off or inactive. All arms of the switch remain on in this state, creating a poor fiveway power splitter. The return loss of all RF ports is very low in this state. RF should not be applied when VDD is not present and should only be used to conserve current.

## Electrical and Mechanical Specifications

The absolute maximum ratings of the SKY13358-388LF are provided in Table 2. Electrical specifications are provided in Table 3.

Typical performance characteristics of the SKY13358-388LF are illustrated in Figures 3, 4, and 5.
The state of the SKY13358-388LF is determined by the logic provided in Table 4.

Table 2. SKY13358-388LF Absolute Maximum Ratings

| Parameter | Symbol | Minimum | Maximum | Units |
| :---: | :---: | :---: | :---: | :---: |
| Power supply (VDD) | VdD |  | 3.5 | V |
| Control voltage (V1, V2, V3) | Vctl |  | Vdd | V |
| RF input power | Pin |  | +38 | dBm |
| Storage temperature | Tstg | -40 | +125 | ${ }^{\circ} \mathrm{C}$ |
| Operating temperature | Top | -40 | +85 | ${ }^{\circ} \mathrm{C}$ |
| Electrostatic Discharge: <br> Charged Device Model (CDM), Class 4 Human Body Model (HBM), Class 0 Machine Model (MM), Class A | ESD |  | $\begin{gathered} 1000 \\ 150 \\ 100 \\ \hline \end{gathered}$ | $\begin{aligned} & \text { V } \\ & \text { V } \\ & \text { V } \end{aligned}$ |

Note: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may sesult in permanent damage to the device.

CAUTION: Although this device is designed to be as robust as possible, Electrostatic Discharge (ESD) can damage this device. This device must be protected at all times from ESD. Static charges playeasily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection Industry-standard ESD precautions should be used at all times.

Table 3. SKY13358-388LF Electrical Specifications (1 of 2)(Note 1)
( $V_{\mathrm{oD}}=2.5 \mathrm{~V}, \mathrm{~V} 1=\mathrm{V} 2=\mathrm{V} 3=0 / 1.8 \mathrm{~V}$, $\mathrm{Top}_{\mathrm{of}}=+25^{\circ} \mathrm{C}, \mathrm{P}_{\mathrm{N}}=\mathbf{0} \mathrm{dBm}$, Characteristic Impedance $\left[Z_{0}\right]=50 \Omega$, Unless Otherwise Noted)

| Parameter |  | Test Condition | Min | Typical | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| RF Specifications |  |  |  |  |  |  |
| Insertion loss | IL | RFC to RF1/2/3/4/5 <br> 0.1 to 1.0 GHz <br> 1.0 to 2.2 GHz <br> 2.2 to 3.0 GHz |  | $\begin{aligned} & 0.50 \\ & 0.80 \\ & 1.00 \end{aligned}$ | $\begin{aligned} & 0.55 \\ & 0.90 \\ & 1.20 \end{aligned}$ |  |
| Isolation | Iso | RFC to RF $1 / 2 / 3 / 4 / 5$ <br> 0.1 to 1.0 GHz <br> 1.0 to 2.2 GHz <br> 2.2 to 3.0 GHz | $\begin{aligned} & 29 \\ & 23 \\ & 18 \end{aligned}$ | $\begin{aligned} & 30 \\ & 24 \\ & 21 \end{aligned}$ |  | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \end{aligned}$ |
| Return loss | \|S11| | RFC to RF $1 / 2 / 3 / 4 / 5$ <br> 0.1 to 1.0 GHz <br> 1.0 to 2.2 GHz <br> 2.2 to 3.0 GHz | $\begin{aligned} & 18 \\ & 14 \\ & 12 \end{aligned}$ | $\begin{aligned} & 20 \\ & 15 \\ & 13 \end{aligned}$ |  | $\begin{aligned} & \mathrm{dB} \\ & \mathrm{~dB} \\ & \mathrm{~dB} \end{aligned}$ |
| $2^{\text {nd }}$ Order Intermodulation Distortion | IMD2 | $\begin{aligned} & \mathrm{f} 1=1.95 \mathrm{GHz} \\ & @+20 \mathrm{dBm}, \\ & \mathrm{f} 2=0.19 \mathrm{GHz} \\ & @-15 \mathrm{dBm}, \text { worst } \\ & \text { case over phase } \end{aligned}$ |  | -91 | -89 | dBm |
| $3{ }^{\text {rd }}$ Order Intermodulation Distortion | IMD3 | $\begin{aligned} & \mathrm{f} 1=1.95 \mathrm{GHz} \\ & \quad @+20 \mathrm{dBm}, \\ & \mathrm{f} 2=1.76 \mathrm{GHz} \\ & @-15 \mathrm{dBm}, \text { worst } \\ & \text { case over phase } \end{aligned}$ |  | -98 | -95 | dBm |
| 1 dB Input Compression Point | IP1dB | 0.1 to 2.2 GHz | +37.0 | +37.5 |  | dBm |
| $2^{\text {nd }}$ harmonic | 2 fo | $\begin{array}{r} \mathrm{PIN}=+24 \mathrm{dBm} \\ @ 900 \mathrm{MHz} \end{array}$ |  | -77 | -75 | dBc |
| $3{ }^{\text {rd }}$ harmonic | 3 fo | $\begin{array}{r} \mathrm{PIN}=+24 \mathrm{dBm} \\ @ 900 \mathrm{MHz} \end{array}$ |  | -90 | -85 | dBc |

Table 3. SKY13358-388LF Electrical Specifications (2 of 2) (Note 1)


| Parameter | Symbol | Test Condition | Min | Typical | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Switching Speed Spectifications |  |  |  |  |  |  |
| Switching characteristics: <br> On time <br> Off time <br> Rise time <br> Fall time |  | $50 \%$ VctL to $90 \%$ RF on $50 \%$ Vctl to $10 \%$ RF off $10 \%$ RF to $90 \%$ RF rise $90 \%$ RF to $10 \%$ RF fall |  | $\begin{aligned} & 0.7 \\ & 0.1 \\ & 0.2 \\ & 0.1 \end{aligned}$ |  | $\begin{aligned} & \mu s \\ & \mu s \\ & \mu s \\ & \mu s \end{aligned}$ |
| Startup time (wait time required from when Vod is applied until control voltage can be applied) |  |  |  | 25 |  | $\mu \mathrm{s}$ |
| DC Specifications |  |  |  |  |  |  |
| ```Control voltage (V1, V2, V3): high low``` | Vcti |  | $1.5$ | 1.8 | $\begin{aligned} & V_{D D} \\ & 0.2 \end{aligned}$ | $\begin{aligned} & V \\ & V \end{aligned}$ |
| Control current: high low | Icc | $\begin{aligned} & V 1 / 2 / 3=1.5 \mathrm{~V} \text { to } V_{D D} \\ & V 1 / 2 / 3=0 \text { to } 0.2 \mathrm{~V} \end{aligned}$ |  | $\begin{aligned} & 0.01 \\ & 0.01 \\ & \hline \end{aligned}$ |  | $\mu \mathrm{A}$ <br> $\mu \mathrm{A}$ |
| Supply voltage | Vdd |  | 1.80 | 2.85 | 3.50 | V |
| Supply current | IDD |  |  | 65 | 125 | $\mu \mathrm{A}$ |
| Note 1: Performance is guaranteed only under the con | tions listed in |  |  |  |  |  |

## Typical Performance Characteristics




Figure 3. Insertion Loss vs Frequency


Figure 4. Average Isolation vs Frequency

Figure 5. Return Loss vs Frequency

Table 4. SKY13358-388LF Truth Table

| V1 <br> (Pin 6) | V2 <br> (Pin 7) | V3 <br> (Pin 8) | RFC (Pin 4) to <br> RF1 (Pin 2) | RFC (Pin 4) to <br> RF2 (Pin 16) | RFC (Pin 4) to <br> RF3 (Pin 14) | RFC (Pin 4) to <br> RF4 (Pin 12) | RFC (Pin 4) to <br> RF5 (Pin 10) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | 0 | 1 | Insertion loss | Isolation | Isolation | Isolation | Isolation |
| 1 | 1 | 0 | Isolation | Insertion loss | Isolation | Isolation | Isolation |
| 1 | 0 | 0 | Isolation | Isolation | Insertion loss | Isolation | Isolation |
| 0 | $X$ | 0 | Isolation | Isolation | Isolation | Insertion loss | Isolation |
| 0 | $X$ | 1 | Isolation | Isolation | Isolation | Isolation | Insertion loss |

Note: $\quad$ "1" $=+1.5 \mathrm{~V}$ to VDD
" 0 " = 0 V to +0.2 V
" $X$ " = don't care

## Evaluation Board Description

The SKY13358-388LF Evaluation Board is used to test the performance of the SKY13358-388LF SP5T Switch. An Evaluation Board schematic diagram is provided in Figure 6. An assembly drawing for the Evaluation Board is shown in Figure 7.

## Packageand Handling Information

Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The SKY13358-388LF is rated to Moisture Sensitivity Level 1 (MSL1) at $260^{\circ} \mathrm{C}$. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, Solder Reflow Information, document number 200164.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.


Figure 6. SKY13358-388LF Evaluation Board Schematic

Table 6. SKY13358-388LF Evaluation Board Bill of Materials

| Component | Value | Size | Manufacturer | Characteristic |
| :---: | :---: | :---: | :--- | :--- |
| CBL | 56 pF | 0402 | Murata GRM Series | DC blocking capacitor |
| CBYP | 100 pF | 0402 | Murata GRM Series | Decoupling capacitor |



Figure 7. SKY13358-388LF Evaluation Board Assembly Diagram


Figure 8. SKY13358-388LF PCB Layout Footprint (Top View)


All measurements are in millimeters

Figure 10. SKY13358-388LF 16-Pin QFN Package Dimensions


## Ordering Information

| Model Name | Manufacturing Part Number | Evaluation Board Part Number |
| :--- | :--- | :--- |
| SKY13358-388LF SP5T Switch | SKY13358-388LF | SKY13358-388LF-EVB |



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