

DATA SHEET

SKY13523-639LF: 0.7 to 3.0 GHz High-Isolation SP3T Switch

Applications

• CDMA/WCDMA/TD-SCDMA/LTE single-ended filter switching

Features

- Broadband frequency range: 0.7 to 3.0 GHz
- High isolation: >45 dB @ 2.0 GHz
- Low insertion loss: 0.5 dB typical @ 2.0 GHz
- Small QFN (14-pin, 1.6 x 1.6 mm) package (MSL1, 260 °C per JEDEC J-STD-020)



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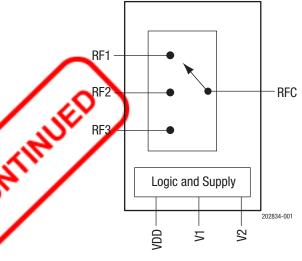


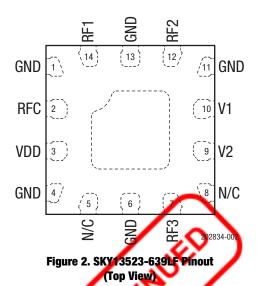
Figure 1. SKY13523-639LF Block Diagram

Description

The SKY13523-639LF is a single pole, triple-throw (SP3T) switch used for single-ended filter switching in cellular applications. The SKY13523-639LF maintains low insertion loss and high isolation for all switching paths.

Depending on the logic voltage level applied to the control pins (V1 and V2), the RFC pin is connected to one of three switched RF outputs (RF1 to RF3) using a low insertion loss path, while the paths between the RFC pin and the other RF pins are in a high isolation state.

The SKY13523-639LF is manufactured in a compact, 14-pin 1.6 x 1.6 mm, Quad Flat No-Lead (QFN) package. A functional block diagram is shown in Figure 1. The pin configuration and package are shown in Figure 2. Signal pin assignments and functional pin descriptions are provided in Table 1.



RF input/output port 3. DC blocking capacitor required.

Pin	Name	Description		Name	Description
1	GND	Ground	8	N/C	Not connected
2	RFC	Antenna port. No DC blocking capacitor required.	9	V2	Control voltage 2
3	VDD	DC power supply	10	V1	Control voltage 1
4	GND	Ground	11	GND	Ground
5	N/C	Not connected	12	RF2	RF input/output port 2. DC blocking capacitor required.

13

14

GND

RF1

Ground

Table 1. SKY13523-639LF Signal Descriptions¹

Functional Description

GND

6

The SKY13523-639LF includes an internal decoder and internal blocking capacitors on the RF common ports. External DC blocking capacitors are required on the RF1, RF2, and RF3 ports for proper operation. DC decoupling capacitors may be added on the VDD and control lines if necessary.

Switching is controlled by two control voltage inputs, V1 and V2. Depending on the logic voltage level applied to the control pins, the antenna pin is connected to one of three switched RF outputs.

Electrical and Mechanical Specifications

Ground

The absolute maximum ratings of the SKY13523-639LF are provided in Table 2. Electrical specifications are provided in Table 3.

RF input/output port 1. DC blocking capacitor required.

The state of the SKY13523-639LF is determined by the logic shown in Table 4.

Bottom ground paddles must be connected to ground.

Table 2. SKY13523-639LF Absolute Maximum Ratings¹

Parameter	Symbol	Minimum	Maximum	Units
Supply voltage	V _{DD}		3.7	V
Control voltage (V1,V2)	Vctl	-0.5	+3.3	V
RF input power	Pin		+26	dBm
Operating temperature	Тор	-30	+90	°C
Storage temperature	Тѕтс	- 55	+150	°C

¹ Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

ESD HANDLING: Although this device is designed to be as robust as possible, electrostatic discharge (ESD) can damage this device.

This device must be protected at all times from ESD when handling or transporting. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection.

Industry-standard ESD handling precautions should be used at all times.

Table 3. SKY13523-639LF General Electrical Specifications¹ (VDD = 3.3 V, V1 = V2 = High = 1.8 V, PIN = 0 dBm, Top = +25°C, Characteristic Impedance [Zo] = 50 Ω , Unless Otherwise Noted)

Parameter	Symbol	Test Condition (Note 2)	Min	Typical	Max	Units
DC Specifications						
Supply voltage	VDD		2.5		3.3	V
Supply current	loo	VDD = 3.3 V		5	10	μΑ
Control voltage: High Low	VCTL_H VCTL_L		1.35 0	1.80	3.30 0.45	V V
Control current	Ість	V1 = V2 = 1.8 V, VDD = 3.3 V			5	μΑ
RF Specifications						
Insertion loss (RFC pin to RF1/2/3 pins)	IL	704 to 960 MHz 1710 to 2170 MHz 2300 to 2690 MHz		0.65 0.75 0.8	0.85 0.95 1.0	dB dB dB
Isolation (RFC pin to RF1/2/3 pins)	Iso	704 to 960 MHz 1710 to 2170 MHz 2300 to 2690 MHz	47 45 45	50 48 48		dB dB dB
Voltage standing wave ratio, all ports	VSWR	0.7 to 3.0 GHz, referenced to 50 Ω		1.4		-
0.1 dB input compression point (RFC pin to RF1/2/3 pins)	IP0.1dB	0.7 to 3.0 GHz		+32		dBm
Third order input intercept point	IIP3	0.7 to 3.0 GHz, all RF ports, fo = 0.8 to 3.0 GHz, $\Delta f = 1$ MHz, $P_{IN} = +20$ dBm/tone		+56		dBm
Turn-on/turn-off time		Measured from 50% of final VDD supply voltage to final RF power ±1 dB		400	550	ns
Switching speed		Measured from 50% of final VCTRL voltage to final RF power ±1 dB		500	650	ns

Performance is guaranteed only under the conditions listed in this table.

Table 4. SKY13523-639LF Control Logic¹

VDD (Pin 3)	V1 (Pin 10)	V2 (Pin 9)	Insertion Loss Path
1	1	0	RFC to RF1
1	0	0	RFC to RF2
1	0	1	RFC to RF3

^{1 &}quot;1" = 1.8 V; "0" = 0 V. Any state other than that described in this table places the switch into an undefined state. An undefined state will not damage the device.

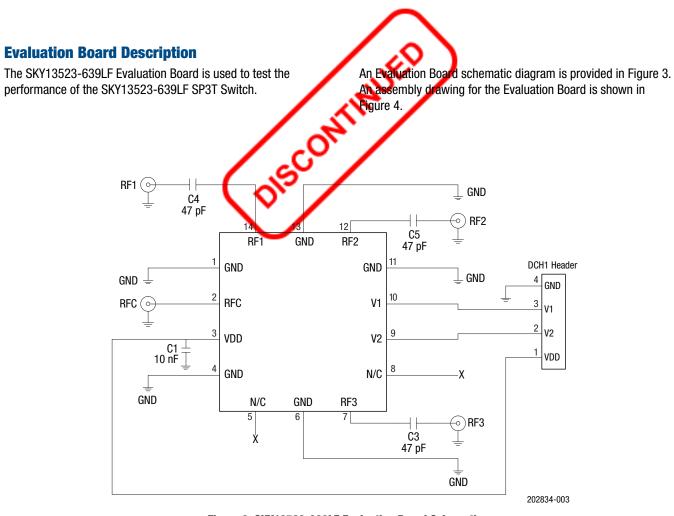


Figure 3. SKY13523-639LF Evaluation Board Schematic

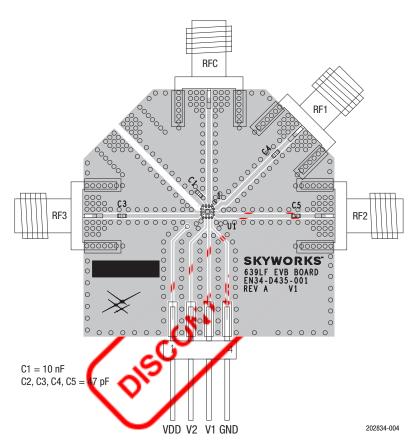


Figure 4. SKY13523-639LF Evaluation Board Assembly Diagram

Package Dimensions

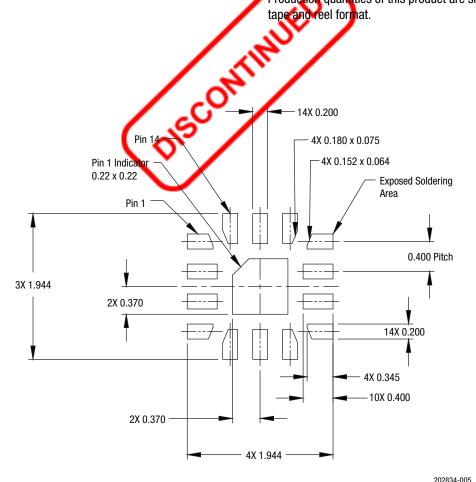
The PCB layout footprint for the SKY13523-639LF is provided in Figure 5. Typical part markings are shown in Figure 6. Package dimensions are shown in Figure 7, and tape and reel dimensions are provided in Figure 8.

Package and Handling Information

Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The SKY13523-639LF is rated to Moisture Sensitivity Level 1 (MSL1) at 260 °C. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, *Solder Reflow Information*, document number 200164.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.



All dimensions are in millimeters

Figure 5. SKY13523-639LF PCB Layout Footprint (Top View)

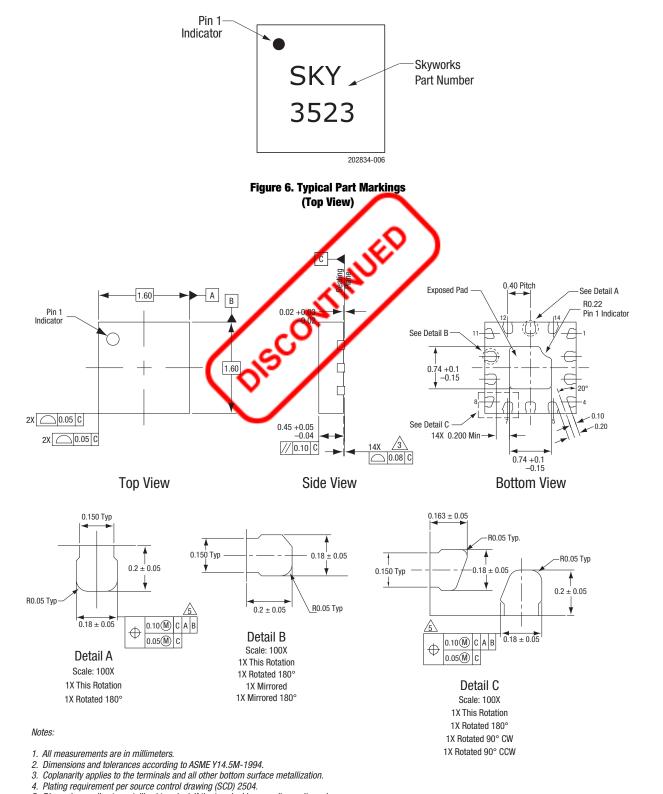


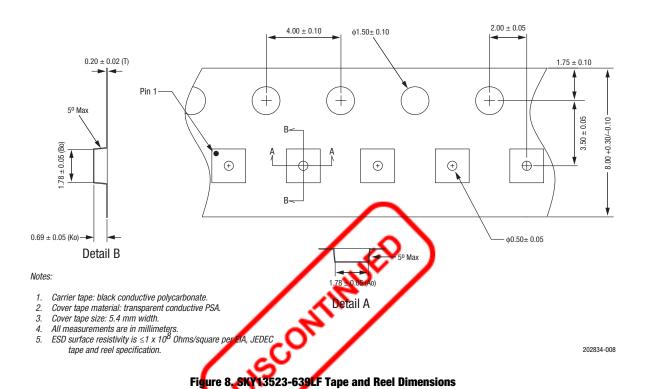
Figure 7. SKY13523-639LF Package Dimensions

5. Dimension applies to metallized terminal. If the terminal has a radius on its end,

the width dimension should not be measured in that radius area.

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Ordering Information

Model Name	Manufacturing Part Number	Evaluation Board Part Number	
SKY13523-639LF: 0.7 to 3.0 GHz High Isolation SP3T Switch	SKY13523-639LF	SKY13523-639LF-EVB	



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