

#### **DATA SHEET**

# SKY66403-11: 2.4 GHz ZigBee®/Thread/Bluetooth® Front-End Module

## **Applications**

- In-home appliances
- · Smart thermostats
- Internet of Things (IoT) devices
- Smart lighting
- Sensors
- Range extender
- Wireless audio

#### **Features**

- Integrated PA with up to +21 dBm output power
- Integrated LNA (2 dB noise figure typical) and bypass path
- · Integrated antenna diversity switching for all modes
- Single-ended transmit/receive interface
- Fast switch on/off time: < 800 ns
- Supply range: 1.8 V to 3.6 V
- Sleep mode current: < 1 μA typical
- · No external bias resistor is required
- Small MCM (22-pin, 3.5 mm x 3.0 mm x 1.0 mm) package, NiPdAu-plated (MSL3, 260 °C per JEDEC-J-STD-020)



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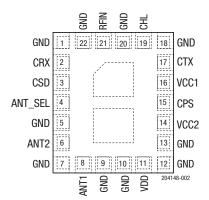


Figure 2. SKY66403-11 Pinout (Top View)

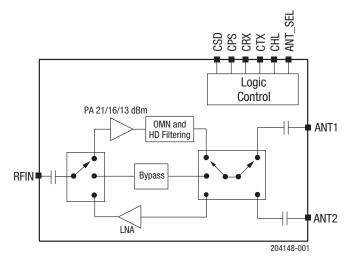


Figure 1. SKY66403-11 Block Diagram

## **Description**

The SKY66403-11 is a high-performance, fully integrated RF front-end module (FEM) designed for ZigBee, Thread, and Bluetooth (including Low Energy) applications.

The SKY66403-11 is designed for ease of use and maximum flexibility. The device provides an integrated inter-stage matching and harmonic filter, and digital controls compatible with 1.6 V to 3.6 V CMOS levels.

The RF blocks operate over a wide supply voltage range from 1.8 V to 3.6 V that allows the SKY66403-11 to be used in battery powered applications over a wide spectrum of the battery discharge curve.

A functional block diagram is shown in Figure 1. The SKY66403-11 is provided in a small, 22-pin, 3.5 mm x 3.0 mm Multi-Chip Module (MCM) package. The pin configuration and package are shown in Figure 2. Signal pin assignments and functional pin descriptions are provided in Table 1.

Table 1. SKY66403-11 Signal Descriptions<sup>1</sup>

Pin	Name	Description	Pin	Name	Description
1	GND	Ground	12	GND	Ground
2	CRX	Connect to GPIO signal for mode control (see Table 6)	13	GND	Ground
3	CSD	Connect to GPIO signal for mode control (see Table 6)	14	VCC2	PA output stage supply
4	ANT_SEL	Connect to GPIO signal to control antenna switch (see Table 7)	15	CPS	Connect to GPIO signal for mode control (see Table 6)
5	GND	Ground	16	VCC1	PA first stage and LNA supply
6	ANT2	Connect to 50 $\Omega$ antenna	17	CTX	Connect to GPIO signal for mode control (see Table 6)
7	GND	Ground	18	GND	Ground
8	ANT1	Connect to 50 $\Omega$ antenna	19	CHL	Connect to GPIO signal for mode control (see Table 6)
9	GND	Ground	20	GND	Ground
10	GND	Ground	21	RFIN	RF input power (transmit/receive port)
11	VDD	Digital logic and RF switch supply	22	GND	Ground

<sup>&</sup>lt;sup>1</sup> The paddle should be connected to ground.

# **Electrical and Mechanical Specifications**

The absolute maximum ratings of the SKY66403-11 are provided in Table 2. The recommended operating conditions are specified in Table 3, and electrical specifications are provided in Tables 4 and 5.

The state of the SKY66403-11 is determined by the logic provided in Table 6. Table 7 shows the antenna select logic.

Table 2. SKY66403-11 Absolute Maximum Ratings<sup>1</sup>

Parameter	Symbol	Minimum	Maximum	Units
Supply voltage	Vcc1 Vcc2	-0.3 -0.3	+3.6 +3.6	V
	VDD	-0.3	+3.6	v
Control pin voltages	VCTL	-0.3	+3.6	V
Transmit output power at ANT1 or ANT2 port into 50 $\Omega$ load	Pout_tx_max		+22.5	dBm
Transmit input power at RFIN port	PIN_TX_MAX		+5.0	dBm
Receive input power at ANT1 or ANT2 ports (Note 2)	PIN_RX_MAX		+15	dBm
Bypass input power at ANT1 or ANT2 ports (Note 2)	PIN_BYP_MAX		+20	dBm
Operating temperature	Та	-40	+85	°C
Storage temperature	Тѕтс	-40	+125	°C
Electrostatic discharge:	ESD			
Human Body Model (HBM)			3000	V

<sup>1</sup> Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

ESD HANDLING: Although this device is designed to be as robust as possible, electrostatic discharge (ESD) can damage this device.

This device must be protected at all times from ESD when handling or transporting. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection.

Industry-standard ESD handling precautions should be used at all times.

<sup>&</sup>lt;sup>2</sup> CW test signal.

**Table 3. Recommended Operating Conditions** 

Parameter	Symbol	Min	Тур	Max	Units
Supply voltage on VCC1 pin	Vcc1	1.7	1.8	3.6	V
Supply voltage on VCC2 pin	Vcc2	0.6	3.0	3.6	V
Supply voltage on VDD pin	VDD	1.8 <sup>1</sup>	3.0	3.6	V
Operating temperature	Та	-40	+25	+85	°C

 $<sup>\</sup>overline{\ }^{1}$  Performance at VDD = 1.8 V will be slightly degraded compared to VDD = 2.5 V and above.

Table 4. SKY66403-11 Electrical Specifications<sup>1</sup>

(Vcc1 = 1.8 V, Vcc2 = 3.0 V, VDD =  $\stackrel{\cdot}{3.0}$  V, TA = +25 °C, Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Тур	Max	Units
DC Characteristics						
Total supply current	Icc_tx	Pout = +20 dBm, linear Pout = +20 dBm, high-efficiency Pout = +16 dBm, high-efficiency Pout = +13 dBm, high-efficiency		97 90 60 45		mA mA mA mA
Total supply current	Icc_rx			3.5	6	mA
Total supply current	Ісс_вур			5		μΑ
Sleep supply current	Icc_off	No RF			1	μΑ
Quiescent current	Iccq_tx	Linear mode (CHL high) High-efficiency mode (CHL low)		35 15		mA mA
Logic Characteristics						
Control voltage: High Low	Vih Vil		1.6 0		VDD 0.3	V V
Control current: High Low	Іін Ііс				1.0 1.0	μ <b>Α</b> μ <b>Α</b>
Dual Antenna Switch Characteristics						
Isolation between ANT1 and ANT2 ports	ISO <sub>ANTSW</sub>			-20		dB
ANT1 to ANT2 switching time	tant1_ant2			400		ns

<sup>&</sup>lt;sup>1</sup> Performance is guaranteed only under the conditions listed in this table.

Table 5. SKY66403-11 Electrical Specifications <sup>1</sup> (Vcc1 = 1.8 V, Vcc2 = 3.0 V, VDD = 3.0 V, TA = +25 °C, All Unused Ports Terminated with 50  $\Omega$ , Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Тур	Max	Units
Transmit Characteristics						
Frequency range	f		2400		2483	MHz
Output power at ANT1 or ANT2 port Pout		Linear mode High-efficiency mode High-efficiency mode, Vcc2 = 1.8 V High-efficiency mode, Vcc2 = 1.2 V		+20 +20 +16 +13		dBm dBm dBm dBm
Saturated gain	G_SAT	PIN = -2 dBm, linear mode		22		dB
Small signal gain	S21	Linear mode		22		dB
Saturated output power variation	ΔРоυт	Across all ZigBee channels			1	dBp-p
2 <sup>nd</sup> and 3 <sup>rd</sup> harmonics <sup>2</sup>	2fo, 3fo	Pouτ = +20.0 dBm, IEEE 802.15.4 source			-42	dBm/MHz
Input return loss	S11			-12		dB
Turn-on time <sup>2</sup>	trise	From 50% of CTX edge to 90% of final RF output power		800		ns
Turn-off time <sup>2</sup>	tfall	From 50% of CTX edge to 10% of final RF output power		800		ns
Stability <sup>2</sup> STAB		CW, Pin = 0 dBm, 0.1 GHz to 20 GHz, load VSWR = 6:1	All non-harmonically related outputs < -42 dBm/MHz			
Ruggedness <sup>2</sup> RUG		CW, Pin = 0 dBm, load VSWR = 10:1	No permanent damage			
Receive Characteristics			•			
Frequency range	f		2400		2483	MHz
Receive gain	RX_gain			11		dB
Receive noise figure	NF			2		dB
Third order input intercept point	IIP3			2		dBm
1 dB input compression point	IP1dB		-14	-8		dBm
Input return loss	S11	ANT1 or ANT2 ports		-10		dB
Output return loss	S22			-10		dB
Turn-on time <sup>2</sup>	trise	From 50% of CRX edge to 90% of final RF output power		800		ns
Turn-off time <sup>2</sup>	<b>t</b> fall	From 50% of CRX edge to 10% of final RF output power		800		ns
Bypass Characteristics	•	•	•		•	•
Frequency range	f		2400		2483	MHz
Bypass gain	BYP_gain			-2		dB
Input return loss	S11			-15		dB
Output return loss	S22			-20		dB

<sup>1</sup> Performance is guaranteed only under the conditions listed in this table.

 $<sup>^{\</sup>mbox{2}}$  Not tested in production. Fully characterized and guaranteed by design.

# Table 6. SKY66403-11 Mode Control Logic $^1$ (Vcc1 = 1.8 V, Vcc2 = 3.0 V, Vdd = 3.0 V, TA = +25 $^{\circ}$ C)

Mode	Description	CSD (Pin 3)	CPS (Pin 15)	CRX (Pin 2)	CTX (Pin 17)	CHL (Pin 19)
0	All off (sleep mode) <sup>1</sup>	0	X	X	X	X
1	Receive LNA mode	1	0	1	0	X
2	Transmit linear mode	1	0	X	1	1
3	Transmit high-efficiency mode	1	0	X	1	0
4	Receive bypass mode	1	1	1	0	X
5	Transmit bypass mode	1	1	X	1	X
6	All off (sleep mode)	1	X	0	0	X

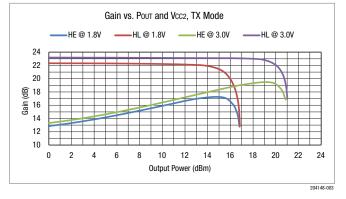
All controls must be at VDD or 0 V to achieve the specified sleep current.

# Table 7. SKY66403-11 Antenna Select Logic (Vcc1 = 1.8 V, Vcc2 = 3.0 V, Vdd = 3.0 V, TA = +25 °C)

Description	ANT_SEL (Pin 4)		
ANT1 port enabled	0		
ANT2 port enabled	1		

#### **CHL Control Pin**

The CHL pin controls the bias of the PA. Use CHL high for high-linearity (HL) mode, which features higher gain and flat gain vs. P<sub>IN</sub>. Use CHL low for high-efficiency (HE) mode, which features slightly lower lcc for a given POUT. Due to ACP requirements, it is necessary to use HL mode for Bluetooth EDR applications.



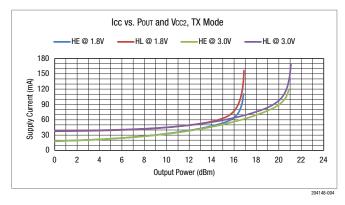


Figure 3. Effect of CHL on Gain (Vcc1 = 1.8 V, Vdd = 3.0 V, f = 2440 MHz)

Figure 4. Effect of CHL on Supply Current (Vcc1 = 1.8 V, VDD = 3.0 V, f = 2440 MHz)

#### **Effect of VDD**

VDD supplies the digital logic and the RF switches. It has a nominal level of 3.0 V and typically draws 5 to 20  $\mu$ A in TX, RX, and bypass modes. Lowering VDD to 1.8 V reduces TX gain by ~0.25 dB and RX gain by ~0.4 dB, but improves RX P1dB by ~0.2 dB.

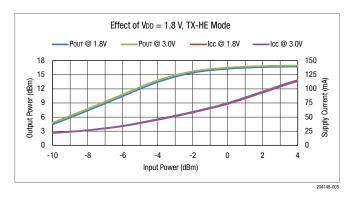


Figure 5. Effect of Lowering VDD (VCC1 = VCC2 = 1.8 V, TX-HE mode, f = 2440 MHz)

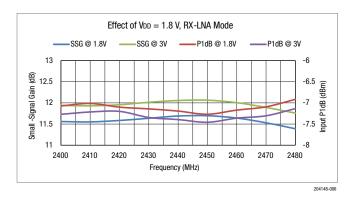
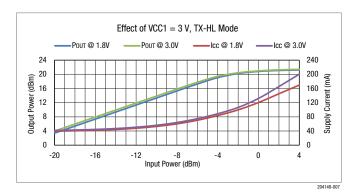


Figure 6. Effect of Lowering VDD (VCC1 = VCC2 = 1.8 V, PIN = -25 dBm, RX-LNA Mode)

#### **Effect of Vcc1**

Vcc1 supplies the LNA and the first stage of the PA. It has a nominal level of 1.8 V and typically draws 10 to 20 mA in TX mode and 3.5 mA in RX mode. Raising Vcc1 to 3.0 V increases RX and TX small-signal gain by ~0.3 dB and RX P1dB by ~1 dB. However, it also increases TX lcc by 1 to 30 mA depending on input power. To avoid high TX lcc, it is recommended to keep PIN at or below -2 dBm.



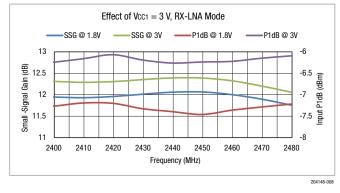
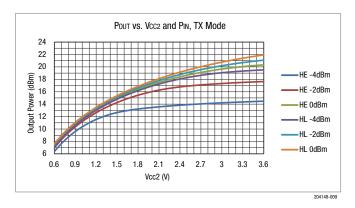


Figure 7. Effect of Raising Vcc1 (Vcc2 = Vdd = 3.0 V, f = 2440 MHz, TX-HL Mode)

Figure 8. Effect of Raising Vcc1 (Vcc2 = Vdd = 3.0 V, Pin = -25 dBm, RX-LNA Mode)

#### **Effect of Vcc2**

Vcc2 supplies the output stage of the PA. The level of Vcc2 directly controls the saturated TX output power and this supply draws the majority of the current in TX mode.



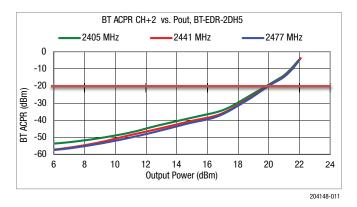
Icc (Total) vs Vcc2 and PIN, TX Mode 140 120 HE -4dBm 100 HE -2dBm Supply current HE 0dBm 60 HL -4dBm HL -2dBm HL 0dBm 2.4 0.9 1.2 1.5 1.8 2.1 2.7 3 3.3 3.6 Vcc2 (V) 204148-010

Figure 9. Pout vs. Vcc2 and PiN (Vcc1 = 1.8 V, Vdd = 3.0 V, f = 2440 MHz)

Figure 10. lcc vs. Vcc2 and PIN (Vcc1 = 1.8 V, Vdd = 3.0 V, f = 2440 MHz)

# **Bluetooth EDR Adjacent Channel Power**

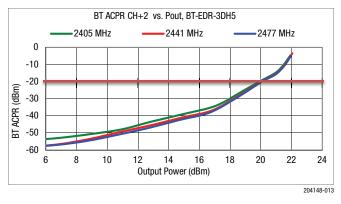
The SKY66403 in high-linearity (HL) mode benefits from excellent adjacent channel power (ACP) performance in Bluetooth EDR applications. Typical ACP measurements for both EDR modulations ( $\pi$ /4-DQPSK with 2-DH5 packets and 8-DPSK with 3-DH5 packets) are shown in Figures 11 through 14.



BT ACPR CH+3 vs. Pout, BT-EDR-2DH5 2405 MHz 2441 MHz 2477 MHz 0 -10 -20 BT ACPR (dBm) -30 -40 -50 -60 6 8 10 12 14 18 20 22 24 Output Power (dBm) 204148-012

Figure 11. BT ACPR CH+2 vs POUT, BT-EDR-2DH5

Figure 12. BT ACPR CH+3 vs POUT, BT-EDR-2DH5



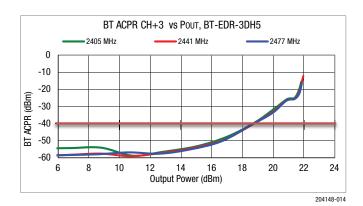
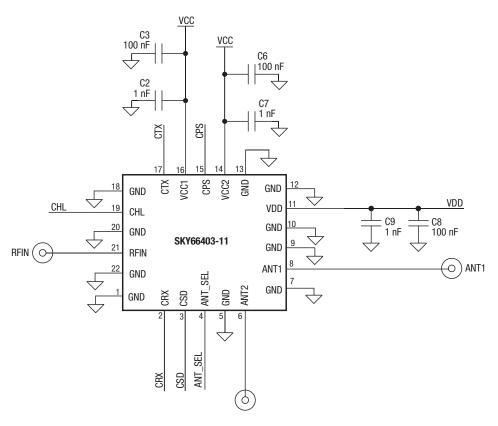


Figure 13. BT ACPR CH+3 vs POUT, BT-EDR-3DH5

Figure 14. BT ACPR CH+2 vs POUT, BT-EDR-3DH5

# **Application Schematic Description**

A reference design schematic is provided in Figure 15. An evaluation board schematic diagram is shown in Figure 16.



Note: The paddle should be connected to ground.

204148-015

Figure 15. SKY66403-11 Reference Design Schematic

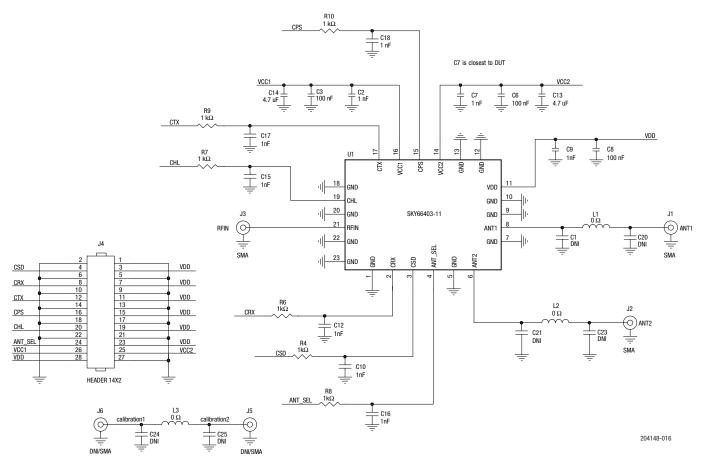


Figure 16. SKY66403-11 Evaluation Board Schematic Diagram

#### **Package Dimensions**

The typical part marking is shown in Figure 17. The PCB layout footprint for the SKY66403-11 is provided in Figure 18. Package dimensions are shown in Figure 19, and tape and reel dimensions are provided in Figure 20.

## **Package and Handling Information**

Since the device package is sensitive to moisture absorption, it is baked and vacuum packed before shipping. Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The SKY66403-11 is rated to Moisture Sensitivity Level 3 (MSL3) at 260 °C. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, *PCB Design and SMT Assembly/Rework Guidelines for MCM-L Packages*, document number 101752.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.

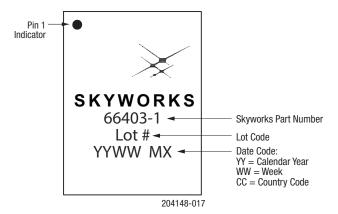
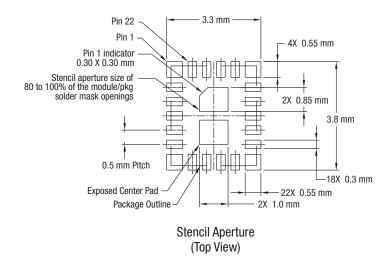
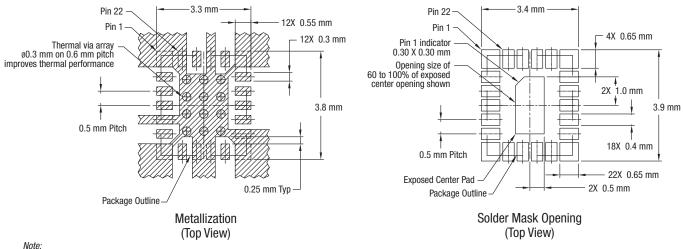


Figure 17. SKY66403-11 Typical Part Marking (Top View)

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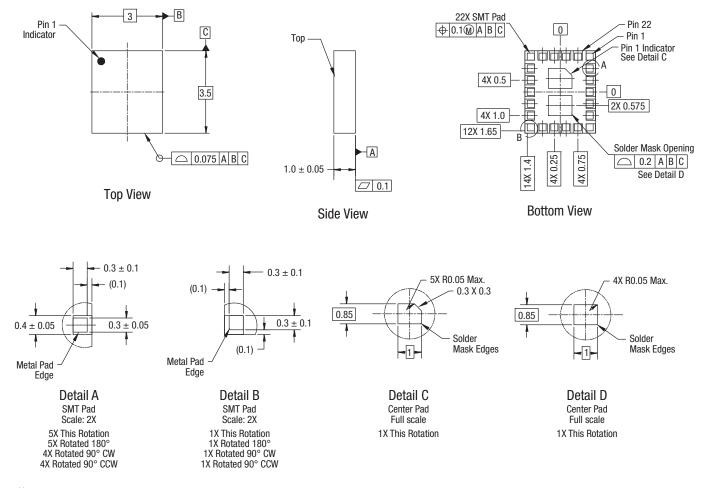




- 1. All measurements are in millimeters.
- 2. Thermal vias should be resin filled and capped in accordance with IPC-4761 type VII vias. Recommended Cu thickness is 30 to 35  $\mu m$ .

204148-018

Figure 18. SKY66403-11 PCB Layout Footprint



#### Notes:

- 1. Dimensions and tolerances according to ASME Y14.5M-1994.
- 2. All measurements are in millimeters.

204148-019

Figure 19. SKY66403-11 Package Dimensions

#### DATA SHEET • SKY66403-11: ZIGBEE/THREAD/BLUETOOTH FEM

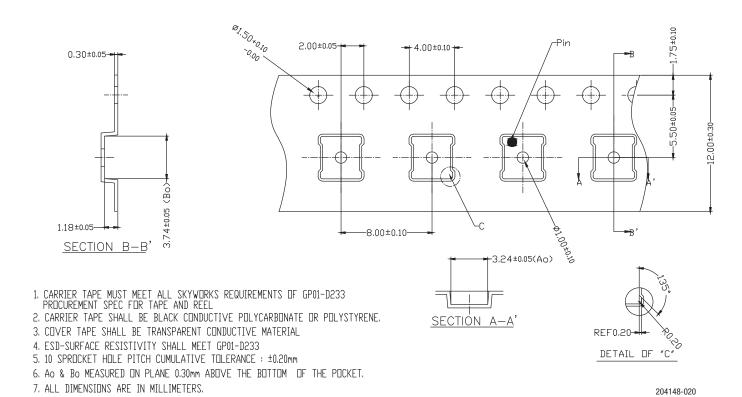


Figure 20. SKY66403-11 Tape and Reel Dimensions

#### **Ordering Information**

Model Name	Manufacturing Part Number	Evaluation Board Part Number	
SKY66403-11: 2.4 GHz Zigbee/Thread/Bluetooth FEM	SKY66403-11	SKY66403-11EK1	

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XB24-PDKJ XBEE-MP-MCRO XBEE-MP-SMT XBEE-MP-TH XBIB-CU-TH XBP09-DMDK XBP24-DKS XK8-DMSB0 XKA2C-Z7TU