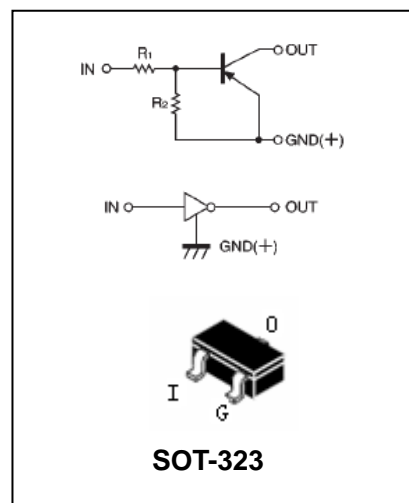


## Digital Transistor

### DTA(R<sub>1</sub>=R<sub>2</sub> SERIES)UA

#### FEATURES

- Epitaxial planar die construction.
- Complementary NPN types available(DTA).
- Built-in biasing resistors,R<sub>1</sub>=R<sub>2</sub>
- Also available in lead free version.



#### APPLICATIONS

- The NPN style digital transistor.

#### ORDERING INFORMATION

Type No.	Marking	Package Code
DTA114EUA	14	SOT-323
DTA143EUA	13	SOT-323
DTA124EUA	15	SOT-323
DTA144EUA	16	SOT-323

#### MAXIMUM RATING @ Ta=25°C unless otherwise specified

Symbol	Parameter	Value	Units	
V <sub>CC</sub>	Supply Voltage	-50	V	
V <sub>IN</sub>	Input Voltage	DTA114EUA DTA124EUA DTA143EUA DTA144EUA	-40 to+10 -40 to+10 -30 to+10 -40 to+10	V
I <sub>o</sub>	Output Current	DTA114EUA DTA124EUA DTA143EUA DTA144EUA	-50 -30 -100 -30	mA
I <sub>C</sub> (Max.)	Output current	ALL	-100	mA
P <sub>D</sub>	Power Dissipation		200	mW
R <sub>θJA</sub>	Thermal Resistance, Junction to Ambient Air		625	°C/W
T <sub>j</sub> , T <sub>stg</sub>	Operating and Storage and Temperature Range		-55 to +150	°C

## ELECTRICAL CHARACTERISTICS @ Ta=25°C unless otherwise specified

Parameter	Symbol	Test conditions	MIN	TYP	MAX	UNIT
Input Voltage	$V_{I(off)}$	$V_{CC}=-5V, I_O=-100\mu A$	-0.5	-1.1	-	V
Input Voltage DTA114EUA DTA124EUA DTA143EUA DTA144EUA	$V_{I(on)}$	$V_O=-0.3V, I_O=-10mA$ $V_O=-0.2V, I_O=-5mA$ $V_O=-0.3V, I_O=-20mA$ $V_O=-0.3V, I_O=-2mA$	-	-1.9	-3	
Output Voltage DTA114EUA DTA124EUA DTA143EUA DTA144EUA	$V_{O(on)}$	$I_O/I_I=-10mA/-0.5mA,$		-0.1	-0.3	V
Input Current DTA114EUA DTA124EUA DTA143EUA DTA144EUA	$I_I$	$V_I=-5V$			-0.88 -0.36 -1.8 -0.18	mA
Output Current	$I_{O(off)}$	$V_{CC}=-50V, V_I=0V$			-0.5	$\mu A$
DC Current Gain DTA114EUA DTA124EUA DTA143EUA DTA144EUA	$G_I$	$V_O=-5V, I_O=-5mA$ $V_O=-5V, I_O=-5mA$ $V_O=-5V, I_O=-10mA$ $V_O=-5V, I_O=-5mA$	30 56 20 68			
Input Resistor DTA114EUA DTA124EUA DTA143EUA DTA144EUA	$R_1(R_2)$		7 15.4 3.29 32.9	10 22 4.7 47	13 28.6 6.11 61.1	k $\Omega$
Resistance Ratio	$R_2/R_1$		0.8	1	1.2	
Gain-Bandwidth Product	$f_T$	$V_{CE}=-10V, I_E=5mA,$ $f=100MHz$	-	250	-	MHz

## TYPICAL CHARACTERISTICS @ Ta=25°C unless otherwise specified

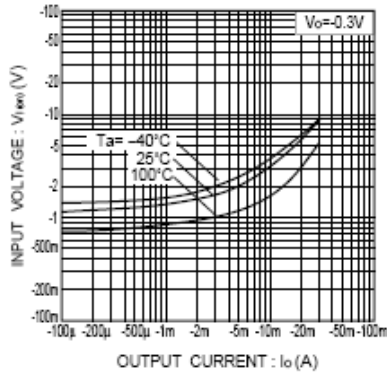


Fig.1 Input voltage vs. output current (ON characteristics)

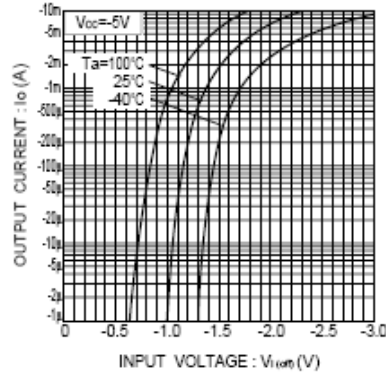


Fig.2 Output current vs. input voltage (OFF characteristics)

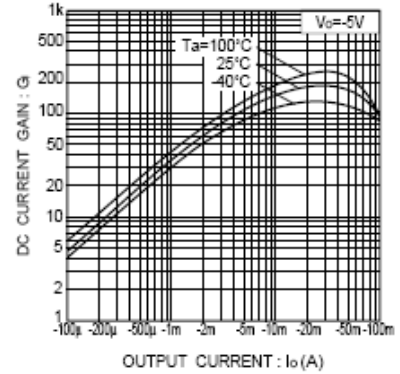


Fig.3 DC current gain vs. output current

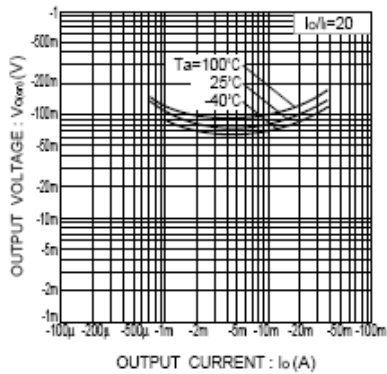
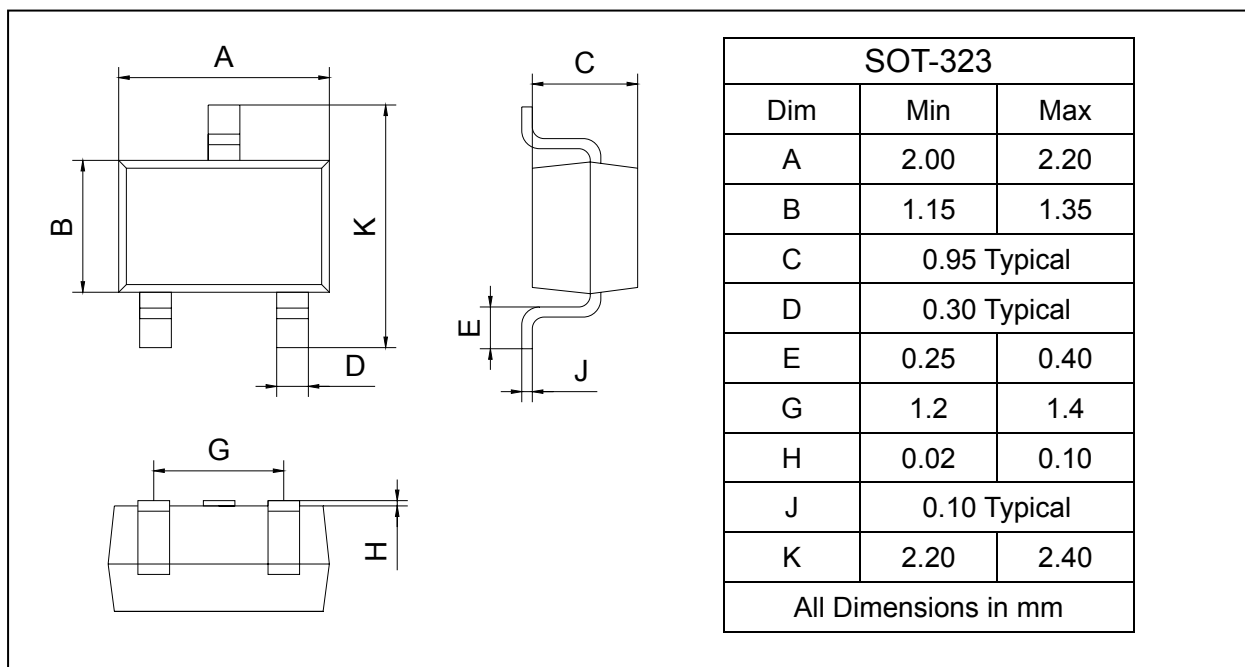


Fig.4 Output voltage vs. output current

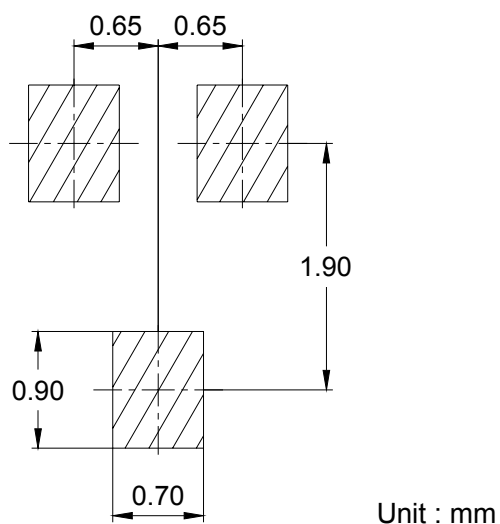
## PACKAGE OUTLINE

Plastic surface mounted package

SOT-323



## SOLDERING FOOTPRINT



## PACKAGE INFORMATION

Device	Package	Shipping
DTC114EUA/124EUA/143EUA/144EUA	SOT-323	3000/Tape&Reel

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[DTC144VUAT106](#) [MUN5241T1G](#) [BCR158WH6327XTSA1](#) [NSBA114TDP6T5G](#) [SMUN5330DW1T1G](#) [SSVMUN5312DW1T2G](#)  
[RN1303\(TE85L,F\)](#) [RN1306\(TE85L,F\)](#) [EMH15T2R](#) [SMUN2214T3G](#) [SMUN5335DW1T1G](#) [NSBC143ZPDP6T5G](#) [NSVDTA143ZET1G](#)  
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[DTC124ECA-TP](#) [DTA114ECA-TP](#) [DTC113EM3T5G](#) [NSVMUN5135DW1T1G](#) [NSVMUN2237T1G](#) [NSVDTC143ZM3T5G](#)  
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[NSVMUN2112T1G](#) [NSVIMD10AMT1G](#) [NSVEMC2DXV5T1G](#) [NSVDTC144WET1G](#) [NSVDTC123JET1G](#) [NSVDTA143EM3T5G](#)  
[NSVB1706DMW5T1G](#) [NSBC143EDP6T5G](#) [RN2101,LF\(CT](#) [NSBA144WDXV6T1G](#) [DTA115TET1G](#) [NSBC115TDP6T5G](#)