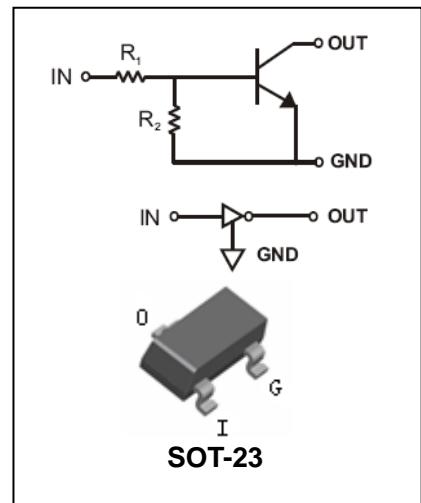


**Digital Transistor****DTC(R<sub>1</sub>=R<sub>2</sub> SERIES)CA****FEATURES**

- Epitaxial planar die construction.
- Complementary PNP types available(DTA).
- Built-in biasing resistors,R<sub>1</sub>=R<sub>2</sub>.
- Also available in lead free version.



Lead-free

**APPLICATIONS**

- The NPN style digital transistor.

**ORDERING INFORMATION**

Type No.	Marking	Package Code
DTC114ECA	24	SOT-23
DTC124ECA	25	SOT-23
DTC143ECA	23	SOT-23
DTC144ECA	26	SOT-23

**MAXIMUM RATING @ Ta=25°C unless otherwise specified**

Symbol	Parameter	Value	Units
V <sub>CC</sub>	Supply Voltage	50	V
V <sub>IN</sub>	Input Voltage DTC114ECA	-10 to +40	V
	DTC124ECA	-10 to +40	
	DTC143ECA	-10 to +30	
	DTC144ECA	-10 to +40	
I <sub>O</sub>	Output Current DTC114ECA	50	mA
	DTC124ECA	30	
	DTC143ECA	100	
	DTC144ECA	100	
I <sub>C</sub> (Max.)	Output current ALL	100	mA
P <sub>D</sub>	Power Dissipation	200	mW
R <sub>θJA</sub>	Thermal Resistance, Junction to Ambient Air	625	°C/W
T <sub>j</sub> , T <sub>stg</sub>	Operating and Storage and Temperature Range	-55 to +150	°C

**ELECTRICAL CHARACTERISTICS @  $T_a=25^\circ\text{C}$  unless otherwise specified**

<b>Parameter</b>		<b>Symbol</b>	<b>Test conditions</b>	<b>MIN</b>	<b>TYP</b>	<b>MAX</b>	<b>UNIT</b>
Input Voltage		$V_{I(\text{off})}$	$V_{CC}=5\text{V}, I_O=100\mu\text{A}$	0.5	1.1	-	
Input Voltage	DTC114ECA	$V_{I(\text{on})}$	$V_O=0.3\text{V}, I_O=10\text{mA}$	-	1.9	3	V
	DTC124ECA		$V_O=0.2\text{V}, I_O=5\text{mA}$				
	DTC143ECA		$V_O=0.3\text{V}, I_O=20\text{mA}$				
	DTC144ECA		$V_O=0.3\text{V}, I_O=2\text{mA}$				
Output Voltage		$V_{O(\text{on})}$	$I_O/I_I=10\text{mA}/0.5\text{mA}$ ,	-	0.1	0.3	V
Input Current	DTC114ECA	$I_I$	$V_I=5\text{V}$	-	-	0.88 0.36 1.8 0.18	mA
	DTC124ECA						
	DTC143ECA						
	DTC144ECA						
Output Current		$I_O(\text{off})$	$V_{CC}=50\text{V}, V_I=0\text{V}$	-	-	0.5	$\mu\text{A}$
DC Current Gain	DTC114ECA	$G_I$	$V_O=5\text{V}, I_O=5\text{mA}$	30			
	DTC124ECA		$V_O=5\text{V}, I_O=5\text{mA}$	56	-	-	
	DTC143ECA		$V_O=5\text{V}, I_O=10\text{mA}$	20			
	DTC144ECA		$V_O=5\text{V}, I_O=5\text{mA}$	68			
Input Resistor	DTC114ECA	$R_1(R_2)$		7	10	13	$\text{k}\Omega$
	DTC124ECA			15.4	22	28.6	
	DTC143ECA			3.29	4.7	6.11	
	DTC144ECA			32.9	47	61.1	
Resistance Ratio		$R_2/R_1$	-	0.8	1	1.2	
Gain-Bandwidth Product		$f_T$	$V_{CE}=10\text{V}, I_E=-5\text{mA}$ , $f=100\text{MHz}$	-	250	-	MHz

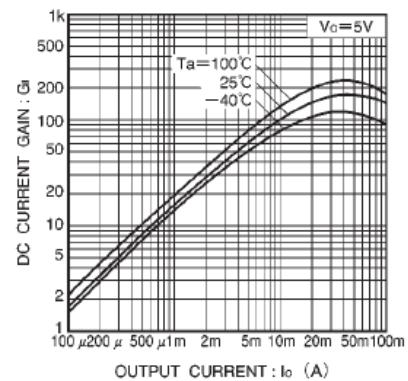
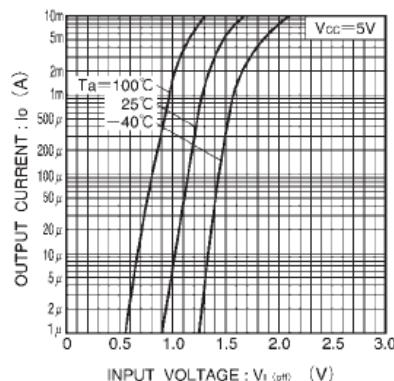
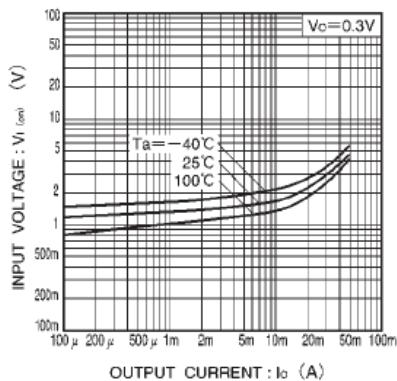
**TYPICAL CHARACTERISTICS @  $T_a=25^\circ\text{C}$  unless otherwise specified**


Fig.1 Input voltage vs. output current (ON characteristics)

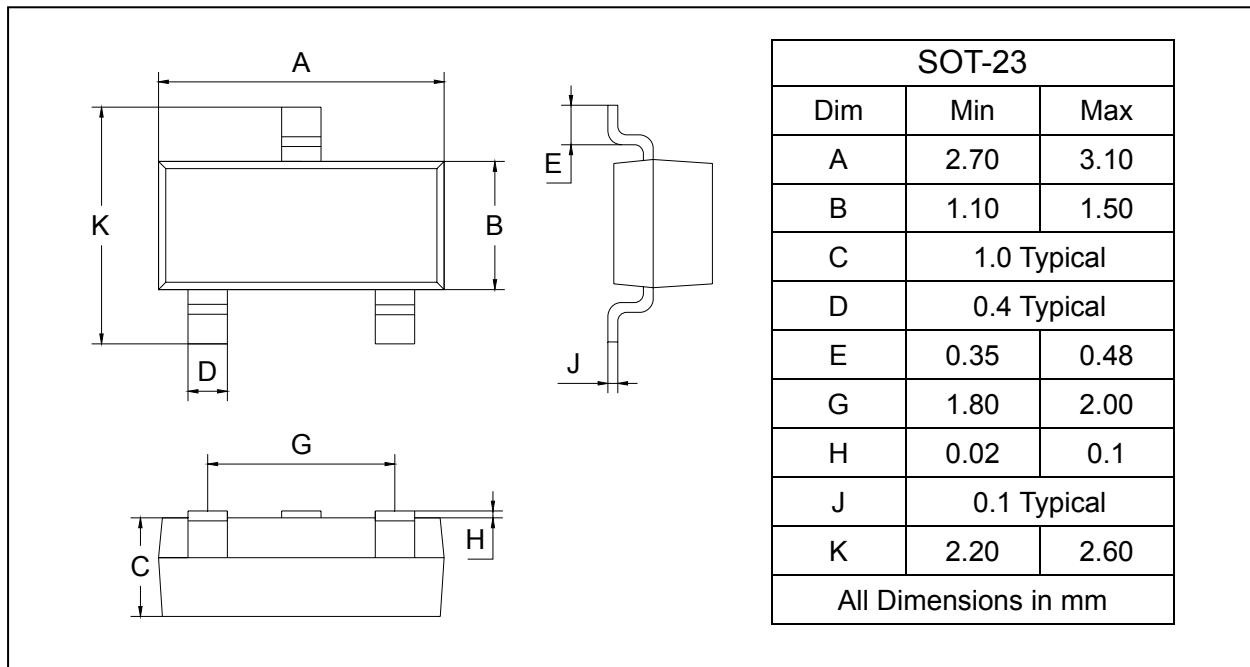
Fig.2 Output current vs. input voltage (OFF characteristics)

Fig.3 DC current gain vs. output current

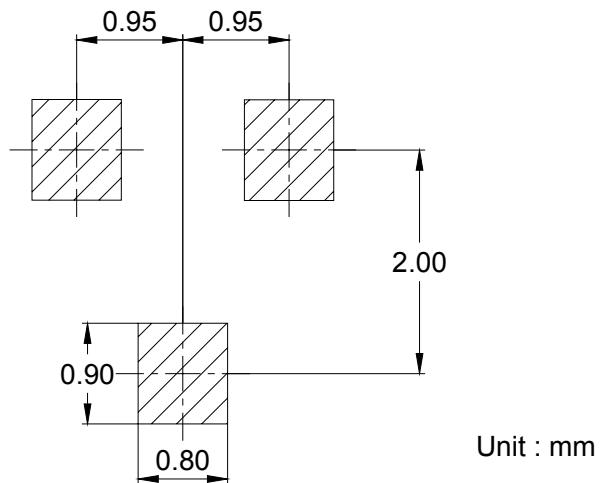
## PACKAGE OUTLINE

Plastic surface mounted package

SOT-23



## SOLDERING FOOTPRINT



## PACKAGE INFORMATION

Device	Package	Shipping
DTC114ECA/124ECA/143ECA/144ECA	SOT-23	3000/Tape&Reel

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[DTC144VUAT106](#) [MUN5241T1G](#) [BCR158WH6327XTSA1](#) [NSBA114TDP6T5G](#) [SMUN5330DW1T1G](#) [SSVMUN5312DW1T2G](#)  
[RN1303\(TE85L,F\)](#) [RN1306\(TE85L,F\)](#) [EMH15T2R](#) [SMUN2214T3G](#) [SMUN5335DW1T1G](#) [NSBC143ZPDP6T5G](#) [NSVDTA143ZET1G](#)  
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