

FEATURES

- The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- For surface mounted applications
- ◆ Low reverse leakage
- Built-in strain relief,ideal for automated placement
- High forward surge current capability
- High temperature soldering guaranteed: 250°C/10 seconds at terminals
- Glass passivated chip junction

MECHANICAL DATA

Case: JEDEC DO-214AA molded plastic body **Terminals**: leads solderable per MIL-STD-750,

Method 2026

Polarity: Color band denotes cathode end

Mounting Position: Any

Weight: 0.003 ounce. 0.093 grams

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified. Single phase half-wave 60Hz, resistive or inductive load, for capacitive load current derate by 20%.

MDD Catalog Number	SYMBOLS	ES3AB	ES3BB	ES3CB	ES3DB	ES3EB	ES3GB	ES3JB	UNITS
Maximum repetitive peak reverse voltage	Vrrm	50	100	150	200	300	400	600	VOLTS
Maximum RMS voltage	Vrms	35	70	105	140	210	280	420	VOLTS
Maximum DC blocking voltage	VDC	50	100	150	200	300	400	600	VOLTS
Maximum average forward rectified current at TL=75°C	l(AV)	3.0							Amps
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	lfsm	100.0							Amps
Maximum instantaneous forward voltage at 3.0A	VF	0.95				1.2	25	1.7	Volts
Maximum DC reverse current Ta=25°C at rated DC blocking voltage Ta=100°C	lR	5.0 100.0							μΑ
Maximum reverse recovery time (NOTE 1)	trr	35							ns
Typical junction capacitance (NOTE 2)	C¹	45.0						pF	
Typical thermal resistance (NOTE 3)	Reja	47.0						°C/W	
Operating junction and storage temperature range	ТЈ,Тѕтс	-50 to +150						°C	

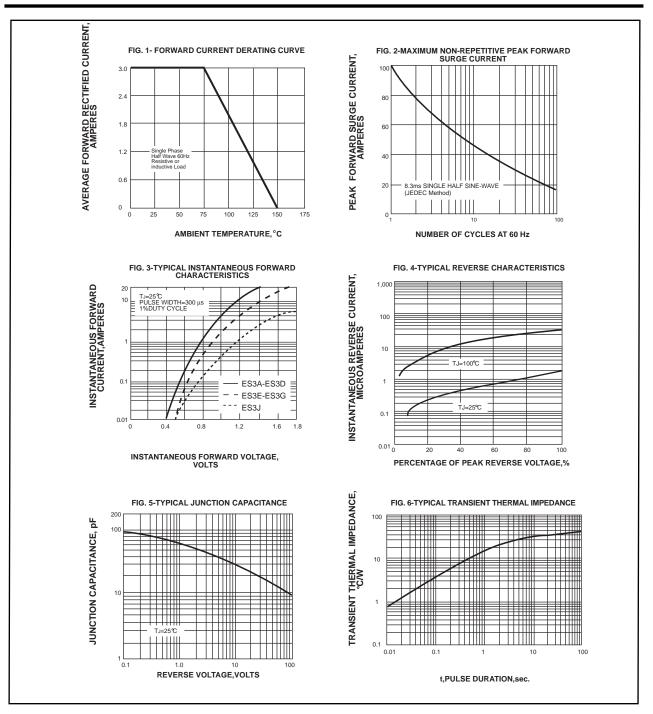
Note:1.Reverse recovery condition IF=0.5A,IR=1.0A,Irr=0.25A

2. Measured at 1MHz and applied reverse voltage of 4.0V D.C.

3.P.C.B. mounted with 0.2x0.2"(5.0x5.0mm) copper pad areas







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