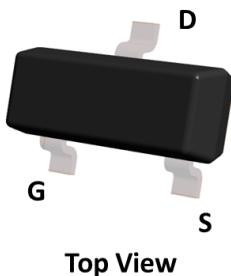
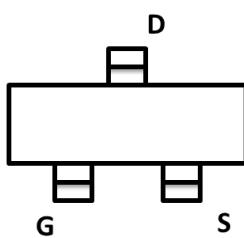


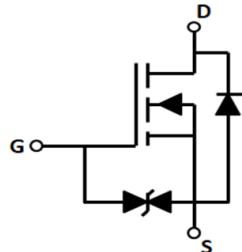
N-Channel Enhancement Mode Field Effect Transistor



Top View



SOT-23



Product Summary

- V_{DS} 30V
- I_D 560mA
- $R_{DS(ON)}$ (at $V_{GS}=4.5V$) <1.5ohm
- $R_{DS(ON)}$ (at $V_{GS}=3.7V$) <2.5ohm

General Description

- Trench Power MV MOSFET technology
- Voltage controlled small signal switch
- Low input Capacitance
- Fast Switching Speed
- Low Input / Output Leakage

Applications

- Battery operated systems
- Solid-state relays
- Direct logic-level interface: TTL/CMOS

■ Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-source Voltage	V_{DS}	30	V
Gate-source Voltage	V_{GS}	± 20	V
Drain Current	I_D	560	mA
Pulsed Drain Current ^A	I_{DM}	1.7	A
Total Power Dissipation @ $T_A=25^\circ\text{C}$	P_D	690	mW
Thermal Resistance Junction-to-Ambient @ Steady State ^B	$R_{\theta JA}$	357	$^\circ\text{C} / \text{W}$
Junction and Storage Temperature Range	T_J, T_{STG}	-55~+150	$^\circ\text{C}$

■ Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	30			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}$			1	μA
Gate-Body Leakage Current	I_{GSS1}	$V_{\text{GS}}= \pm 20\text{V}, V_{\text{DS}}=0\text{V}$			± 1	μA
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	0.8		1.6	V
Static Drain-Source On-Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}= 4.5\text{V}, I_{\text{D}}=10\text{mA}$		1.2	1.5	Ω
		$V_{\text{GS}}= 3.7\text{V}, I_{\text{D}}=10\text{mA}$		1.9	2.5	
Diode Forward Voltage	V_{SD}	$I_{\text{S}}=300\text{mA}, V_{\text{GS}}=0\text{V}$			1.2	V
Maximum Body-Diode Continuous Current	I_{S}				100	mA
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$		17.5		pF
Output Capacitance	C_{oss}			11.5		
Reverse Transfer Capacitance	C_{rss}			6.5		
Switching Parameters						
Total Gate Charge	Q_{g}	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=25\text{V}, I_{\text{D}}=0.3\text{A}$		1.7	2.4	nC
Turn-on Delay Time	$t_{\text{D(on)}}$	$V_{\text{GS}}=10\text{V}, V_{\text{DD}}=25\text{V}, I_{\text{D}}=100\text{mA}, R_{\text{GEN}}=6\Omega$		5		ns
Turn-off Delay Time	$t_{\text{D(off)}}$			17		
Reverse recovery Time	t_{rr}	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=300\text{mA}, V_{\text{R}}=25\text{V}, dI_{\text{S}}/dt=-100\text{A}/\mu\text{s}$		30		ns

A. Pulse Test: Pulse Width $\leq 300\text{us}$, Duty cycle $\leq 2\%$.

B. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.

■ Typical Performance Characteristics

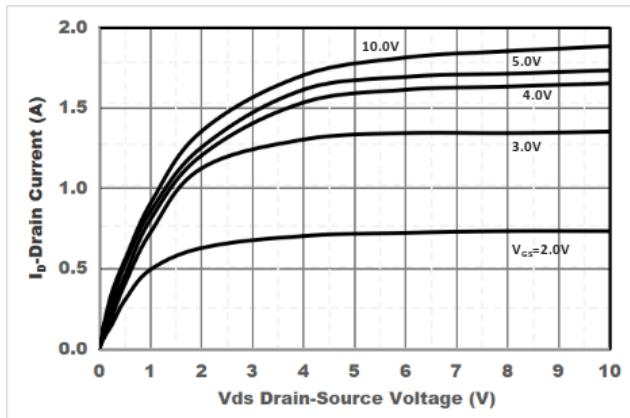


Figure1. Output Characteristics

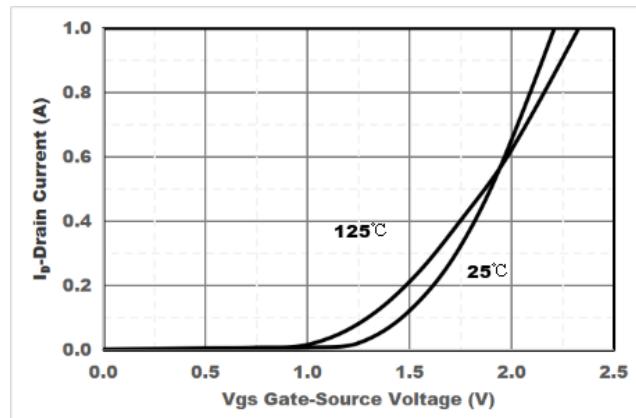


Figure2. Transfer Characteristics

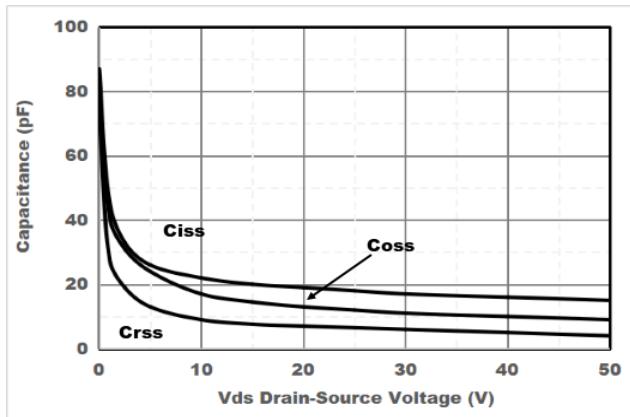


Figure3. Capacitance Characteristics

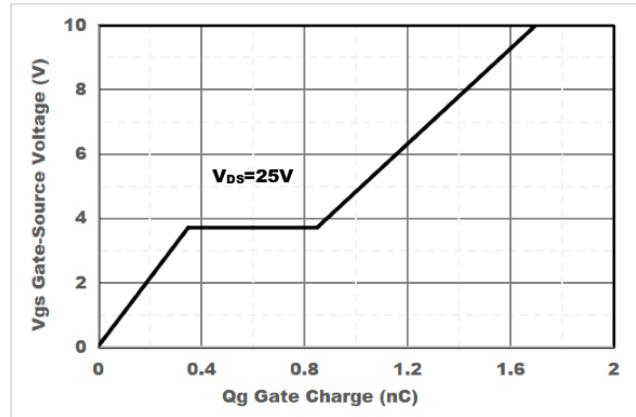


Figure4. Gate Charge

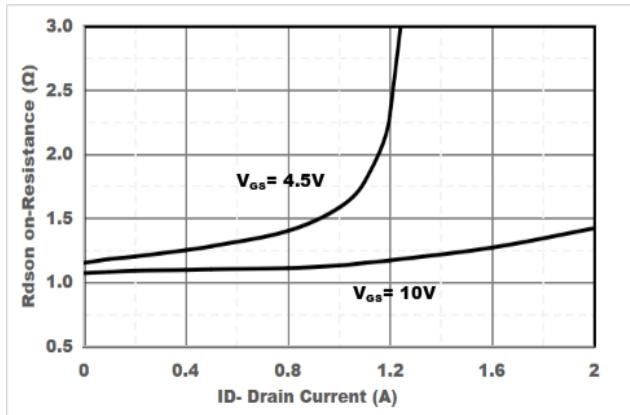


Figure5. Drain-Source on Resistance

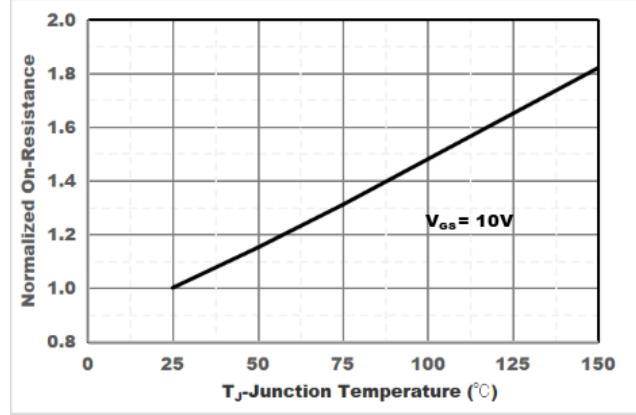


Figure6. Drain-Source on Resistance

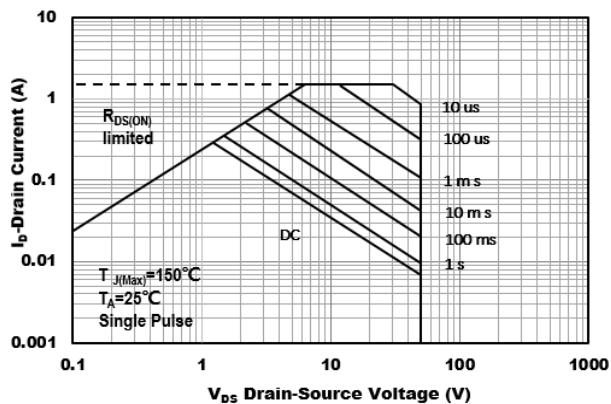


Figure7. Safe Operation Area

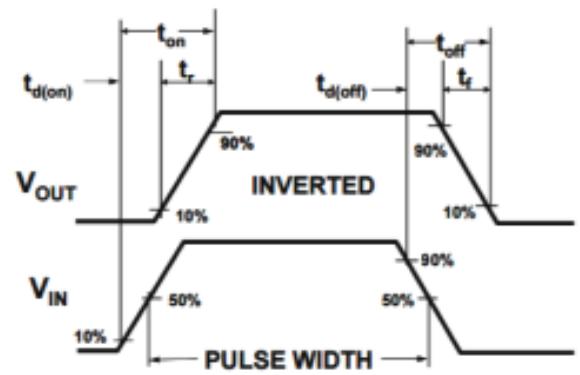
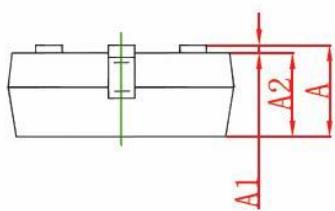
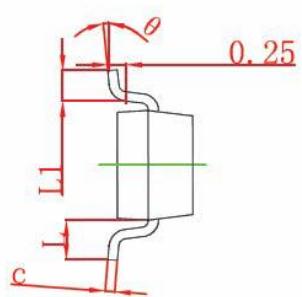
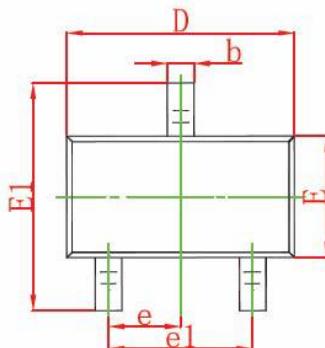
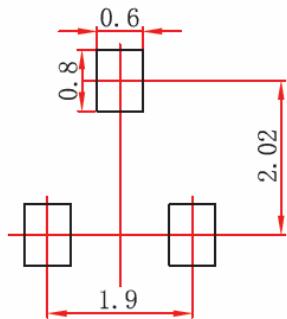


Figure8. Switching wave

■ SOT-23 Package information

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

■ SOT-23 Suggested Pad Layout**Note:**

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for MOSFET category:

Click to view products by SLKORMICRO manufacturer:

Other Similar products are found below :

[614233C](#) [648584F](#) [MCH3443-TL-E](#) [MCH6422-TL-E](#) [NTNS3A92PZT5G](#) [IRFD120](#) [IRFF430](#) [JANTX2N5237](#) [2N7000](#) [AOD464](#)
[2SK2267\(Q\)](#) [2SK2545\(Q,T\)](#) [405094E](#) [423220D](#) [MIC4420CM-TR](#) [VN1206L](#) [614234A](#) [715780A](#) [SSM6J414TU,LF\(T](#) [751625C](#)
[IPS70R2K0CEAKMA1](#) [BSF024N03LT3 G](#) [PSMN4R2-30MLD](#) [TK31J60W5,S1VQ\(O](#) [2SK2614\(TE16L1,Q\)](#) [DMN1017UCP3-7](#)
[EFC2J004NUZTDG](#) [FCAB21350L1](#) [P85W28HP2F-7071](#) [DMN1053UCP4-7](#) [NTE2384](#) [NTE2969](#) [NTE6400A](#) [DMC2700UDMQ-7](#)
[DMN2080UCB4-7](#) [DMN61D9UWQ-13](#) [US6M2GTR](#) [DMN31D5UDJ-7](#) [SSM6P54TU,LF](#) [DMP22D4UFO-7B](#) [IPS60R3K4CEAKMA1](#)
[DMN1006UCA6-7](#) [DMN16M9UCA6-7](#) [STF5N65M6](#) [IRF40H233XTMA1](#) [IPSA70R950CEAKMA1](#) [IPSA70R2K0CEAKMA1](#) [STU5N65M6](#)
[C3M0021120D](#) [DMN6022SSD-13](#)