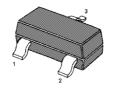


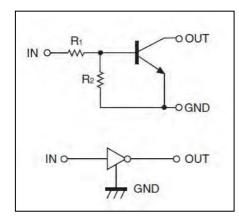
FEATURES

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors(see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy





1.Base (IN) 2.Emitter (GND) 3.Collector (OUT) SOT-23 Plastic Package



Equivalent Circuit

MAXIMUM RATINGS(Ta=25℃ unless otherwise noted)

Parameter	Symbol	Value	Unit	
Supply voltage	V _{CC}	50	V	
Input voltage	V _{IN}	-10 ~ 40	V	
Output current	I _O	50	A	
	$I_{C(MAX)}$	100	mA	
Power dissipation	Pd	200	mW	
Junction temperature	Tj	150	$^{\circ}$ C	
Storage temperature	Tstg	-55-150	$^{\circ}$ C	

ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

Parameter	Symbol	Min.	Тур	Max.	Unit	Conditions
Input voltage	V _{I(off)}	0.5			V	V _{CC} =5V ,I _O =100 μ A
	$V_{I(on)}$			3		V ₀ =0.3V ,I ₀ =10 mA
Output voltage	V _{O(on)}		0.1	0.3	V	I _O /I _I =10mA/0.5mA
Input current	$I_{\rm I}$			0.88	mA	V _I =5V
Output current	I _{O(off)}			0.5	μА	$V_{CC} = 50V, V_{I} = 0$
DC current gain	G _I	30				$V_O=5V$, $I_O=5mA$
Input resistance	R ₁	7	10	13	ΚΩ	
Resistance ratio	R ₂ /R ₁	0.8	1	1.2		
Transition frequency	f_T		250		MHz	V _{CE} =10V ,I _E =-5mA,f=100MHz



Typical Characteristics

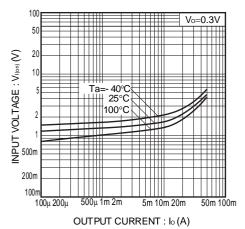


Fig.1 Input voltage vs. output current (ON characteristics)

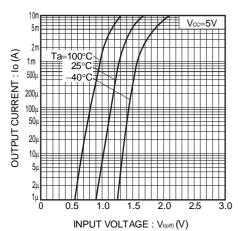


Fig.2 Output current vs. input voltage (OFF characteristics)

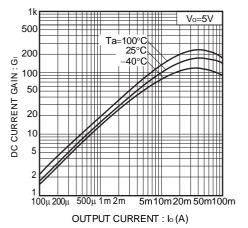


Fig.3 DC current gain vs. output current

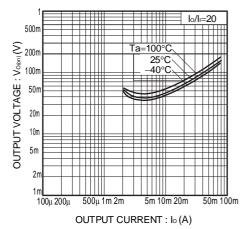


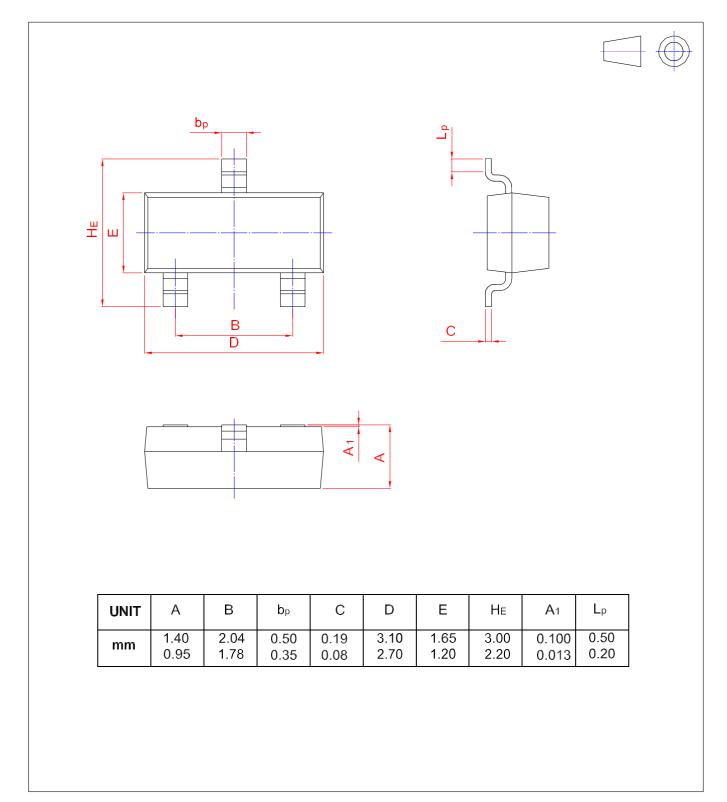
Fig.4 Output voltage vs. output current



PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT-23



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