## FEATURES

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors(see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input.They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy

MARKING: 24

1.Base (IN) 2.Emitter (GND) 3.Collector (OUT)

SOT-23 Plastic Package


Equivalent Circuit

## MAXIMUM RATINGS( $\mathbf{T a}=\mathbf{2 5}{ }^{\circ} \mathrm{C}$ unless otherwise noted)

| Parameter | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Supply voltage |  | 50 | V |
| Input voltage | $\mathrm{V}_{\mathrm{IN}}$ | $-10 \sim 40$ | V |
| Output current | I | 50 | mA |
|  | $\mathrm{I}_{\mathrm{C}(\mathrm{MAX})}$ | 100 | mW |
| Power dissipation | Pd | 200 | ${ }^{\circ}$ |
| Junction temperature | Tj | 150 | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature | Tstg | $-55-150$ |  |

ELECTRICAL CHARACTERISTICS $\left(\mathbf{T a}=25^{\circ} \mathrm{C}\right.$ unless otherwise specified)

| Parameter | Symbol | Min. | Typ | Max. | Unit | Conditions |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Input voltage | $\mathrm{V}_{\mathrm{I} \text { (off) }}$ | 0.5 |  |  | V | $\mathrm{~V}_{\mathrm{CC}}=5 \mathrm{~V}, \mathrm{I}_{\mathrm{O}}=100 \mu \mathrm{~A}$ |
|  | $\mathrm{~V}_{\mathrm{I} \text { (on })}$ |  |  | 3 |  | $\mathrm{~V}_{\mathrm{O}}=0.3 \mathrm{~V}, \mathrm{I}_{\mathrm{O}}=10 \mathrm{~mA}$ |
| Output voltage | $\mathrm{V}_{\mathrm{O} \text { (on })}$ |  | 0.1 | 0.3 | V | $\mathrm{I}_{\mathrm{O}} / \mathrm{I}_{\mathrm{I}}=10 \mathrm{~mA} / 0.5 \mathrm{~mA}$ |
| Input current | $\mathrm{I}_{\mathrm{I}}$ |  |  | 0.88 | mA | $\mathrm{~V}_{\mathrm{I}}=5 \mathrm{~V}$ |
| Output current | $\mathrm{I}_{\mathrm{O} \text { (off) }}$ |  |  | 0.5 | $\mu \mathrm{~A}$ | $\mathrm{~V}_{\mathrm{CC}}=50 \mathrm{~V}, \mathrm{~V}_{\mathrm{I}}=0$ |
| DC current gain | $\mathrm{G}_{\mathrm{I}}$ | 30 |  |  |  | $\mathrm{~V}_{\mathrm{O}}=5 \mathrm{~V}, \mathrm{I}_{\mathrm{O}}=5 \mathrm{~mA}$ |
| Input resistance | $\mathrm{R}_{1}$ | 7 | 10 | 13 | $\mathrm{~K} \Omega$ |  |
| Resistance ratio | $\mathrm{R}_{2} / \mathrm{R}_{1}$ | 0.8 | 1 | 1.2 |  |  |
| Transition frequency | $\mathrm{f}_{\mathrm{T}}$ |  | 250 |  | MHz | $\mathrm{V}_{\mathrm{CE}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{E}}=-5 \mathrm{~mA}, \mathrm{f}=100 \mathrm{MHz}$ |
|  |  |  |  |  |  |  |

## Typical Characteristics



Fig. 1 Input voltage vs. output current (ON characteristics)


Fig. 3 DC current gain vs. output current


Fig. 2 Output current vs. input voltage (OFF characteristics)


Fig. 4 Output voltage vs. output current

## PACKAGE OUTLINE



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