SD103AWT

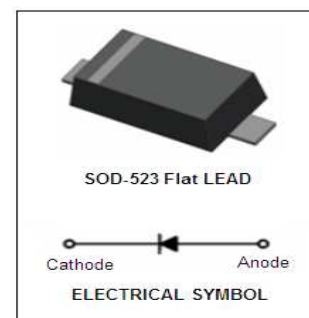## Features :

- Low Forward Voltage Drop;
- Guard Ring Construction for Transient Protection;
- Low Reverse Recover Time;
- Low Reverse Capacitance;
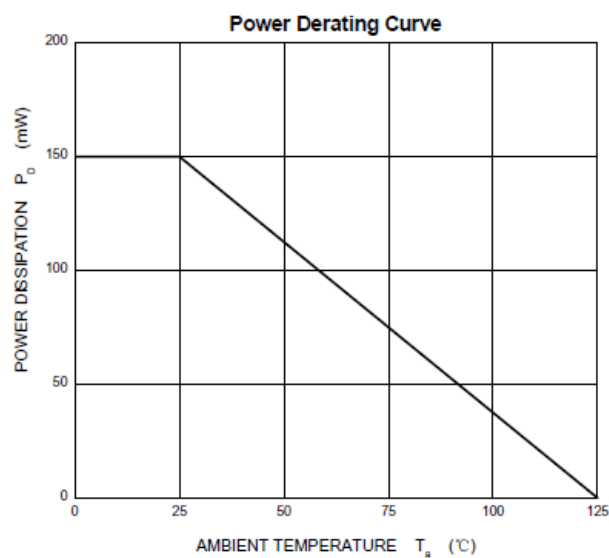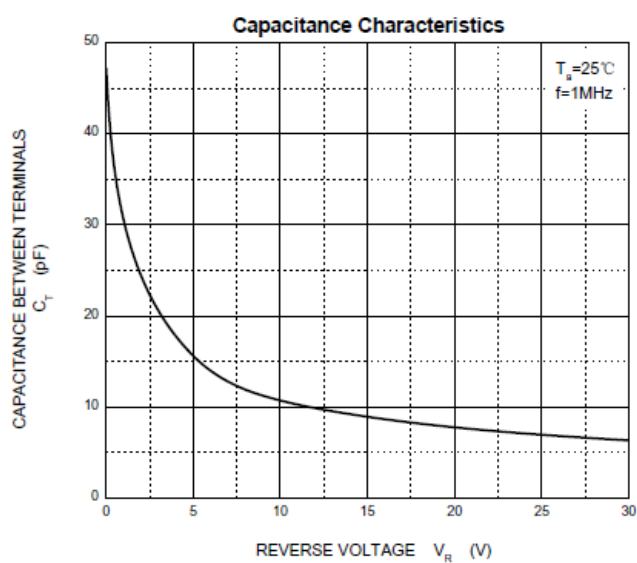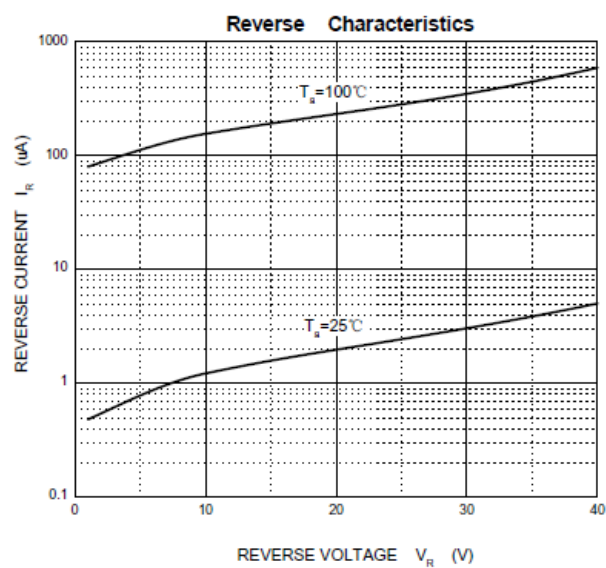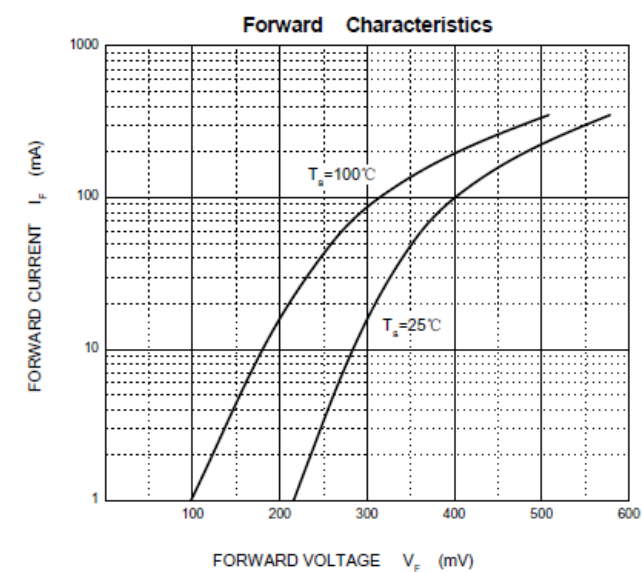
Marking: S4

SOD-523 Flat LEAD

Cathode ──────►|────── Anode

ELECTRICAL SYMBOL

MAXIUM RATINGS (Ta=25℃ unless otherwise noted)

Parameter	Symbol	Value	Unit
Peak Repetitive Peak Reverse Voltage	V_{RRM}	40	V
Working Peak Reverse Voltage	V_{RWM}		
DC Blocking Voltage	V_R		
RMS Reverse Voltage	$V_{R(RMS)}$	28	V
Forward Continuous current	I_0	350	mA
Repetitive Peak Forward Current @t≤1S	I_{FRM}	1	A
Non- Repetitive Peak Forward Surge Current @t≤8.3mS Half Sine Wave	I_{FSM}	15	A
Power Dissipation	P_d	150	mW
Thermal Resistance Junction To Ambient	$R_{\theta JA}$	667	℃/mW
Junction Temperature	Tstg	125	℃
Storage Temperature	Tstg	−55～150	℃

ELECTRIAL CHARACTERISTICS (Ta=25℃)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Reverse Voltage	$V_{(BR)}$	I_R=100 u A	40			V
Reverse current	I_R	V_R=30V			5	u A
		V_R=20V			2	
		V_R=30V			5	
Forward Voltage	V_F	I_F=1mA		0.27		V
		I_F=5mA		0.32		
		I_F=20mA			0.37	
		I_F=200mA			0.6	
Diode Capacitance	C_D	V_R=4V, f=1.0MHz		50		pF
Recover Reverse Time	t_{rr}	I_F=I_R=200mA, I_{rr}=0.1×I_R, R_L=100Ω		10		nS

典型特性曲线图

Forward Characteristics

FORWARD CURRENT I_F (mA) vs FORWARD VOLTAGE V_F (mV)

$T_a=100°C$
$T_a=25°C$

Reverse Characteristics

REVERSE CURRENT I_R (uA) vs REVERSE VOLTAGE V_R (V)

$T_a=100°C$
$T_a=25°C$

Capacitance Characteristics

CAPACITANCE BETWEEN TERMINALS C_T (pF) vs REVERSE VOLTAGE V_R (V)

$T_a=25°C$
f=1MHz

Power Derating Curve

POWER DISSIPATION P_D (mW) vs AMBIENT TEMPERATURE T_a (°C)

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