

80V N-Channel Enhancement Mode MOSFET

Description

The SL80N08D uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

$V_{DS} = 80V$ $I_D = 80A$

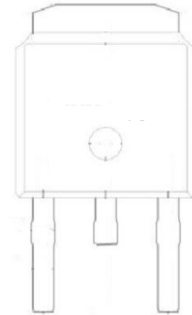
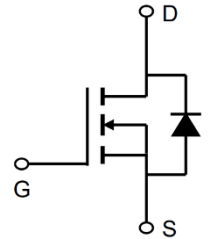
$R_{DS(ON)} < 6.5m\Omega$ $V_{GS} = 10V$

Application

Battery protection

Load switch

Uninterruptible power supply



Package Marking and Ordering Information

| Product ID | Pack | Marking | Qty(PCS) |
|------------|-----------|----------|----------|
| SL80N08D | TO-252-3L | SL80N08D | 2500 |

Absolute Maximum Ratings ($T_C = 25^\circ C$ unless otherwise noted)

| Symbol | Parameter | Rating | Units |
|---------------------------|--|------------|--------------|
| V_{DS} | Drain-Source Voltage | 80 | V |
| V_{GS} | Gate-Source Voltage | ± 20 | V |
| $I_D @ T_C = 25^\circ C$ | Continuous Drain Current, $V_{GS} @ 10V^{1,6}$ | 80 | A |
| $I_D @ T_C = 100^\circ C$ | Continuous Drain Current, $V_{GS} @ 10V^{1,6}$ | 42.5 | A |
| IDM | Pulsed Drain Current ² | 170 | A |
| EAS | Single Pulse Avalanche Energy ³ | 57.8 | mJ |
| IAS | Avalanche Current | 34 | A |
| $P_D @ T_C = 25^\circ C$ | Total Power Dissipation ⁴ | 56 | W |
| TSTG | Storage Temperature Range | -55 to 150 | $^\circ C$ |
| T_J | Operating Junction Temperature Range | -55 to 150 | $^\circ C$ |
| $R_{\theta JA}$ | Thermal Resistance Junction-Ambient ¹ | 62 | $^\circ C/W$ |
| $R_{\theta JC}$ | Thermal Resistance Junction-Case ¹ | 2.2 | $^\circ C/W$ |

Electrical Characteristics (T_J=25 °C, unless otherwise noted)

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|---------------------|--|--|------|------|------|------|
| BV _{DSS} | Drain-Source Breakdown Voltage | V _{GS} =0V , I _D =250uA | 80 | --- | --- | V |
| R _{DS(ON)} | Static Drain-Source On-Resistance ² | V _{GS} =10V , I _D =20A | --- | 4.8 | 6.5 | mΩ |
| R _{DS(ON)} | Static Drain-Source On-Resistance ² | V _{GS} =4.5V , I _D =20A | --- | 6.3 | 8.5 | mΩ |
| V _{GS(th)} | Gate Threshold Voltage | V _{GS} =V _{DS} , I _D =250uA | 1.0 | --- | 2.5 | V |
| I _{DSS} | Drain-Source Leakage Current | V _{DS} =64V , V _{GS} =0V , T _J =25°C | --- | --- | 1 | uA |
| | | V _{DS} =64V , V _{GS} =0V , T _J =55°C | --- | --- | 5 | |
| I _{GSS} | Gate-Source Leakage Current | V _{GS} =±20V , V _{DS} =0V | --- | --- | ±100 | nA |
| g _{fs} | Forward Transconductance | V _{DS} =5V , I _D =20A | --- | 75 | --- | S |
| R _g | Gate Resistance | V _{DS} =0V , V _{GS} =0V , f=1MHz | --- | 0.5 | --- | Ω |
| Q _g | Total Gate Charge (10V) | V _{DS} =40V , V _{GS} =10V , I _D =20A | --- | 40 | --- | nC |
| Q _{gs} | Gate-Source Charge | | --- | 7.2 | --- | |
| Q _{gd} | Gate-Drain Charge | | --- | 6.5 | --- | |
| T _{d(on)} | Turn-On Delay Time | V _{DD} =40V , V _{GS} =10V , R _G =3Ω, I _D =20A | --- | 8.3 | --- | ns |
| T _r | Rise Time | | --- | 4.2 | --- | |
| T _{d(off)} | Turn-Off Delay Time | | --- | 36 | --- | |
| T _f | Fall Time | | --- | 6.9 | --- | |
| C _{iss} | Input Capacitance | V _{DS} =40V , V _{GS} =0V , f=1MHz | --- | 2860 | --- | pF |
| C _{oss} | Output Capacitance | | --- | 410 | --- | |
| C _{rss} | Reverse Transfer Capacitance | | --- | 38 | --- | |
| I _s | Continuous Source Current ^{1,5} | V _G =V _D =0V , Force Current | --- | --- | 48 | A |
| V _{SD} | Diode Forward Voltage ² | V _{GS} =0V , I _s =A , T _J =25°C | --- | 0.77 | 1.0 | V |
| t _{rr} | Reverse Recovery Time | I _F =20A , di/dt=100A/μs , T _J =25°C | --- | 27 | --- | nS |
| Q _{rr} | Reverse Recovery Charge | | --- | 89 | --- | nC |

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V_{DD}=25V,V_{GS}=10V,L=0.1mH,I_{AS}=34A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.
- 6.The maximum current rating is package limited.

Typical Characteristics

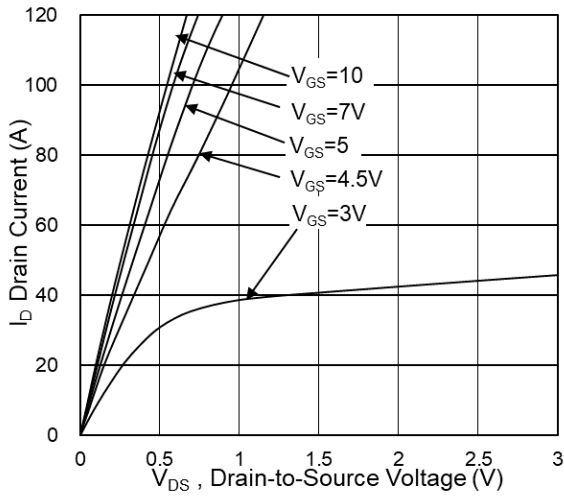


Fig.1 Typical Output Characteristics

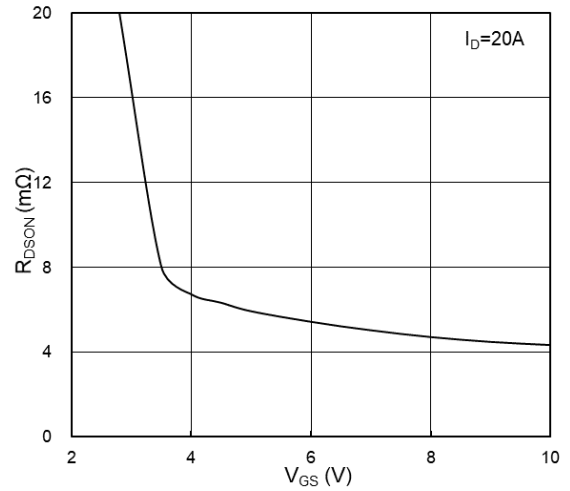


Fig.2 On-Resistance vs G-S Voltage

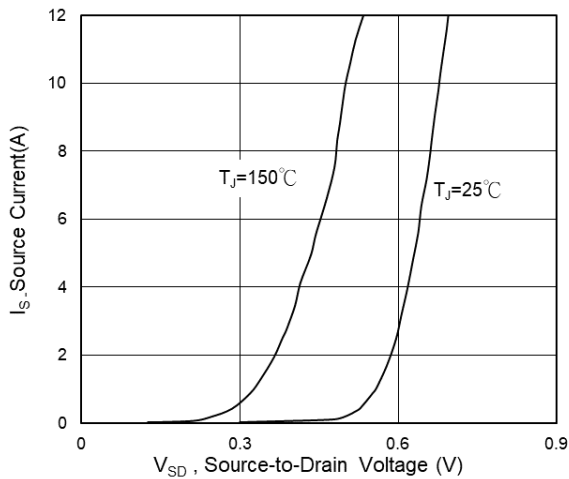


Fig.3 Source Drain Forward Characteristics

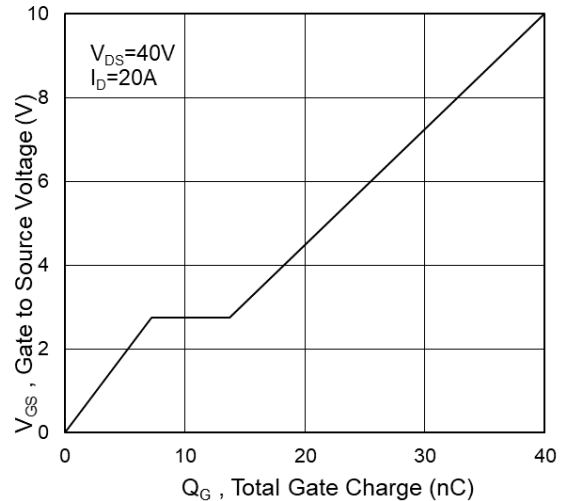


Fig.4 Gate-Charge Characteristics

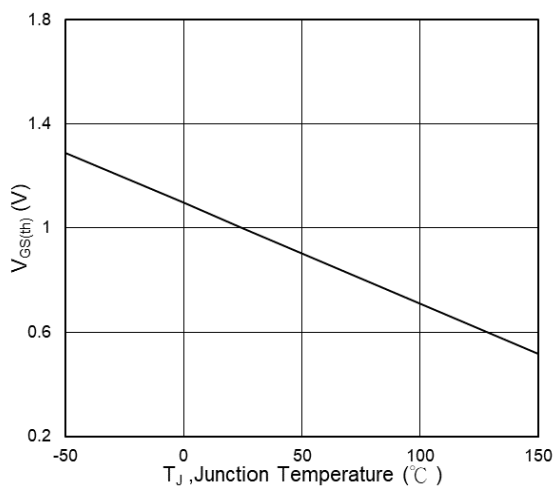


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

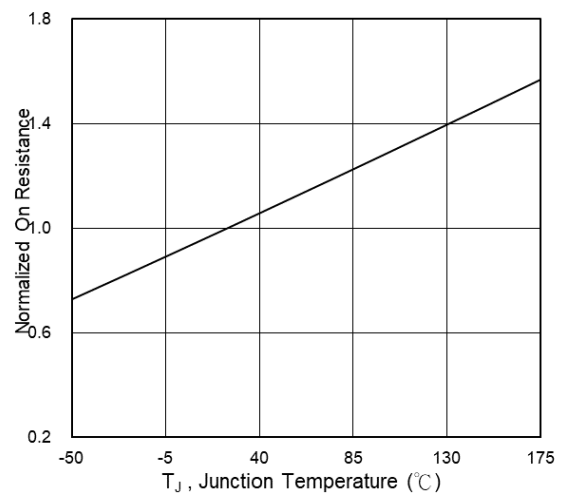


Fig.6 Normalized R_{DSON} vs. T_J

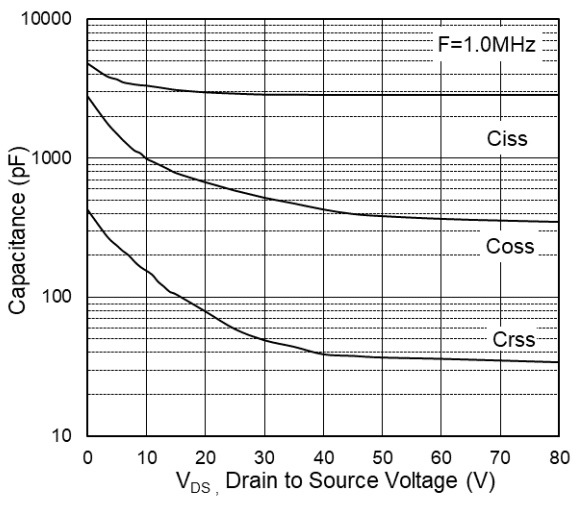


Fig.7 Capacitance

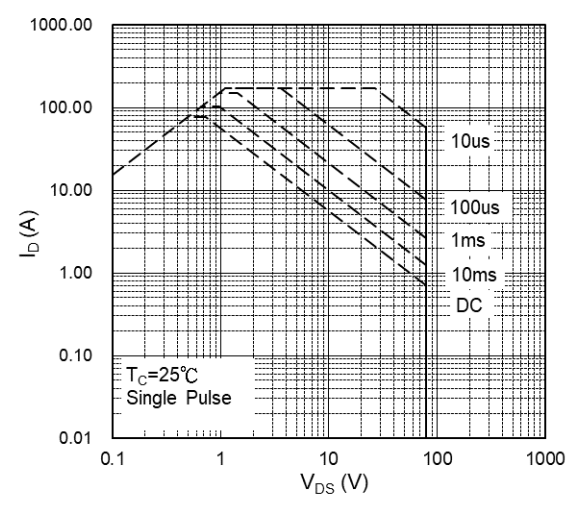


Fig.8 Safe Operating Area

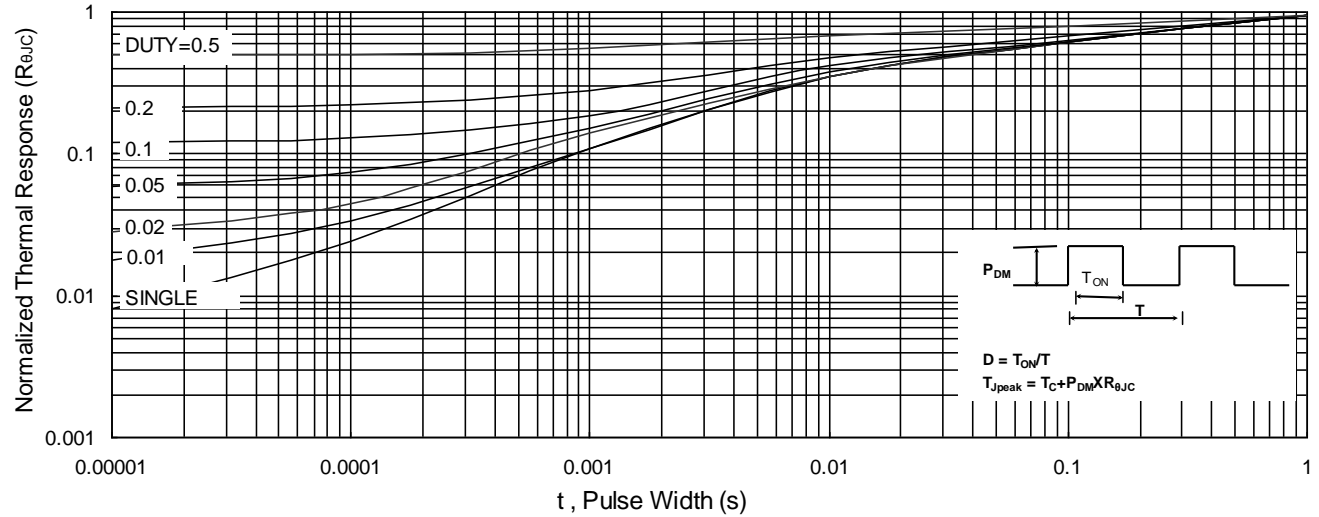


Fig.9 Normalized Maximum Transient Thermal Impedance

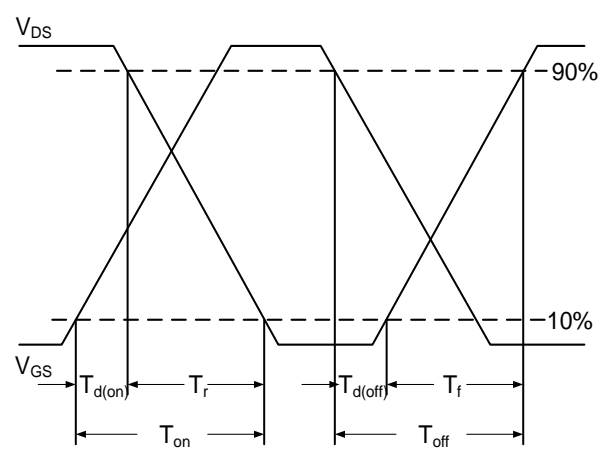


Fig.10 Switching Time Waveform

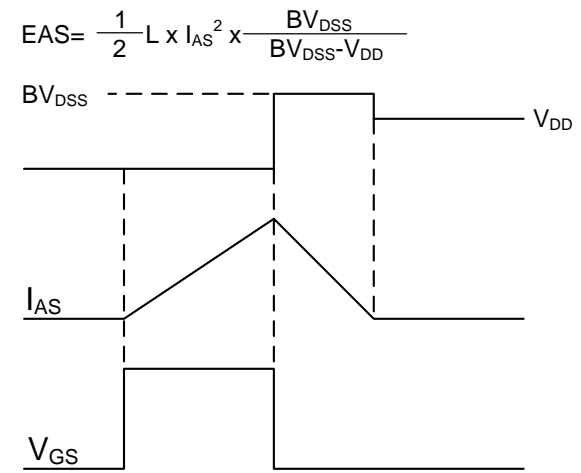
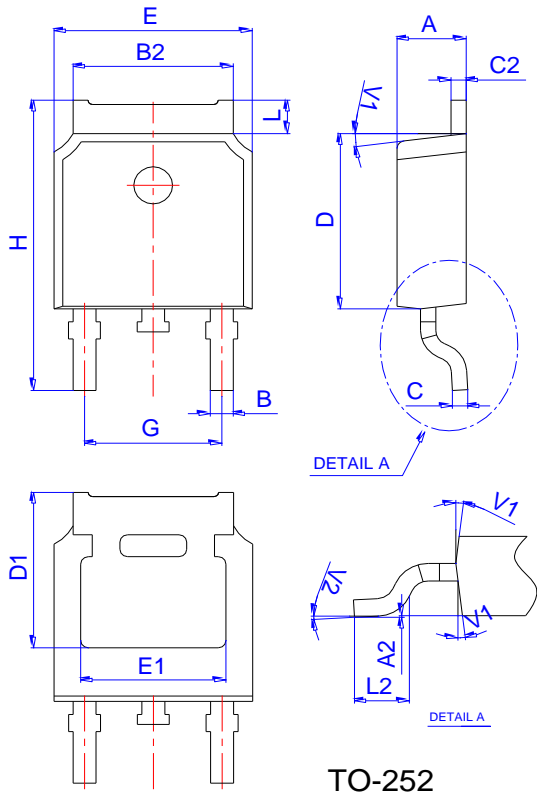


Fig.11 Unclamped Inductive Switching Waveform

Package Mechanical Data: TO-252-3L



| Ref. | Dimensions | | | | | |
|------|-------------|------|-------|----------|------|-------|
| | Millimeters | | | Inches | | |
| | Min. | Typ. | Max. | Min. | Typ. | Max. |
| A | 2.10 | | 2.50 | 0.083 | | 0.098 |
| A2 | 0 | | 0.10 | 0 | | 0.004 |
| B | 0.66 | | 0.86 | 0.026 | | 0.034 |
| B2 | 5.18 | | 5.48 | 0.202 | | 0.216 |
| C | 0.40 | | 0.60 | 0.016 | | 0.024 |
| C2 | 0.44 | | 0.58 | 0.017 | | 0.023 |
| D | 5.90 | | 6.30 | 0.232 | | 0.248 |
| D1 | 5.30REF | | | 0.209REF | | |
| E | 6.40 | | 6.80 | 0.252 | | 0.268 |
| E1 | 4.63 | | | 0.182 | | |
| G | 4.47 | | 4.67 | 0.176 | | 0.184 |
| H | 9.50 | | 10.70 | 0.374 | | 0.421 |
| L | 1.09 | | 1.21 | 0.043 | | 0.048 |
| L2 | 1.35 | | 1.65 | 0.053 | | 0.065 |
| V1 | | 7° | | | 7° | |
| V2 | 0° | | 6° | 0° | | 6° |

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