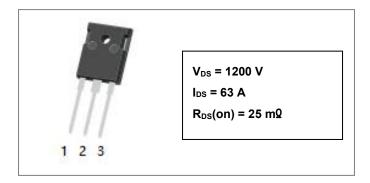
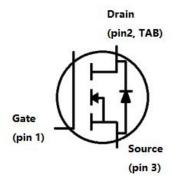




# S2M0025120D 1200V SIC POWER MOSFET



### **Circuit Diagram**



## **Description**

S2M0025120D is single SiC Power MOSFET packaged in TO-247AD case. The device is a high voltage n-channel enhancement mode MOSFET that has very low total conduction losses and very stable switching characteristics over temperature extremes. The S2M0025120D is ideal for energy sensitive, high frequency applications in challenging environments.

#### **Features**

- · Positive temperature characteristics, easy to parallel.
- Low on-resistance Typ. RDS(on) = 25m<sup>Q</sup> .
- Fast switching speed and low switching losses.
- · Very fast and robust intrinsic body diode.
- Process of non-bright Tin electroplatin

## **Applications**

- EV Fast Charging Modules
- EV On Board Chargers
- Solar Inverters
- Online UPS/Industrial UPS
- SMPS (Switch Mode Power Supplies)
- DC-DC Converters
- ESS (Energy Storage Systems)

### Maximum Ratings(T=25°C unless otherwise specified)

Characteristics	Symbol	Condition	Max.	Units
Drain Source Voltage	$V_{DSS}$	V <sub>GS</sub> = 0V, I <sub>DS</sub> = 100uA, T <sub>C</sub> = 25°C	1200	V
Gate Source Voltage	$V_{GSS}$	T <sub>c</sub> = 25°C, Absolute maximum values, AC (f>1Hz)	-10 to +25	V
Gate Source Voltage	$V_{GSOP}$	T <sub>C</sub> = 25°C Recommended Operational Values	-5 to +20	V
Continuous Drain Current	I <sub>D</sub>	V <sub>GS</sub> = 20V, T <sub>C</sub> = 25°C	63	Α
	ID	V <sub>GS</sub> = 20V, T <sub>C</sub> = 100°C	39	Α
Pulsed Drain Current	I <sub>D,pulse</sub>	Pulse width t <sub>P</sub> limited by T <sub>jmax</sub>	250	А
Power Dissipation	P <sub>D</sub>	T <sub>C</sub> =25°C, T <sub>J</sub> = 175 °C	517	W
Solder Temperature	TL	1.6mm (0.063") from case for 10s	260	°C

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## **Electrical Characteristics(T=25°C unless otherwise specified)**

Characteristics	Symbol	Condition	Min.	Тур.	Max.	Units	
Drain Source Breakdown Voltage	$V_{(BR)DSS}$	V <sub>GS</sub> = 0V, I <sub>D</sub> = 100uA 1200				V	
		$V_{DS} = V_{GS}$ , $I_D = 15$ mA	1.8	2.1	4	V	
Gate Threshold Voltage	$V_{\text{GS(th)}}$	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 15mA, T <sub>J</sub> = 150 °C		1.4		V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 1200V, V <sub>GS</sub> = 0V		2	100	uA	
Gate Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = 20V, V <sub>DS</sub> = 0V			250	nA	
Drain Source On-State	5	V <sub>GS</sub> = 20V, I <sub>D</sub> = 50A		25	34	mΩ	
Resistance	$R_{DS(on)}$	V <sub>GS</sub> = 20V, I <sub>D</sub> = 50A, T <sub>J</sub> = 150 °C		41		mΩ	
		V <sub>DS</sub> = 20 V, I <sub>DS</sub> = 50 A		13		S	
Transconductance	gfs	V <sub>DS</sub> = 20 V, I <sub>DS</sub> = 50 A, T <sub>J</sub> = 150 °C		14		S	
Input Capacitance	C <sub>ISS</sub>	$V_{GS} = 0V$ ,		4402			
Output Capacitance	Coss	V <sub>DS</sub> = 1000V		257		pF	
Reverse Transfer Capacitance	C <sub>RSS</sub>	V <sub>AC</sub> = 25mV		7			
Coss Stored Energy	Eoss	f = 1MHz		128		uJ	
Turn-On Switching Energy	Eon	V <sub>DS</sub> = 800V, V <sub>GS</sub> = -5/20V		0.61			
Turn-Off Switching Energy	Eoff	$I_D = 50A, R_{G(ext)} = 2.5\Omega$		0.31		mJ	
Turn-On Delay Time	$t_{d(on)}$	V <sub>DS</sub> = 800V, V <sub>GS</sub> = -5/20V		20			
Rise Time	t <sub>r</sub>	$I_D = 50A, R_{G(ext)} = 2.5\Omega$		24			
Turn-Off Delay Time	$t_{d(off)}$			36		ns	
Fall Time	t <sub>f</sub>			18			
Internal Gate Resistance	$R_{G(int)}$	f = 1MHz, VAC = 25 mV		2.5		Ω	
Gate to Source Charge	$Q_gs$	V <sub>DS</sub> = 800V, V <sub>GS</sub> = -5/20V		37			
Gate to Drain Charge	$Q_{gd}$	I <sub>D</sub> = 50A		38		nC	
Total Gate Charge	arge Q <sub>g</sub>			130			

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### **Reverse Diode Characteristics:**

Characteristics	Symbol	Condition		Max.	Units
Diode Forward Voltage	V <sub>SD</sub>	$V_{SD}$ $V_{GS}$ = -5V, $I_{SD}$ = 25A			V
		V <sub>GS</sub> = -5V, I <sub>SD</sub> = 25A, T <sub>J</sub> = 150°C	3.6		V
Continuous Diode Forward Current	ls	V <sub>GS</sub> = -5V, T <sub>C</sub> = 25°C		63	Α
Reverse Recovery Time	t <sub>rr</sub>	V <sub>GS</sub> = -5V, I <sub>SD</sub> = 50A, T <sub>J</sub> = 25°C	48		ns
Reverse Recovery Charge	Q <sub>rr</sub>	V <sub>R</sub> = 800V	354		nC
Peak Reverse Recovery Current	I <sub>mm</sub>	dif/dt= 1057A/µs	12		Α

## **Thermal-Mechanical Specifications:**

Characteristics	Symbol	Condition	Specification	Units
Junction Temperature	TJ	-	-55 to +175	°C
Storage Temperature	T <sub>stg</sub>	-	-55 to +175	°C
Typical Thermal Resistance Junction to Case	Rejc	DC operation	0.28	°C/W
Typical Thermal Resistance Junction to Ambient	$R_{ heta JA}$		32	°C/W

## **Ordering Information:**

Device	Package	Shipping
S2M0025120D	TO-247-3	25pcs/tube

## **Marking Diagram**



Where XXXXX is YYWWL

S2M = Device Type

0025  $= R_{DS}(on)$ 

120 = Reverse Voltage (1200V)

= Package SSG = SSG = Year WW = Week = Lot Number

Cautions: Molding resin

Epoxy resin UL:94V-0

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## **Ratings and Characteristics Curves**

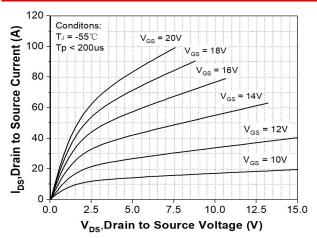


Figure 1. Output Characteristics T<sub>J</sub> = -55 °C

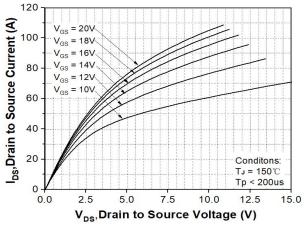


Figure 3. Output Characteristics T<sub>J</sub> = 150°C

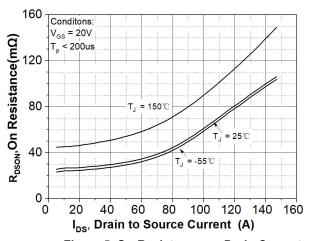


Figure 5. On-Resistance vs. Drain Current For Various Temperatures

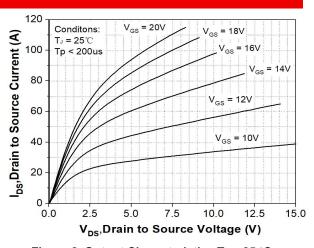


Figure 2. Output Characteristics T<sub>J</sub> = 25 °C

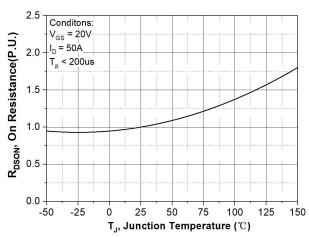


Figure 4. Normalized On-Resistance vs. Temperature

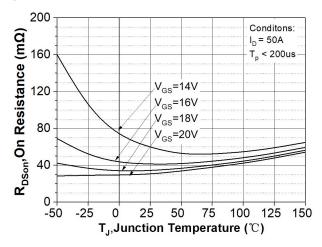


Figure 6. On-Resistance vs. Temperature For Various Gate Voltage

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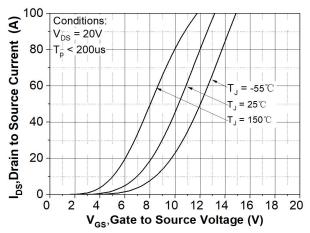


Figure 7. Transfer Characteristic for Various Junction Temperatures

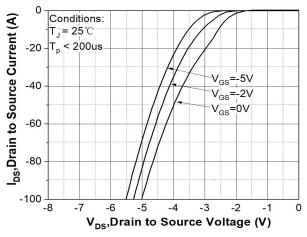


Figure 9. Body Diode Characteristic at T<sub>J</sub> = 25 °C

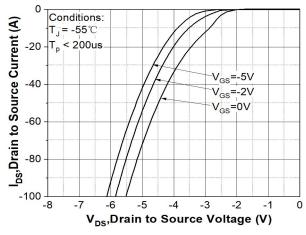


Figure 8. Body Diode Characteristic at T<sub>J</sub> = -55 °C

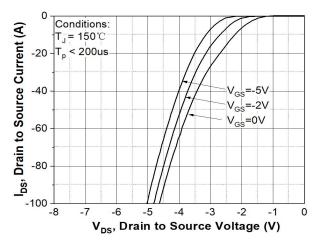


Figure 10. Body Diode Characteristic at T<sub>J</sub> = 150 °C

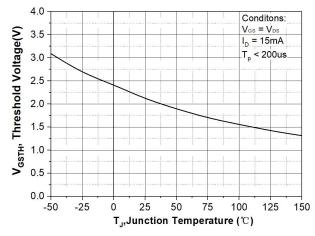


Figure 11. Threshold Voltage vs. Temperature

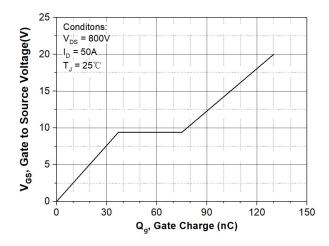


Figure 12. Gate Charge Characteristic

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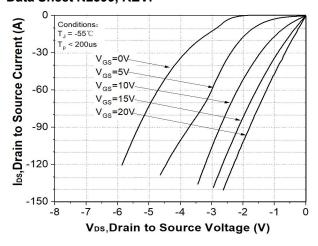


Figure 13. 3rd Quadrant Characteristic at T<sub>J</sub> = -55 °C

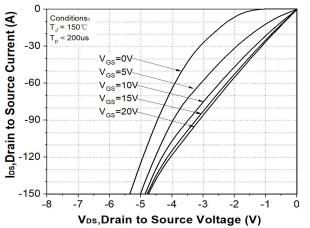


Figure 15. 3rd Quadrant Characteristic at T<sub>J</sub> = 150°C

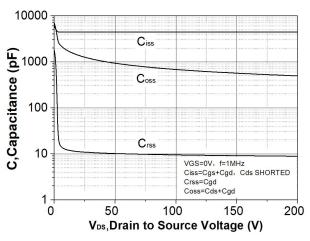


Figure 17. Capacitances vs. Drain-Source Voltage (0 - 200V)

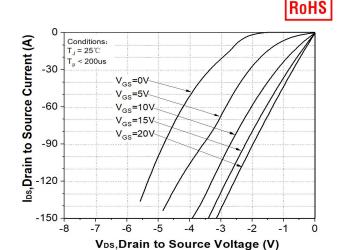


Figure 14. 3rd Quadrant Characteristic at T<sub>J</sub> = 25 °C

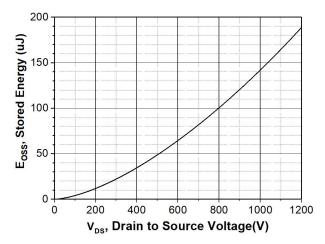


Figure 16. Output Capacitor Stored Energy

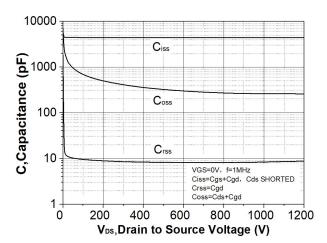


Figure 18. Capacitances vs. Drain-Source Voltage (0 - 1000V)

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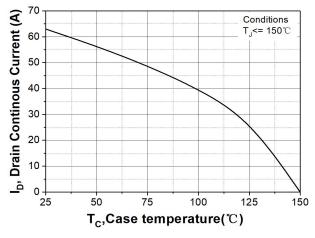


Figure 19. Continuous Drain Current Derating vs.
Case Temperature

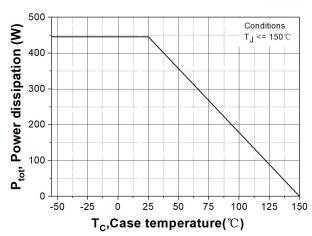


Figure 20. Maximum Power Dissipation Derating vs. Case Temperature

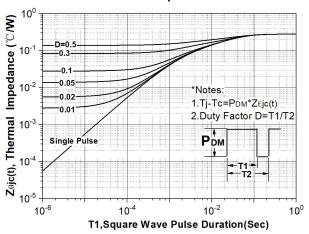


Figure 21. Transient Thermal Impedance (Junction - Case)

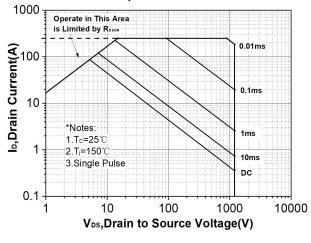


Figure 22. Safe Operating Area

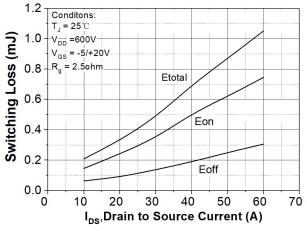


Figure 23. Clamped Inductive Switching Energy vs. Drain Current (V<sub>DD</sub> = 600V)

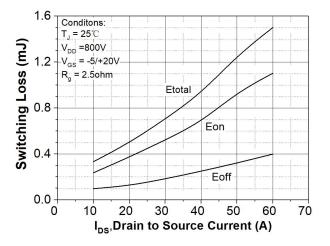


Figure 24. Clamped Inductive Switching Energy vs. Drain Current (V<sub>DD</sub> = 800V)

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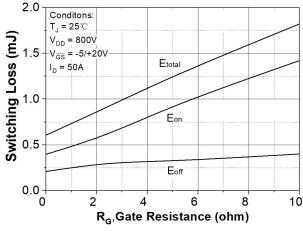


Figure 25. Clamped Inductive Switching Energy vs. R<sub>G(ext)</sub>

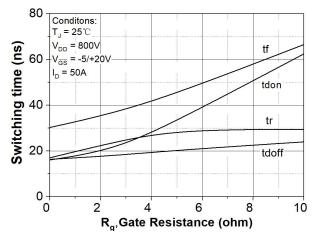


Figure 27. Switching Times vs. R<sub>G(ext)</sub>

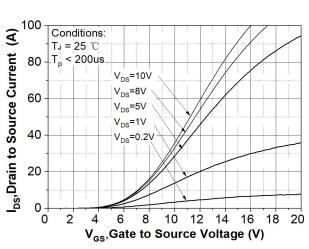


Figure 29. Transfer Characteristic for Various Drain Source Voltage

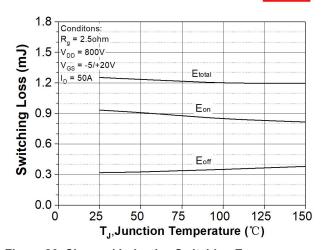


Figure 26. Clamped Inductive Switching Energy vs.
Temperature

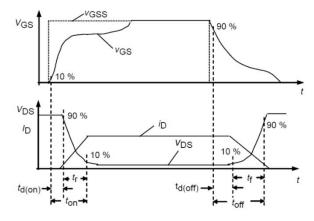


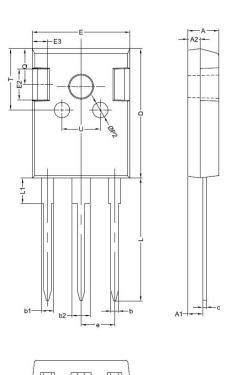
Figure 28. Switching Times Definition

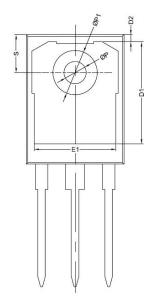
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## **Mechanical Dimensions TO-247-3**





SYMBOL	Millimeters				
STWIBOL	MIN.	TYP.	MAX.		
Α	4.80		5.20		
A1	2.00		2.75		
A2	1.90		2.10		
b	1.00		1.40		
b1	1.80		2.40		
b2	2.80		3.40		
С	0.40		0.75		
D	19.80		21.20		
D1		16.55			
D2		1.20			
E	15.20		16.00		
E1		13.30			
E2		5.00			
E3		2.50			
е	5.20		5.70		
L	13.90		20.70		
L1	3.70		4.30		
Р	3.50		3.70		
P1	7.1		7.40		
P2		2.50			
Q		5.80			
Q S T	6.05		6.25		
Т		10.00			
U		6.20			

### S2M0025120D



#### Technical Data Data Sheet N2399, REV.-



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DMN2080UCB4-7 DMN61D9UWQ-13 US6M2GTR DMN31D5UDJ-7 DMP22D4UFO-7B DMN1006UCA6-7 DMN16M9UCA6-7
STF5N65M6 IRF40H233XTMA1 STU5N65M6 DMN6022SSD-13 DMN13M9UCA6-7 DMTH10H4M6SPS-13 DMN2990UFB-7B
IPB80P04P405ATMA2 2N7002W-G MCAC30N06Y-TP MCQ7328-TP NTMC083NP10M5L BXP7N65D BXP4N65F AOL1454G
WMJ80N60C4 BXP2N20L BXP2N65D BXT1150N10J BXT1700P06M TSM60NB380CP ROG RQ7L055BGTCR DMNH15H110SK3-13
SLF10N65ABV2 BSO203SP BSO211P IPA60R230P6