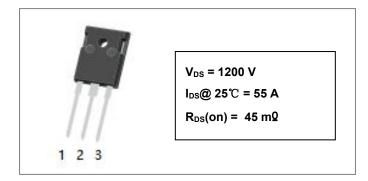
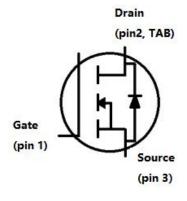




S2M0040120D-1 1200V SIC POWER MOSFET



Circuit Diagram



Description

S2M0040120D-1 is single SiC Power MOSFET packaged in TO-247AD(TO-247-3) case. The device is a high voltage n-channel Enhancement mode MOSFET that has very low total conduction losses and very stable switching characteristics over temperature extremes. The S2M0040120D-1 is ideal for energy sensitive, high frequency applications in challenging environments.

Features

- Positive temperature characteristics, easy to parallel.
- Low on-resistance Typ. RDS(on) = 45m^{\text{Q}} .
- Fast switching speed and low switching losses.
- Very fast and robust intrinsic body diode.
- · Process of non-bright Tin electroplatin
- "-A" is an AEC-Q101 qualified device

Applications

- EV Fast Charging Modules
- EV On Board Chargers
- Solar Inverters
- Online UPS/Industrial UPS

Maximum Ratings(T=25°C unless otherwise specified)

Characteristics	Symbol	Condition	Max.	Units
Drain Source Voltage	V _{DSS}	V _{GS} = 0V, I _{DS} = 100uA, T _j = 25°C	1200	V
Gate Source Voltage	V _{GSS}	T _j = 25°C, Absolute maximum values, AC (f>1Hz)	-10 to 25	V
Gate Source Voltage	V_{GSOP}	T _j = 25°C Recommended Operational Values	-5 to 20	V
Continuous Drain Current	I _D	V _{GS} = 20V, T _j = 25°C	55	А
	I _D	V _{GS} = 20V, T _j = 100°C	32	А
Pulsed Drain Current	I _{D,pulse}	Pulse width tP limited by Tjmax	160	Α
Power Dissipation	PD	TC=25°C, Tj = 175 °C	348	W
Solder Temperature	TL	1.6mm (0.063") from case for 10s	260	°C

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Electrical Characteristics(T=25℃ unless otherwise specified)

Characteristics	Symbol	Condition	Min.	Тур.	Max.	Units	
Drain Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 100uA	1200			V	
0 (T) 1 1 1 1 1 1 1 1 1	.,	$V_{DS} = V_{GS}$, $I_D = 10$ mA	1.8	2.4	4	٧	
Gate Threshold Voltage	$V_{\text{GS(th)}}$	V _{DS} = V _{GS} , I _D = 10mA T _J = 175 °C		1.55		٧	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 1200V, V _{GS} = 0V		1	100	uA	
Gate Source Leakage Current	Igss	V _{GS} = 20V, V _{DS} = 0V			250	nA	
Desir Course On Otata Basistana	Б	V _{GS} = 20V, I _D = 40A		45	52	mΩ	
Drain Source On-State Resistance	$R_{DS(on)}$	V _{GS} = 20V, I _D = 40A, T _J = 175 °C		73		mΩ	
	,	V _{DS} = 20 V, I _{DS} = 40 A		10		S	
Transconductance	gfs	V _{DS} = 20 V, I _{DS} = 40 A, T _J = 175 °C		12		S	
Input Capacitance	C _{ISS}	V _{GS} = 0V,		1904			
Output Capacitance	Coss	V _{DS} = 1000V		108		pF	
Reverse Transfer Capacitance	C _{RSS}	V _{AC} = 25mV f = 1MHz		6			
Coss Stored Energy	Eoss	1 1 - 1101112		72.9		uJ	
Turn-On Switching Energy	Eon	V _{DS} = 800V, V _{GS} = -5/20V		0.25			
Turn-Off Switching Energy	E _{OFF}	I _D =40A, R _{G(ext)} =2.5Ω, L=99uH		0.05		mJ	
Turn-On Delay Time	$t_{d(on)}$	V _{DS} = 800V, V _{GS} = -5/20V		12			
Rise Time	t _r	$I_D = 40A, R_{G(ext)} = 2.5\Omega$		14			
Turn-Off Delay Time	$t_{d(off)}$	Inductive Load Timing relative to VDS Per IEC60747-8-4 pg 83		22		ns	
Fall Time	t f	ν D3 1 er 1Ε 000747-0-4 pg 03		4			
Internal Gate Resistance	R _{G(int)}	f = 1MHz, VAC = 25 mV		2.6		Ω	
Gate to Source Charge	Qgs	V _{DS} = 800V, V _{GS} = -5/20V, I _D = 40A		34.3			
Gate to Drain Charge	Q_{gd}	Per IEC60747-8-4 pg 21		32.1		nC	
Total Gate Charge	Q_g			92.1			

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Reverse Diode Characteristics:

Characteristics	Symbol	Condition	Тур.	Max.	Units
Diada Famuard Valtaria	M	V _{GS} = -5V, I _{SD} = 20A	3.6		V
Diode Forward Voltage	$V_{ extsf{SD}}$	V _{GS} = -5V, I _{SD} = 20A, T _J =175°C	3.2		V
Continuous Diode Forward Current	Is	T _C =25°C	44		Α
Reverse Recovery Time	t _{rr}	V _{GS} =-5V, I _{SD} =40A, T _J =25°C	43.4		ns
Reverse Recovery Charge	Q _{rr}	V _R =800V	162		nC
Peak Reverse Recovery Current	I _{mm}	dif/dt=1047A/µs	8.1		Α

Thermal-Mechanical Specifications:

Characteristics	Symbol	Condition	Specification	Units
Junction Temperature	TJ	-	-55 to +175	°C
Storage Temperature	T_{stg}	-	-55 to +175	°C
Typical Thermal Resistance Junction to Case	R _θ Jc	DC operation	0.43	°C/W
Maximun Thermal Resistance Junction to Ambient	$R_{ heta JA}$		32.6	°C/W

Ordering Information:

Device	Package	Shipping
S2M0040120D-1	TO-247AD(TO-247-3)	30pcs/tube





Ratings and Characteristics Curves

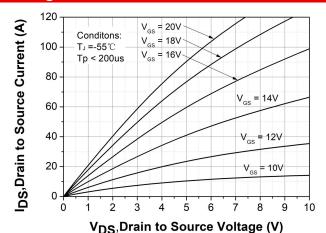


Figure 1. Output Characteristics T_J = -55 °C

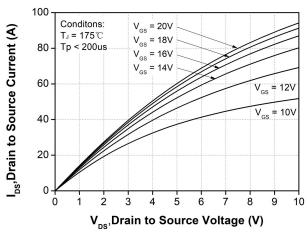


Figure 3. Output Characteristics T_J = 175°C

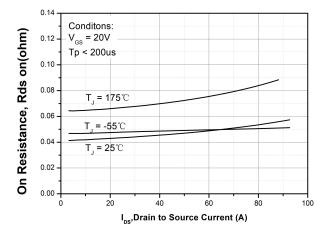


Figure 5. On-Resistance vs. Drain Current For Various Temperatures

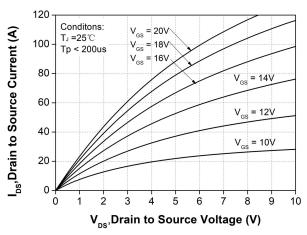


Figure 2. Output Characteristics T_J = 25 °C

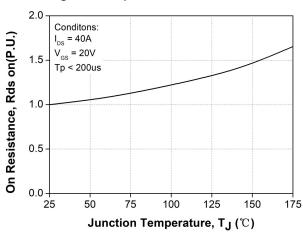


Figure 4. Normalized On-Resistance vs. Temperature

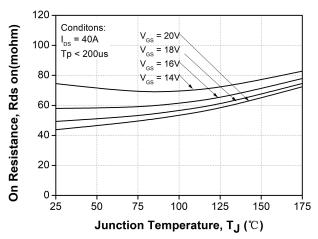


Figure 6. On-Resistance vs. Temperature For Various Gate Voltage

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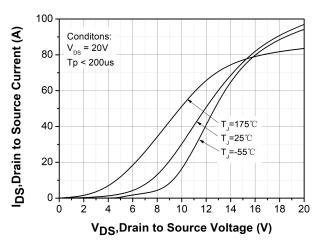


Figure 7. Transfer Characteristic for Various Junction Temperatures

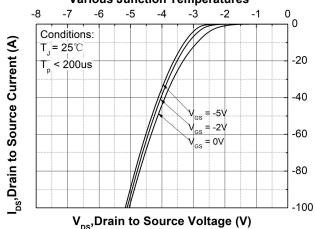


Figure 9. Body Diode Characteristic at T_J = 25 °C

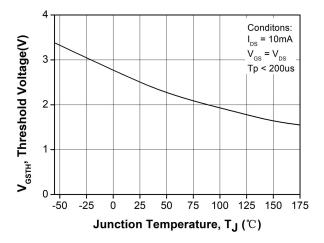


Figure 11. Threshold Voltage vs. Temperature

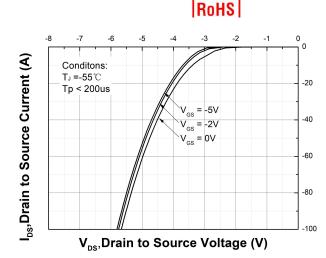


Figure 8. Body Diode Characteristic at T_J = -55 °C

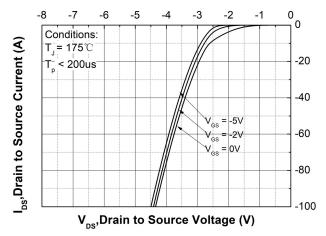


Figure 10. Body Diode Characteristic at T_J = 175 °C

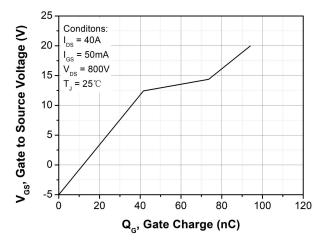


Figure 12. Gate Charge Characteristic

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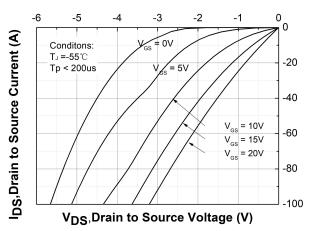


Figure 13. 3rd Quadrant Characteristic at T_J = -55 °C

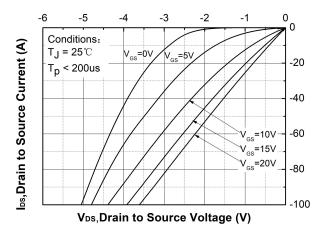


Figure 15. 3rd Quadrant Characteristic at T_J = 175°C

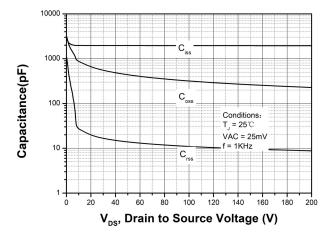


Figure 17. Capacitances vs. Drain-Source Voltage (0 - 200V)

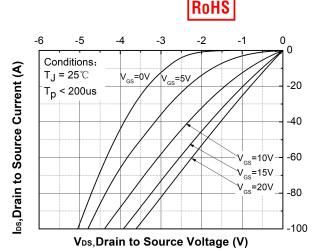


Figure 14. 3rd Quadrant Characteristic at T_J = 25 °C

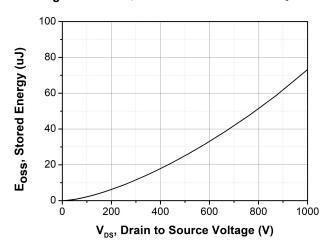


Figure 16. Output Capacitor Stored Energy

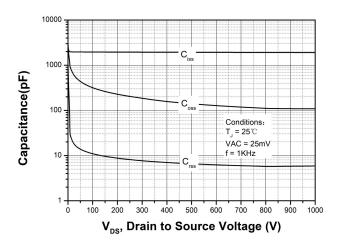


Figure 18. Capacitances vs. Drain-Source Voltage (0 - 1000V)

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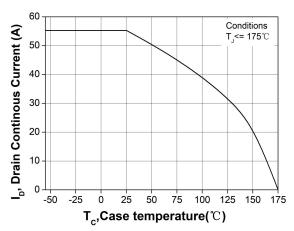


Figure 19. Continuous Drain Current Derating vs.

Case Temperature

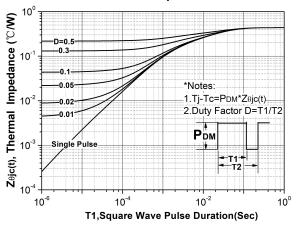


Figure 21. Transient Thermal Impedance (Junction - Case)

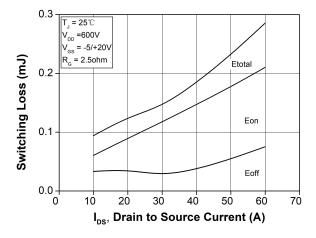


Figure 23. Clamped Inductive Switching Energy vs. Drain Current (V_{DD} = 600V)

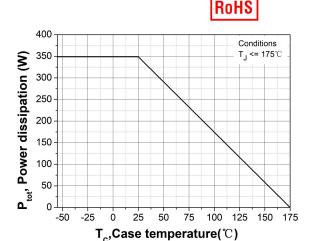


Figure 20. Maximum Power Dissipation Derating vs. Case Temperature

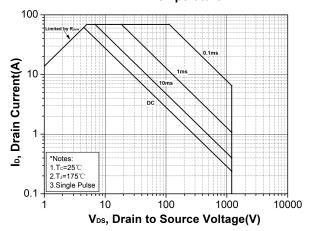


Figure 22. Safe Operating Area

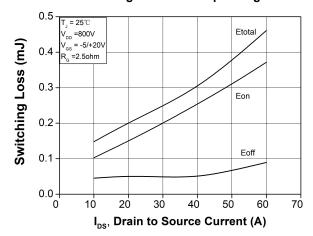


Figure 24. Clamped Inductive Switching Energy vs. Drain Current (V_{DD} = 800V)

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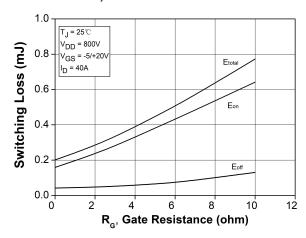


Figure 25. Clamped Inductive Switching Energy vs. R_{G(ext)}

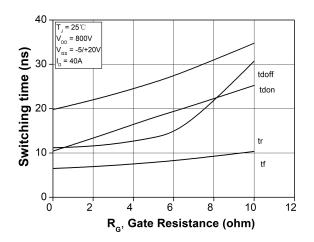


Figure 27. Switching Times vs. R_{G(ext)}

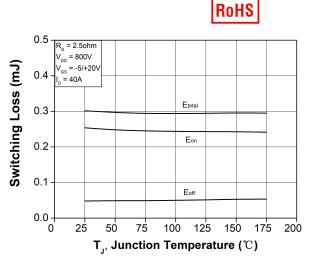


Figure 26. Clamped Inductive Switching Energy vs.
Temperature

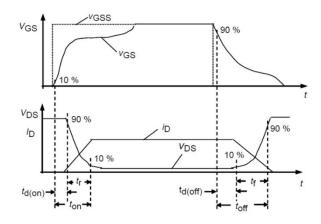


Figure 28. Switching Times Definition





Marking Diagram



Where XXXXX is YYWWL

S2M = Device Type 0040

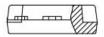
= R_{DS}(on) = Reverse Voltage (1200V) 120

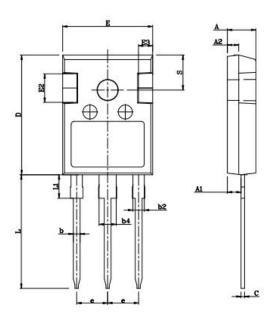
= Package SSG = SSG = Year WW = Week = Lot Number

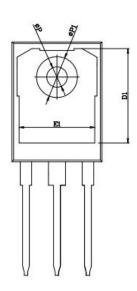
Cautions: Molding resin

Epoxy resin UL:94V-0

Mechanical Dimensions TO-247AD(TO-247-3)







COMMON DIMENSIONS

SYMBOL	mm				
SYMBOL	Min	Nom	Max		
Α	4.80	5.00	5.20		
Al	2.23	2.41	2.59		
A2	1.85	2.00	2.15		
b	1.11	1,21	1.36		
b2	1.91	2.01	2.21		
b4	2.91	3.01	3.21		
c	0.51	0.61	0.75		
D	20.80	21.00	21.30		
Dl	16.25	16.55	16.85		
Е	15.50	15.80	16.10		
El	13.00	13.26	13.56		
E2	4.80	5.00	5.20		
E3	2,30	2.50	2.70		
e	5.44BSC				
L	19.82	19.92	20.22		
L1	3.94	4,12	4.30		
ØP	3.40	3.60	3.80		
ØP1	7.08	7.19	7.30		
S	6.15BSC				

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S2M0040120D-1



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DMN2080UCB4-7 DMN61D9UWQ-13 US6M2GTR DMN31D5UDJ-7 DMP22D4UFO-7B DMN1006UCA6-7 DMN16M9UCA6-7
STF5N65M6 IRF40H233XTMA1 STU5N65M6 DMN6022SSD-13 DMN13M9UCA6-7 DMTH10H4M6SPS-13 DMN2990UFB-7B
IPB80P04P405ATMA2 2N7002W-G MCAC30N06Y-TP MCQ7328-TP BXP7N65D BXP4N65F AOL1454G WMJ80N60C4 BXP2N20L
BXP2N65D BXT1150N10J BXT1700P06M TSM60NB380CP ROG RQ7L055BGTCR DMNH15H110SK3-13 SLF10N65ABV2
BSO203SP BSO211P IPA60R230P6 IPA60R460CE