

Features

- Development Board with Solantro’s SA4011 (IXC) digital power processor
- Preinstalled “starter-code” firmware
- Interface ports for interacting with Solantro’s Helios Test and Control Tool and PuTTY

Development capabilities

- Change register values and parameters on the SA4011 for testing and evaluation purposes
- Download, develop, and test firmware for the SA4011

Development kit

- SA4011(IXC) Development Board with installed firmware
- Helios Test and Control Tool

Description

The Solantro SA4011 Development Board is a PCB on which is mounted a SA4011 IC. An USB port powers the board and used for communications between the SA4011 and a Windows PC. The communication is done with Solantro’s Helios Test and Control Tool (Helios) and PuTTY. Helios is a Windows application which uses a COM port to communicate with the firmware running on the processor through the second UART. The primary purpose is to observe and change program variables and hardware registers as well as loading new firmware. Along with the Solantro Software Development Environment, the SA4011 Development Board provides a platform for developing firmware for the SA4011 to support custom applications. A photograph of the SA4011 Development Board is shown in Figure 1.

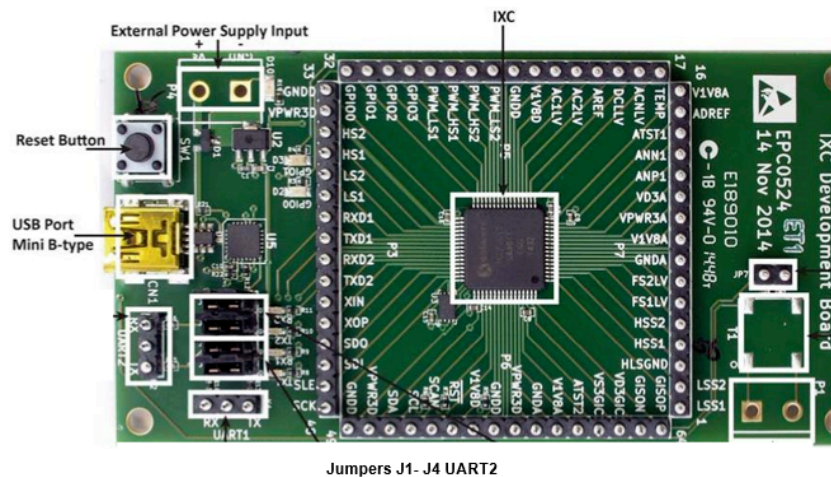


Figure 1 - SA4011 Development Board

PC requirements

- operating system Windows 7 or 10
- USB connection port

Powering the SA4011 Development Board

The SA4011 Development Board is powered via USB cable from a PC to the Mini-B USB port on the Development Board.

Quick start of the SA4011 Development Board

- Step 1** Connect the board to the PC USB port with the USB-A to Mini-B Cable.
- Step 2** Check that the four jumpers JP1 to JP4 are firmly seated.
- Step 3** Install the Solantro Development Environment following DPD1167.
- Step 4** Read SA4011(IXC) datasheet (DPD1036).
- Step 5** Follow the SA4011 (IXC) technical reference guide (DPD1058) to perform examples and become accustomed with SA4011.

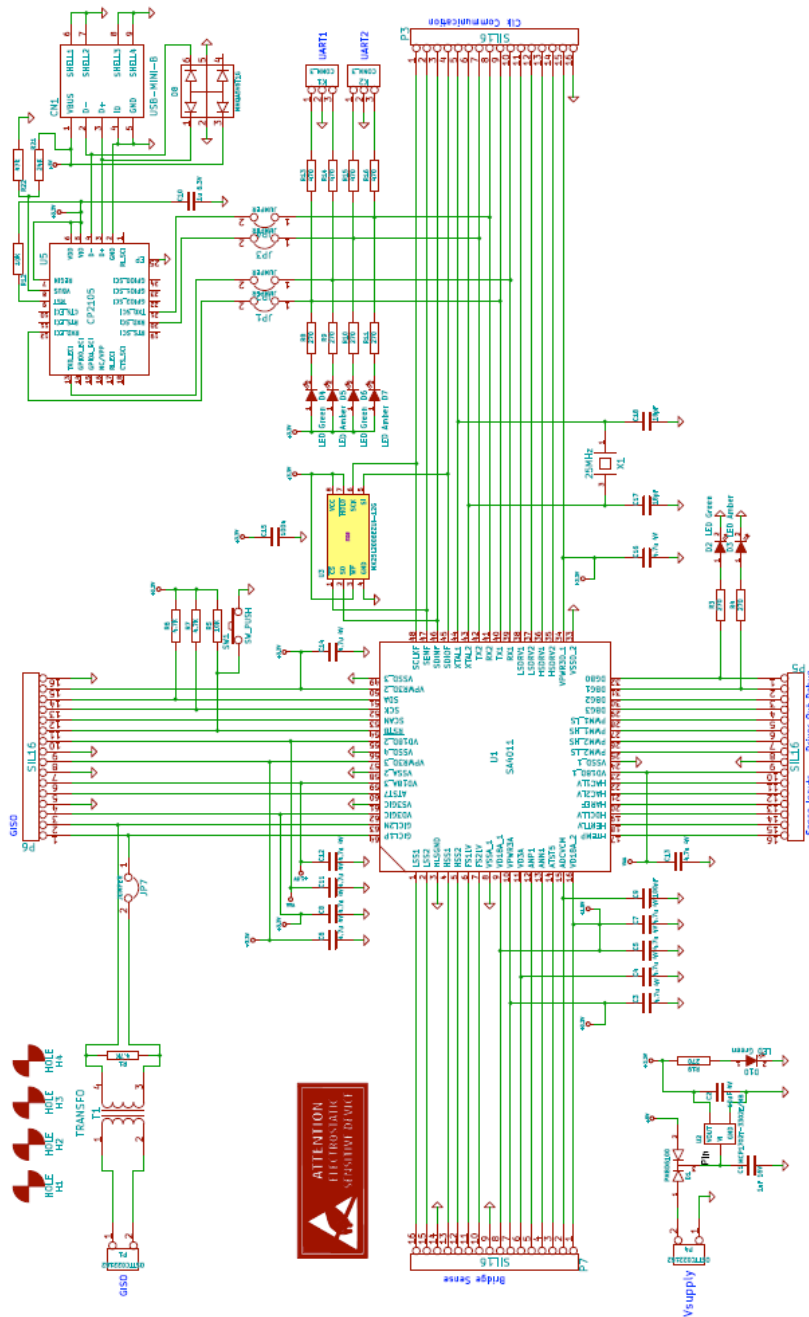


Figure 2 – SA4011 (IXC) Development Board Schematic

For assistance, contact:
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