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April 1999

# FDS6630A

#### FAIRCHILD SEMICONDUCTOR

#### **FDS6630A** N-Channel Logic Level PowerTrench<sup>™</sup> MOSFET

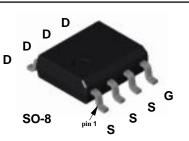
#### **General Description**

This N-Channel Logic Level MOSFET is produced using Fairchild Semiconductor's advanced PowerTrench process that has been especially tailored to minimize on-state resistance and yet maintain superior switching performance.

These devices are well suited for low voltage and battery powered applications where low in-line power loss and fast switching are required.

#### Applications

- DC/DC converter
- Load switch
- Motor drives



#### 

• 6.5 A, 30 V.  $R_{DS(on)} = 0.038 \Omega @ V_{GS} = 10 V$ 

• High performance trench technology for extremely

• High power and current handling capability.

• Low gate charge (5nC typical).

• Fast switching speed.

low R<sub>DS(ON)</sub>.

 $R_{DS(on)} = 0.053 \ \Omega \ @ V_{GS} = 4.5 \ V$ 

#### Absolute Maximum Ratings T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V <sub>DSS</sub>	Drain-Source Voltage		30	V
V <sub>GSS</sub>	Gate-Source Voltage		<u>+</u> 20	V
I <sub>D</sub>	Drain Current - Continuous	(Note 1a)	6.5	А
	- Pulsed		40	
P <sub>D</sub> Po	Power Dissipation for Single Operation	(Note 1a)	2.5	W
		(Note 1b)	1.2	
		(Note 1c)	1	
T <sub>J</sub> , T <sub>stg</sub>	Operating and Storage Junction Temperature Range		-55 to +150	∘C

**Features** 

#### **Thermal Characteristics**

R <sub>θ</sub> JA	Thermal Resistance, Junction-to-Ambient	(Note 1a)	50	°C/W
R <sub>θ</sub> JC	Thermal Resistance, Junction-to-Case	(Note 1)	25	°C/W

#### **Package Outlines and Ordering Information**

Device Marking	Device	Reel Size	Tape Width	Quantity
FDS6630A	FDS6630A	13"	12mm	2500 units

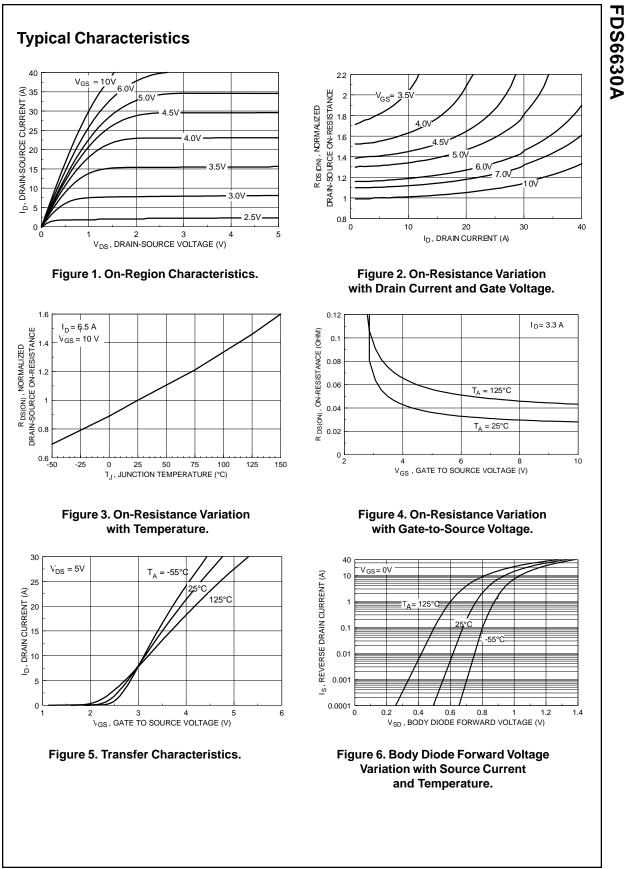
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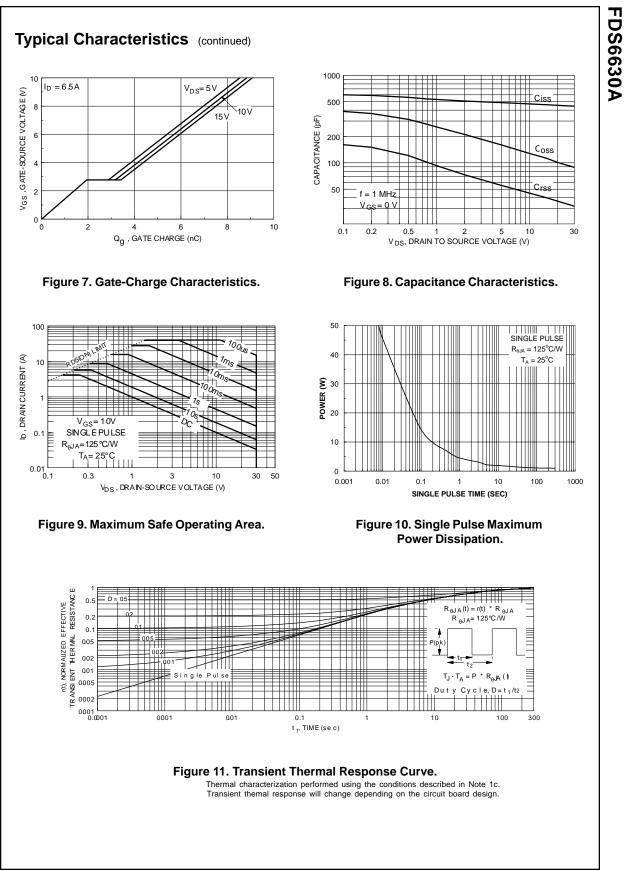
	Parameter	Test Conditions	Min	Тур	Max	Units
Off Char	racteristics	·				
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0 V, I_{D} = 250 \mu A$	30			V
<u>A</u> BVdss ΔTJ	Breakdown Voltage Temperature Coefficient	$I_D = 250 \ \mu$ A, Referenced to 25°C		24		mV/∘C
DSS	Zero Gate Voltage Drain Current	$V_{DS} = 24 V, V_{GS} = 0 V$			1	μA
GSSF	Gate-Body Leakage Current, Forward	$V_{GS} = 20 \text{ V}, V_{DS} = 0 \text{ V}$			100	nA
	Gate-Body Leakage Current, Reverse	$V_{GS} = -20 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nA
On Char	acteristics (Note 2)					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1	1.7	3	V
<u>A</u> VGS(th) ΔTJ	Gate Threshold Voltage Temperature Coefficient	$I_D = 250 \ \mu A$ , Referenced to $25^{\circ}C$		-4		mV/∘C
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance			0.028 0.044 0.040	0.038 0.060 0.053	Ω
I <sub>D(on)</sub>	On-State Drain Current	$V_{GS}$ = 10 V, $V_{DS}$ = 5 V	20			Α
<b>g</b> fs	Forward Transconductance	$V_{DS} = 5 \text{ V}, \text{ I}_{D} = 6.5 \text{ A}$		13		S
Dynami	c Characteristics					
C <sub>iss</sub>	Input Capacitance	$V_{DS} = 15 V, V_{GS} = 0 V,$		460		pF
Coss	Output Capacitance	f = 1.0 MHz		115		pF
Crss	Reverse Transfer Capacitance	1		45		pF
Switchir		1				
<u>Switciiii</u> t <sub>d(on)</sub>	Turn-On Delay Time	$V_{DD} = 15 \text{ V}, \text{ I}_{D} = 1 \text{ A},$		5	11	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{GS} = 10 \text{ V}, \text{ R}_{GEN} = 6 \Omega$		8	17	ns
t <sub>d(off)</sub>	Turn-Off Delay Time			17	28	ns
t <sub>f</sub>	Turn-Off Fall Time	1		13	24	ns
Q <sub>g</sub>	Total Gate Charge	$V_{DS} = 5 V, I_{D} = 6.5 A,$		5	7	nC
∽g Q <sub>gs</sub>	Gate-Source Charge	$V_{GS} = 5 V$		2		nC
Q <sub>qd</sub>	Gate-Drain Charge			0.9		nC
Drain-So	Durce Diode Characteristics and Maximum Continuous Drain-Source Did				2.1	۸
	waximum conunuous Drain-Source Did				1.2	A V
I <sub>S</sub> V <sub>SD</sub>	Drain-Source Diode Forward Voltage	$V_{GS} = 0 V, I_S = 2.1 A$ (Note 2)		0.8		

FDS6630A

Scale 1 : 1 on letter size paper

**2:** Pulse Test: Pulse Width  $\leq$  300 µs, Duty Cycle  $\leq$  2.0%





FDS6630A Rev. C1

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