

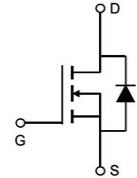
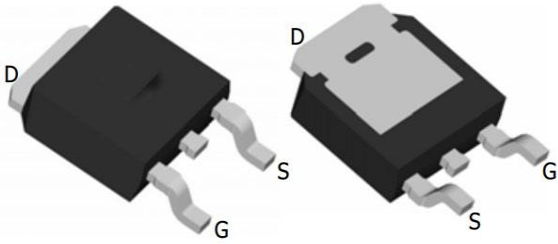
40V /35A Single N Power MOSFET
General Description

40V /35A Single N Power MOSFET

 Very low on-resistance $R_{DS(on)}$ @ $V_{GS}=4.5$ V

Pb-free lead plating; RoHS compliant

V_{DS}	40	V
$R_{DS(on),TYP@V_{GS}=10V}$	13.3	m Ω
$R_{DS(on),TYP@V_{GS}=4.5}$	20.9	m Ω
I_D	35	A



Part ID	Package Type	Marking	Tape and reel information
SM4186T9RL	TO-252	35N04	2500


 100% UIS Tested
 100% Kg Tested

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	20	\pm V
Continuous Drain Current ^A	I_D	$T_A=25^\circ\text{C}$	A
		$T_A=70^\circ\text{C}$	
Pulsed Drain Current ^B	I_{DM}	56.0	
Avalanche Current ^C	I_{AR}	11.2	
Repetitive avalanche energy $L=0.1\text{mH}$ ^G	E_{AR}	25.8	mJ
Power Dissipation ^A	P_D	$T_A=25^\circ\text{C}$	W
		$T_A=70^\circ\text{C}$	
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	$R_{\theta JA}$	47	71	$^\circ\text{C/W}$
Maximum Junction-to-Ambient ^A		Steady State	95	114
Maximum Junction-to-Lead ^C	$R_{\theta JL}$	28	45	$^\circ\text{C/W}$

**STATIC PARAMETERS**

Symbol	Parameter	Conditions	Min	Typ	Max	Units
BV _{DSS}	Drain-Source Breakdown Voltage	I _D = -250uA, V _{GS} = 0V	40			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =40V, V _{GS} =0V			1 5	uA
I _{GSS}	Gate-Body leakage current	V _{DS} = 0V, V _{GS} = ±20V			±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} I _D = 250μA	1.4	2	2.7	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =20A		13.3	19.0	mΩ
		V _{GS} =4.5V, I _D =20A		20.9	27.2	
g _{FS}	Forward Transconductance	V _{DS} =5V, I _D =20A		79		S
V _{SD}	Diode Forward Voltage	I _S =1A, V _{GS} =134V		0.72	1	V
I _S	Maximum Body-Diode Continuous Current				35	A

DYNAMIC PARAMETERS

Symbol	Parameter	Conditions	Min	Typ	Max	Units
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =15V, f=1MHz		980	1195	pF
C _{oss}	Output Capacitance			130	159	pF
C _{rss}	Reverse Transfer Capacitance			80	95	pF
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz			2.25	Ω

SWITCHING PARAMETERS

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Q _g (10V)	Total Gate Charge	V _{GS} =10V, V _{DS} =15V, I _D =20A		9		nC
Q _g 4.5V)	Total Gate Charge			4.5		
Q _{gs}	Gate Source Charge			3.15		
Q _{gd}	Gate Drain Charge			4.5		
t _{D(on)}	Turn-On DelayTime	V _{GS} =10V, V _{DS} =15V, R _L =0.75Ω, R _{GEN} =3Ω		6		ns
t _r	Turn-On Rise Time			4.8		
t _{D(off)}	Turn-Off DelayTime			16.8		
t _f	Turn-Off Fall Time			5.4		
t _{rr}	Body Diode Reverse Recovery Time	I _F =-8A, dI/dt=500A/μs		12		ns
Q _{rr}	Body Diode Reverse Recovery Charge	I _F =18A, dI/dt=500A/μs		31		nC

AL ELECTRICAL AND THERMAL CHARACTERISTICS

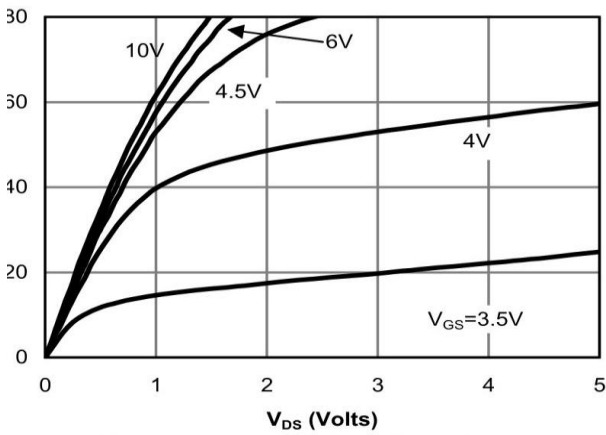


Fig 1: On-Region Characteristics (Note E)

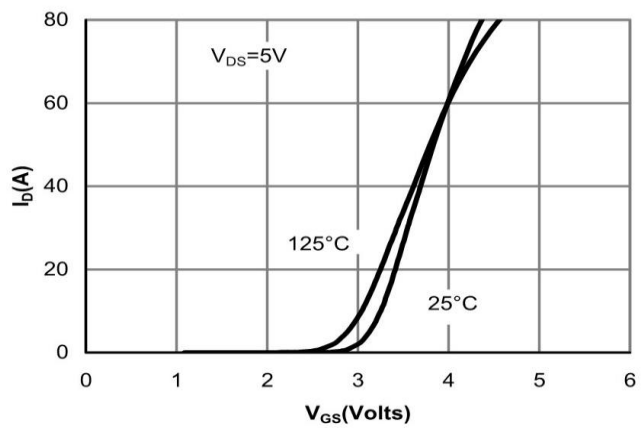


Figure 2: Transfer Characteristics (Note E)

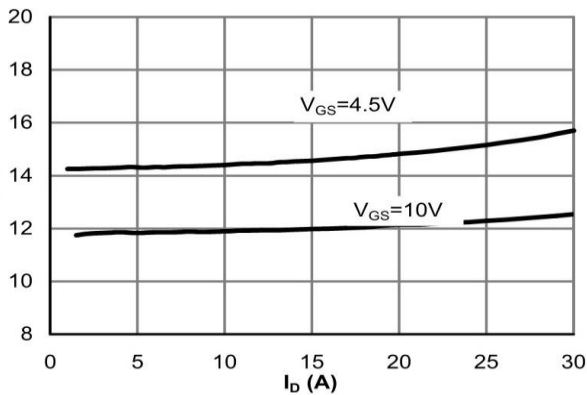


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

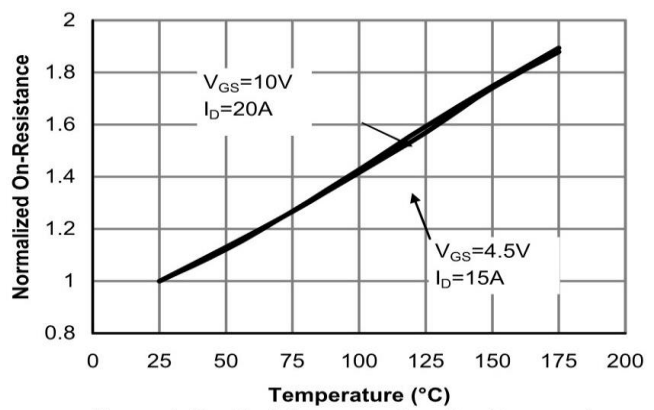


Figure 4: On-Resistance vs. Junction Temperature (Note E)

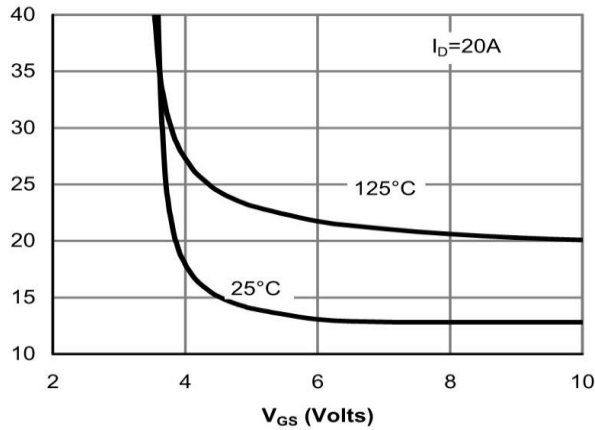


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

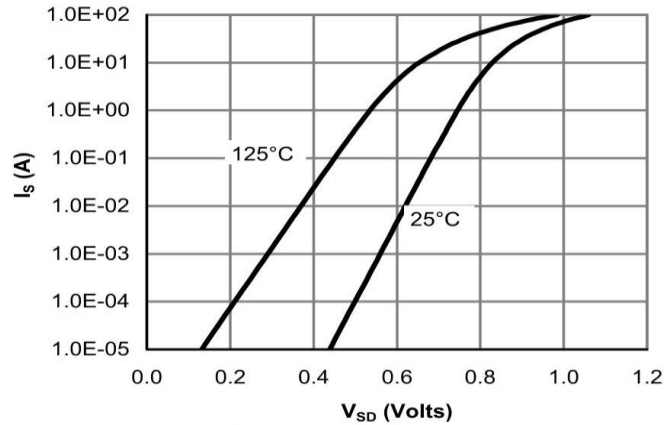


Figure 6: Body-Diode Characteristics (Note E)

AL ELECTRICAL AND THERMAL CHARACTERISTICS

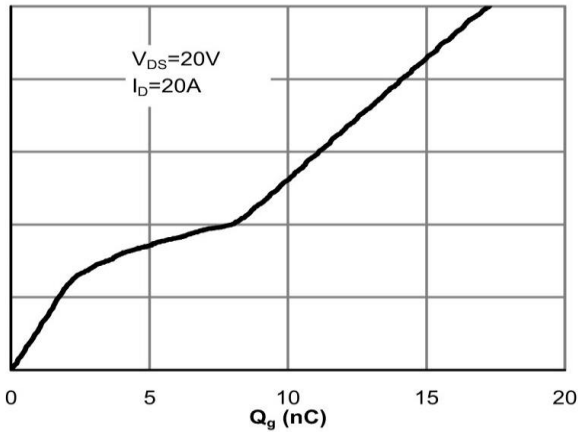


Figure 7: Gate-Charge Characteristics

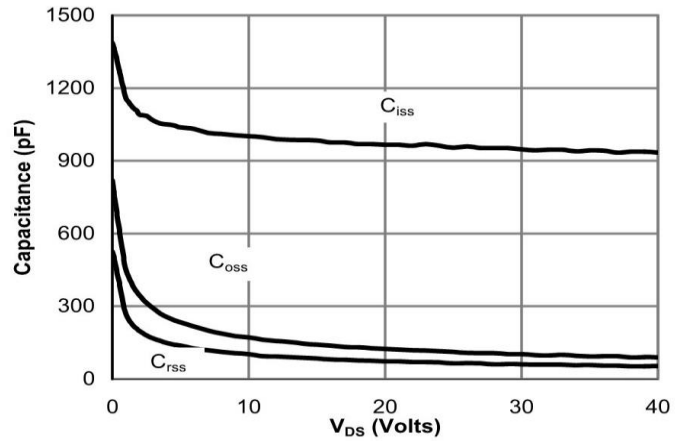


Figure 8: Capacitance Characteristics

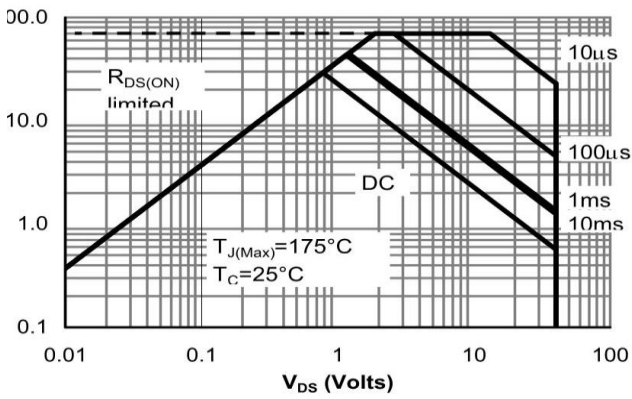


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

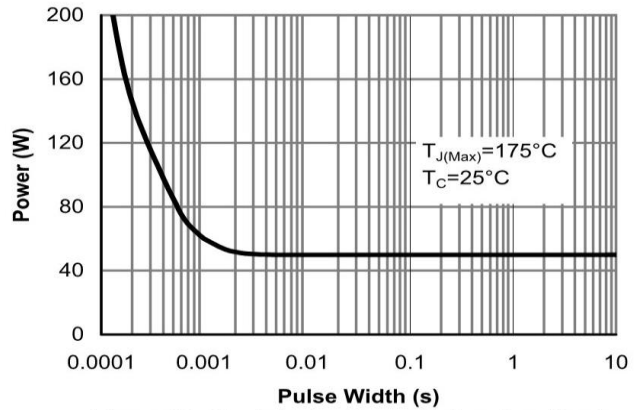


Figure 10: Single Pulse Power Rating Junction-to-Case (Note F)

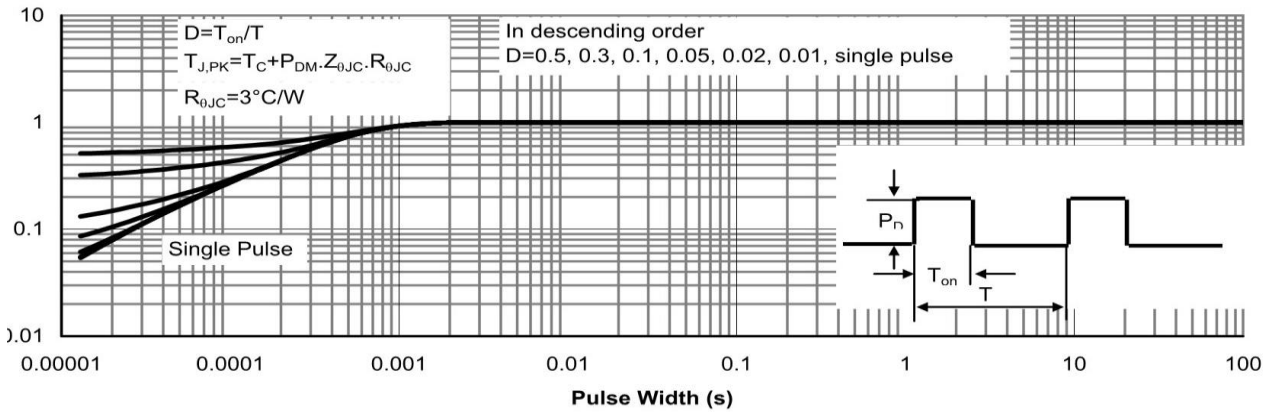


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

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