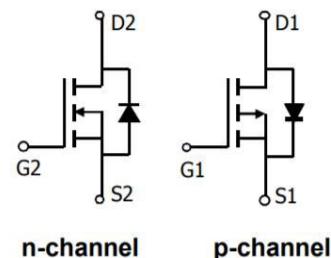
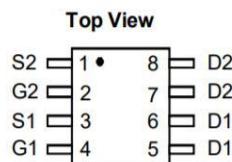
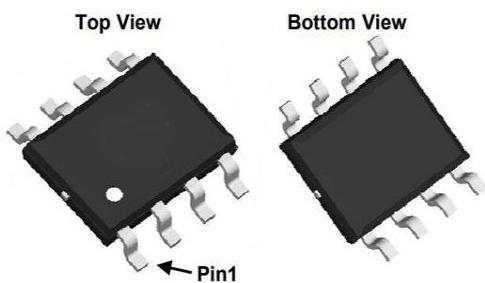


**N+P Complementary Power MOSFET**
**General Description**

0  
Very low on-resistance RDS(on) @ VGS=4.5 V  
Pb-free lead plating; RoHS compliant

	N channel	P channel	V
<b>V<sub>DS</sub></b>	30	-30	
<b>R<sub>DS(on),TYP@VGS=10V</sub></b>	29.4	38.5	mΩ
<b>R<sub>DS(on),TYP@VGS=4.5</sub></b>	46.2	38.5	mΩ
<b>I<sub>D</sub></b>	6	38.5	A



100% UIS Tested

Part ID	Package Type	Marking	Tape and reel infomation
SM4620PRL	SOP8	4620	3000

Parameter	Symbol	Max N-channel	Max P-channel	Units
Drain-Source Voltage	V <sub>DS</sub>	30	-30	V
Gate-Source Voltage	V <sub>GS</sub>	20	20	±V
Continuous Drain Current A	I <sub>D</sub>	6	-5.3	A
		5	-4.5	
Pulsed Drain Current B	I <sub>DM</sub>	9.6	-8.5	A
Avalanche Current G	I <sub>AR</sub>	1.92	-1.7	
Repetitive avalanche energy L=0.1mH G	E <sub>AR</sub>	4.416	-3.9	mJ
Power Dissipation A	P <sub>D</sub>	2	2	W
		1.3	1.44	
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to 150	-55 to 150	°C

**Thermal Characteristics**

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient A	t ≤ 10s	R <sub>θJA</sub>	105	157 °C/W
Maximum Junction-to-Ambient A	Steady State		210	252 °C/W
Maximum Junction-to-Lead c	Steady State	R <sub>θJL</sub>	63	100 °C/W

**N+P Complementary Power MOSFET**
**STATIC PARAMETERS**

<b>Symbol</b>	<b>Parameter</b>	<b>Conditions</b>	<b>Min</b>	<b>Typ</b>	<b>Max</b>	<b>Units</b>
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	I <sub>D</sub> = -250μA, V <sub>GS</sub> = 0V	30			V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V			1	uA
					5	
I <sub>GSS</sub>	Gate-Body leakage current	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V			±100	nA
V <sub>G(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> I <sub>D</sub> = 250μA	1.2	1.8	2.4	V
R <sub>D(on)</sub>	Static Drain-Source On-Resistance	#REF!		29.4	42.0	mΩ
		V <sub>GS</sub> =4.5V, ID=6A		46.2	60.1	
g <sub>F</sub>	Forward Transconductance	V <sub>DS</sub> =5V, ID=6A		83		S
V <sub>SD</sub>	Diode Forward Voltage	I <sub>S</sub> =1A, V <sub>GS</sub> =18V		0.72	1	V
I <sub>S</sub>	Maximum Body-Diode Continuous Current				6	A

**DYNAMIC PARAMETERS**

<b>Symbol</b>	<b>Parameter</b>	<b>Conditions</b>	<b>Min</b>	<b>Typ</b>	<b>Max</b>	<b>Units</b>
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V, V <sub>DS</sub> =15V, f=1MHz		255	311	pF
C <sub>oss</sub>	Output Capacitance			45	55	pF
C <sub>rss</sub>	Reverse Transfer Capacitance			35	41	pF
R <sub>g</sub>	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz			0.65	Ω

**SWITCHING PARAMETERS**

<b>Symbol</b>	<b>Parameter</b>	<b>Conditions</b>	<b>Min</b>	<b>Typ</b>	<b>Max</b>	<b>Units</b>
Q <sub>g</sub> (10V)	Total Gate Charge	V <sub>GS</sub> =10V, V <sub>DS</sub> =15V, ID=6A		2.55		nC
Q <sub>g</sub> 4.5V)	Total Gate Charge			1.275		
Q <sub>gs</sub>	Gate Source Charge			0.91		
Q <sub>gd</sub>	Gate Drain Charge			1.3		
t <sub>D(on)</sub>	Turn-On Delay Time	V <sub>GS</sub> =10V, V <sub>DS</sub> =15V, RL=0.75Ω, R <sub>GEN</sub> =3Ω		4.25		ns
t <sub>r</sub>	Turn-On Rise Time			3.4		
t <sub>D(off)</sub>	Turn-Off Delay Time			11.9		
t <sub>f</sub>	Turn-Off Fall Time			3.825		
t <sub>rr</sub>	Body Diode Reverse Recovery Time	I <sub>F</sub> =-8A, dI/dt=500A/μs		8.5		ns
Q <sub>rr</sub>	Body Diode Reverse Recovery Charge	I <sub>F</sub> =18A, dI/dt=500A/μs		2.2		nC

## N+P Complementary Power MOSFET

### TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

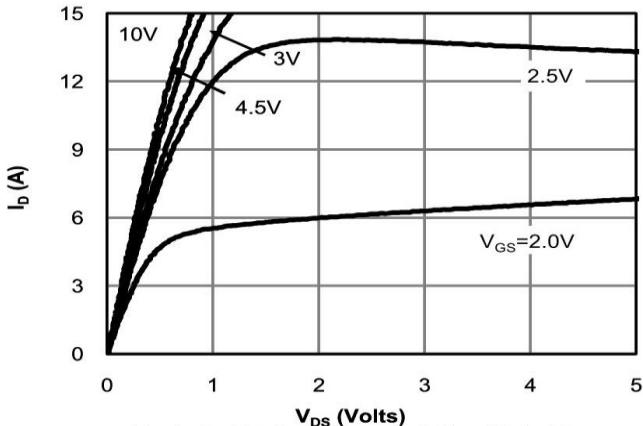


Fig 1: On-Region Characteristics (Note E)

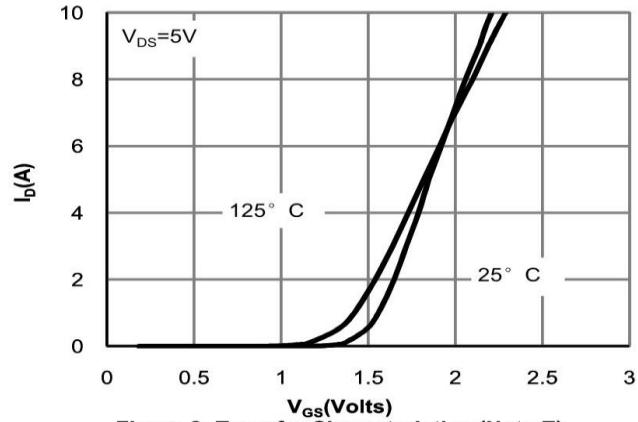


Figure 2: Transfer Characteristics (Note E)

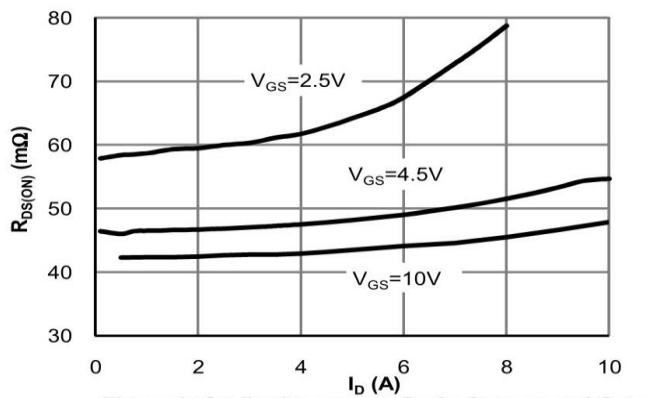


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

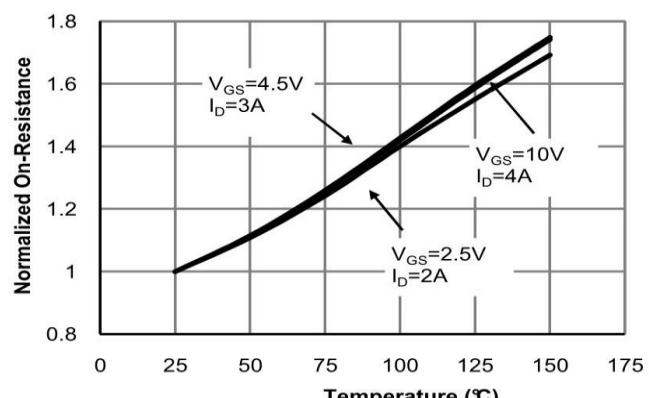
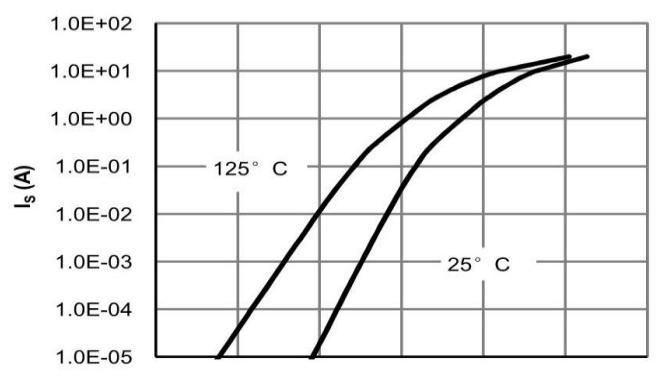
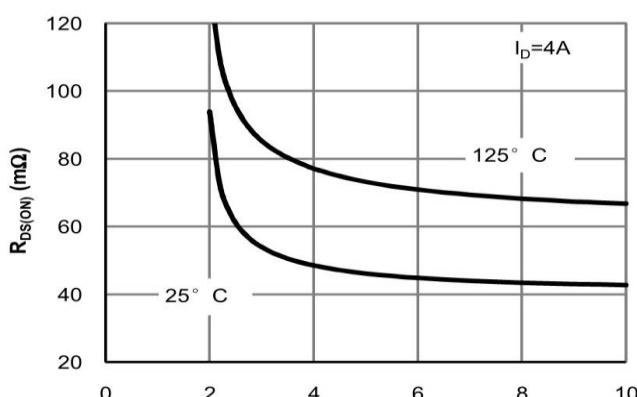


Figure 4: On-Resistance vs. Junction Temperature (Note E)



## N+P Complementary Power MOSFET

### TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

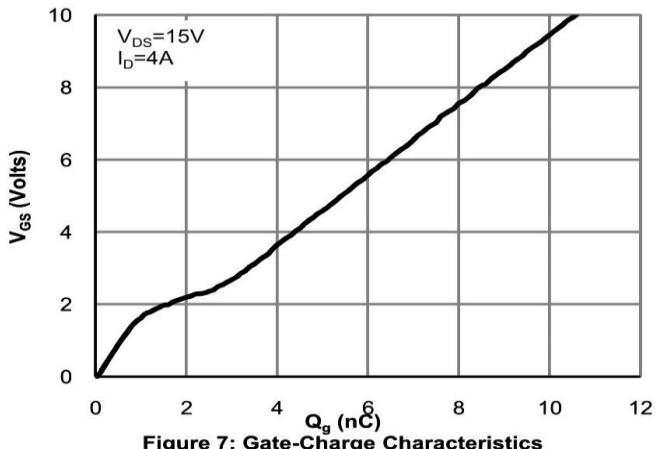


Figure 7: Gate-Charge Characteristics

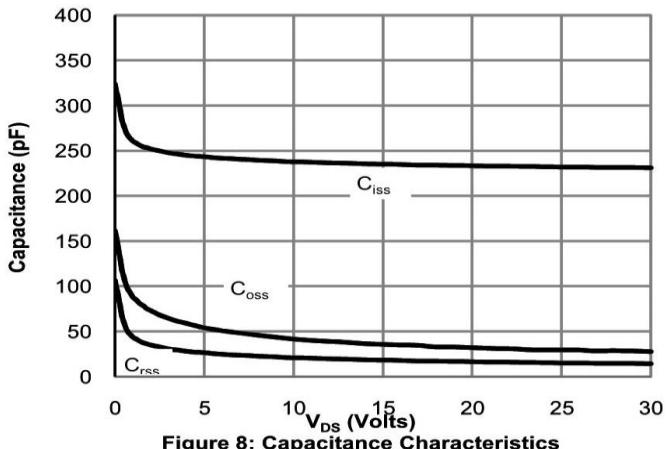


Figure 8: Capacitance Characteristics

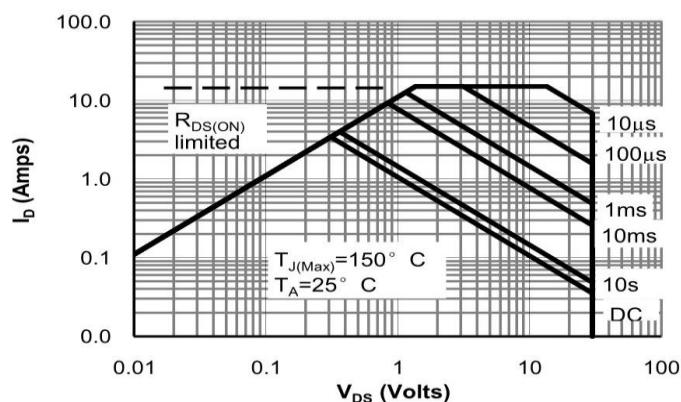


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

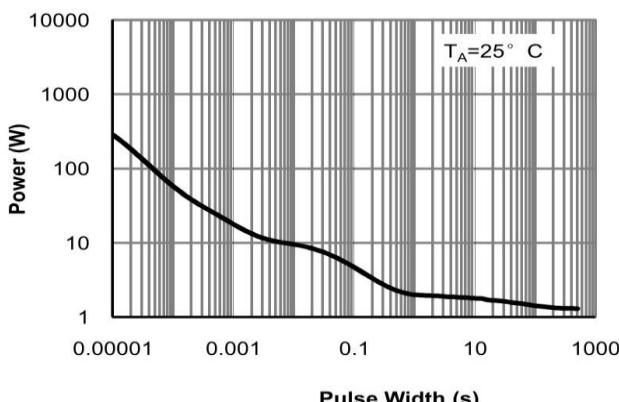


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note F)

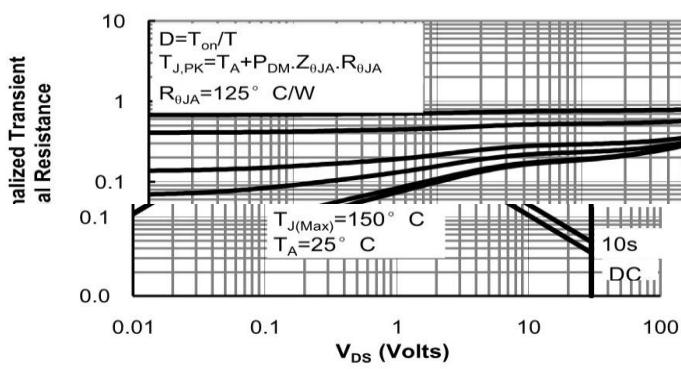


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

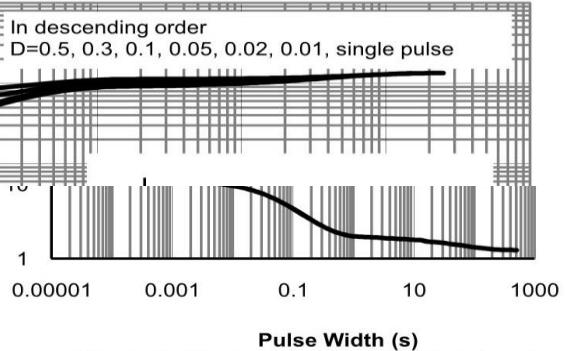
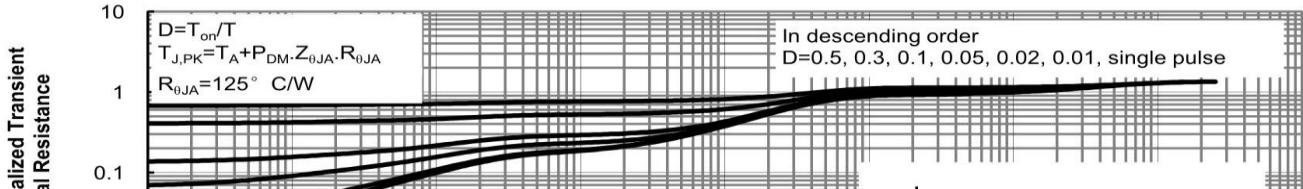


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note F)



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