

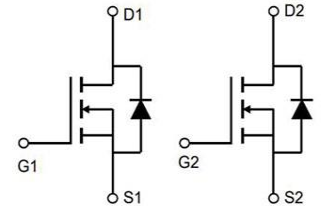
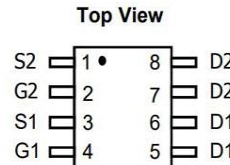
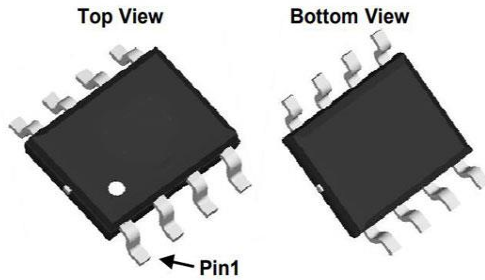
30V /6A Dual 2N Power MOSFET
General Description

30V /6A Dual 2N Power MOSFET

 Very low on-resistance $R_{DS(on)}$ @ $V_{GS}=4.5$ V

Pb-free lead plating; RoHS compliant

| | | |
|-----------------------------|------|------------|
| V_{DS} | 30 | V |
| $R_{DS(on),TYP@V_{GS}=10V}$ | 29.4 | m Ω |
| $R_{DS(on),TYP@V_{GS}=4.5}$ | 46.2 | m Ω |
| I_D | 6 | A |



| | | | |
|-----------|--------------|---------|---------------------------|
| Part ID | Package Type | Marking | Tape and reel information |
| SM4812PRL | SOP8 | 4812 | 3000 |


 100% UIS Tested
 100% Kg Tested

| Parameter | Symbol | Maximum | Units |
|--|----------------|------------------|------------|
| Drain-Source Voltage | V_{DS} | 30 | V |
| Gate-Source Voltage | V_{GS} | 20 | $\pm V$ |
| Continuous Drain Current ^A | I_D | $T_A=25^\circ C$ | A |
| | | $T_A=70^\circ C$ | |
| Pulsed Drain Current ^B | I_{DM} | 9.6 | |
| Avalanche Current ^G | I_{AR} | 1.9 | |
| Repetitive avalanche energy $L=0.1mH$ ^G | E_{AR} | 4.4 | mJ |
| Power Dissipation ^A | P_D | $T_A=25^\circ C$ | W |
| | | $T_A=70^\circ C$ | |
| Junction and Storage Temperature Range | T_J, T_{STG} | -55 to 150 | $^\circ C$ |

Thermal Characteristics

| Parameter | Symbol | Typ | Max | Units |
|--|-----------------|--------------|-----|--------------|
| Maximum Junction-to-Ambient ^A | $R_{\theta JA}$ | 105 | 157 | $^\circ C/W$ |
| Maximum Junction-to-Ambient ^A | | Steady State | 210 | 252 |
| Maximum Junction-to-Lead ^c | $R_{\theta JL}$ | 63 | 100 | $^\circ C/W$ |

**STATIC PARAMETERS**

| Symbol | Parameter | Conditions | Min | Typ | Max | Units |
|---------------------|---------------------------------------|--|-----|--------------|--------------|-------|
| BV _{DSS} | Drain-Source Breakdown Voltage | I _D = -250uA, V _{GS} = 0V | 30 | | | V |
| I _{DSS} | Zero Gate Voltage Drain Current | V _{DS} =30V, V _{GS} =0V | | | 1 5 | uA |
| I _{GSS} | Gate-Body leakage current | V _{DS} = 0V, V _{GS} = ±20V | | | ±100 | nA |
| V _{GS(th)} | Gate Threshold Voltage | V _{DS} = V _{GS} I _D = 250μA | 1.2 | 1.8 | 2.4 | V |
| R _{DS(ON)} | Static Drain-Source On-Resistance | V _{GS} =-10V, I _D =6A V _{GS} =4.5V, I _D =6A | | 29.4 46.2 | 42.0 60.1 | mΩ |
| g _{FS} | Forward Transconductance | V _{DS} =5V, I _D =6A | | 86 | | S |
| V _{SD} | Diode Forward Voltage | I _S =1A, V _{GS} =21V | | 0.72 | 1 | V |
| I _S | Maximum Body-Diode Continuous Current | | | | 6 | A |

DYNAMIC PARAMETERS

| Symbol | Parameter | Conditions | Min | Typ | Max | Units |
|------------------|------------------------------|---|-----|-----|------|-------|
| C _{iss} | Input Capacitance | V _{GS} =0V, V _{DS} =15V, f=1MHz | | 255 | 311 | pF |
| C _{oss} | Output Capacitance | | | 45 | 55 | pF |
| C _{rss} | Reverse Transfer Capacitance | | | 35 | 41 | pF |
| R _g | Gate resistance | V _{GS} =0V, V _{DS} =0V, f=1MHz | | | 0.65 | Ω |

SWITCHING PARAMETERS

| Symbol | Parameter | Conditions | Min | Typ | Max | Units |
|----------------------|------------------------------------|--|-----|-------|-----|-------|
| Q _g (10V) | Total Gate Charge | V _{GS} =10V, V _{DS} =15V, I _D =6A | | 2.55 | | nC |
| Q _g 4.5V) | Total Gate Charge | | | 1.275 | | |
| Q _{gs} | Gate Source Charge | | | 0.91 | | |
| Q _{gd} | Gate Drain Charge | | | 1.3 | | |
| t _{D(on)} | Turn-On DelayTime | V _{GS} =10V, V _{DS} =15V, R _L =0.75Ω, R _{GEN} =3Ω | | 4.25 | | ns |
| t _r | Turn-On Rise Time | | | 3.4 | | |
| t _{D(off)} | Turn-Off DelayTime | | | 11.9 | | |
| t _f | Turn-Off Fall Time | | | 3.825 | | |
| t _{rr} | Body Diode Reverse Recovery Time | I _F =-8A, dI/dt=500A/μs | | 8.5 | | ns |
| Q _{rr} | Body Diode Reverse Recovery Charge | I _F =18A, dI/dt=500A/μs | | 2.2 | | nC |

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

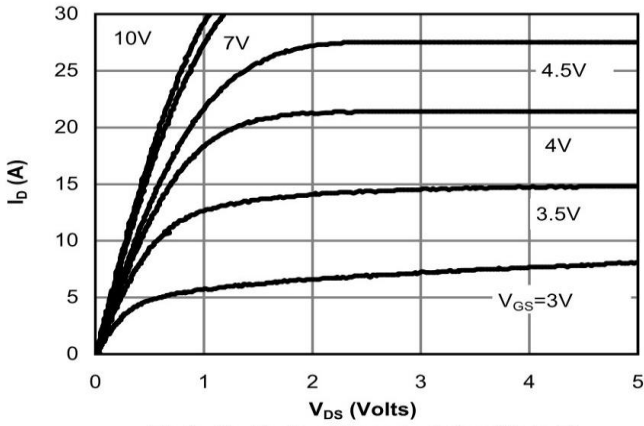


Fig 1: On-Region Characteristics (Note E)

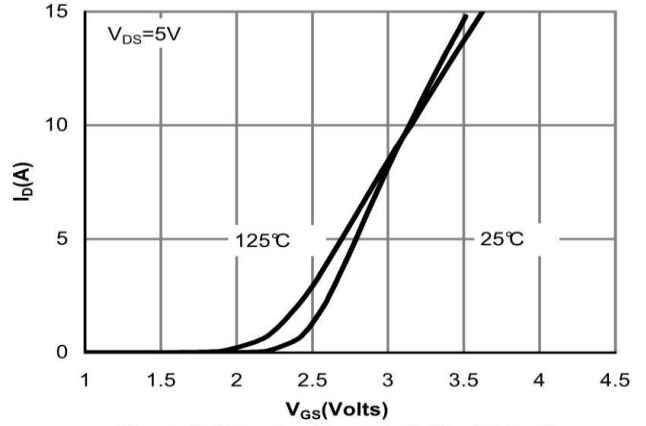


Figure 2: Transfer Characteristics (Note E)

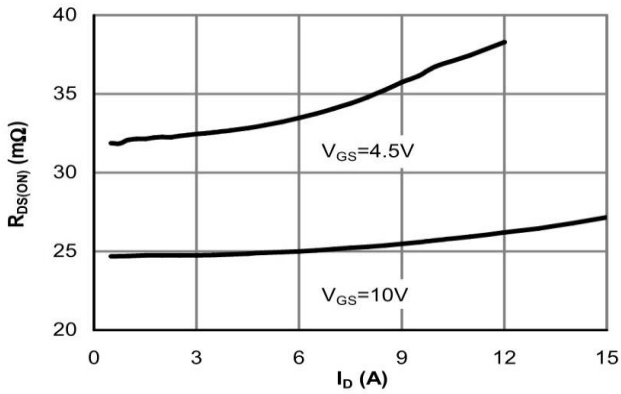


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

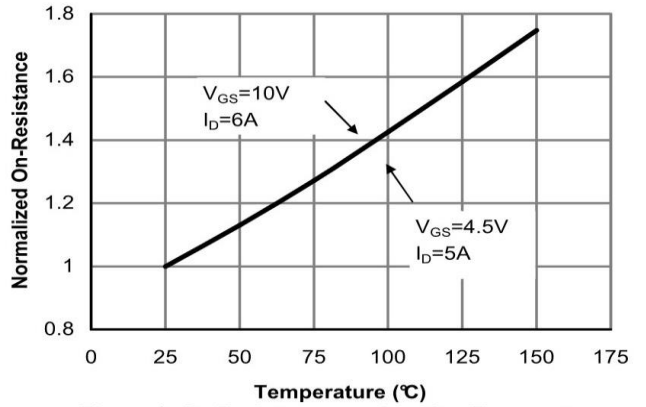


Figure 4: On-Resistance vs. Junction Temperature (Note E)

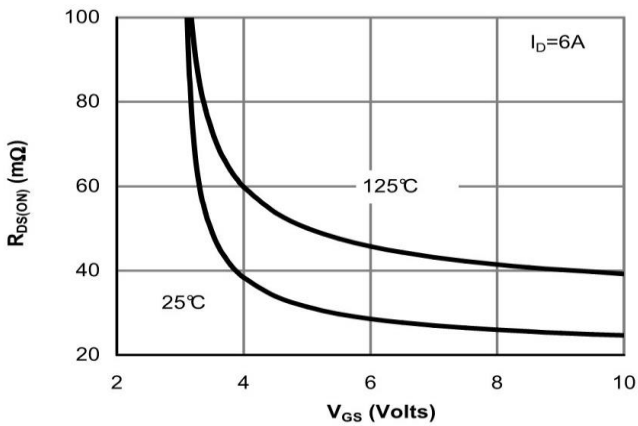


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

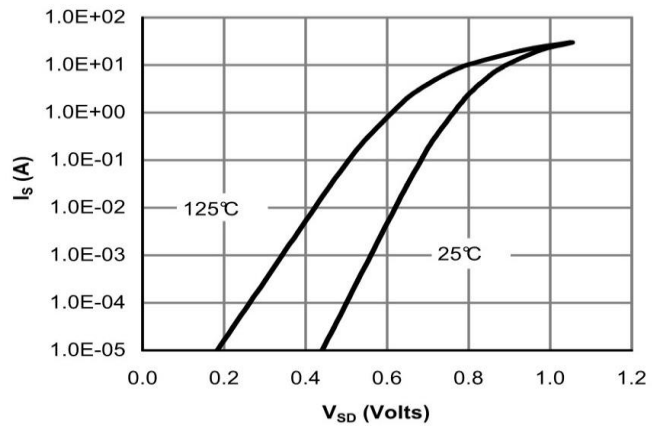


Figure 6: Body-Diode Characteristics (Note E)

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

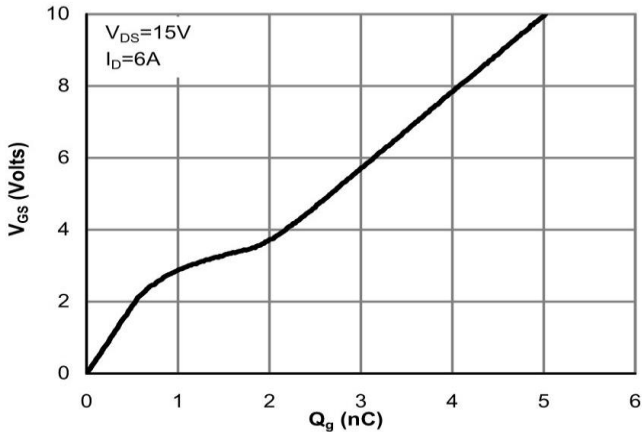


Figure 7: Gate-Charge Characteristics

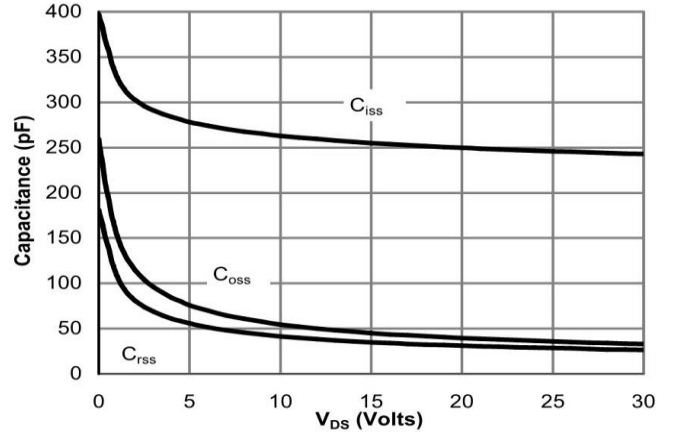


Figure 8: Capacitance Characteristics

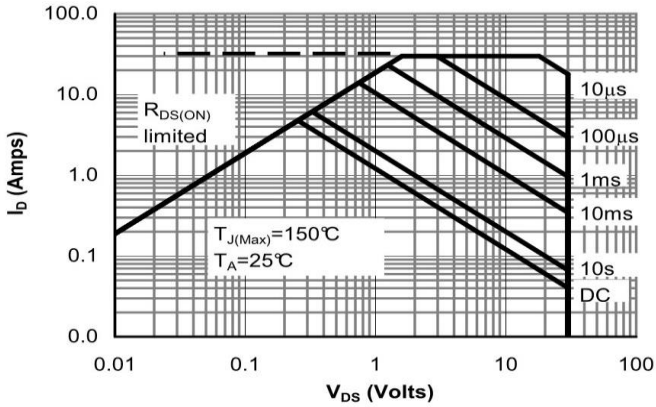


Figure 10: Maximum Forward Biased Safe Operating Area (Note F)

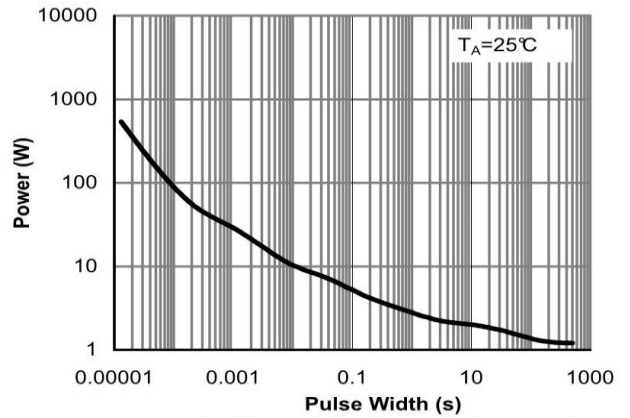


Figure 11: Single Pulse Power Rating Junction-to-Ambient (Note F)

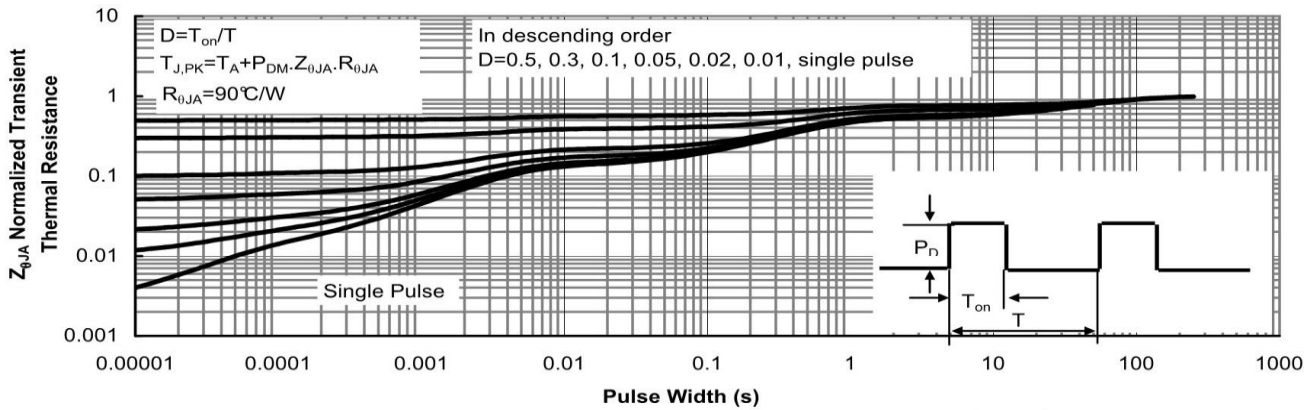


Figure 12: Normalized Maximum Transient Thermal Impedance (Note F)

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