

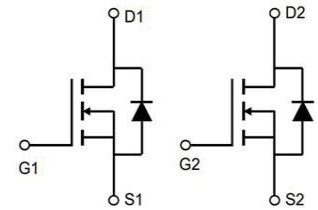
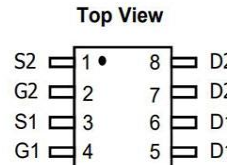
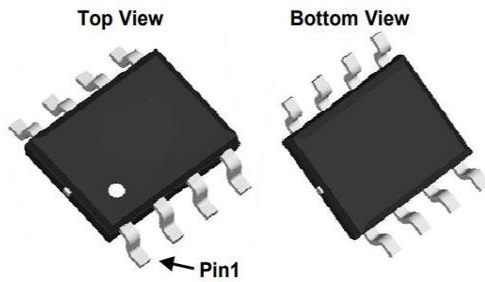
60V /15A Dual 2N Power MOSFET
General Description

60V /15A Dual 2N Power MOSFET

 Very low on-resistance $R_{DS(on)}$ @ $V_{GS}=4.5\text{ V}$

Pb-free lead plating; RoHS compliant

V_{DS}	60	V
$R_{DS(on),TYP@V_{GS}=10V}$	14.0	m Ω
$R_{DS(on),TYP@V_{GS}=4.5}$	25.0	m Ω
I_D	15	A



Part ID	Package Type	Marking	Tape and reel information
SM4828APRL	SOP8	4828A	3000


 100% UIS Tested
 100% Kg Tested

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	20	$\pm V$
Continuous Drain Current ^A	I_D	$T_A=25^\circ C$	A
		$T_A=70^\circ C$	
Pulsed Drain Current ^B	I_{DM}	24.0	
Avalanche Current ^G	I_{AR}	4.8	
Repetitive avalanche energy $L=0.1\text{mH}$ ^G	E_{AR}	11.0	mJ
Power Dissipation ^A	P_D	$T_A=25^\circ C$	W
		$T_A=70^\circ C$	
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ C$

Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	$R_{\theta JA}$	192	288	$^\circ C/W$
Maximum Junction-to-Ambient ^A		Steady State	385	462
Maximum Junction-to-Lead ^c	$R_{\theta JL}$	115	184	$^\circ C/W$



STATIC PARAMETERS

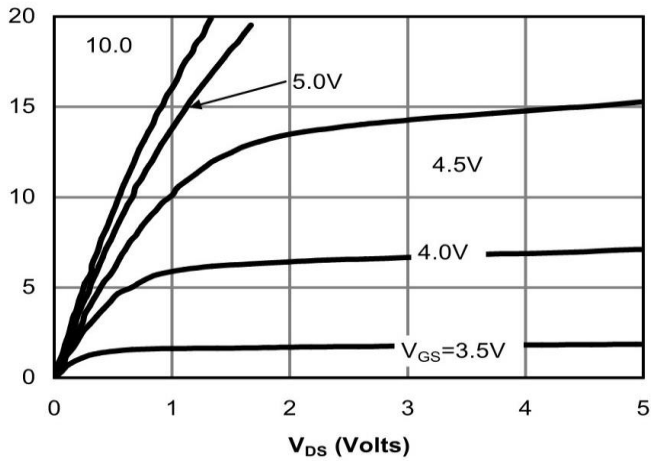
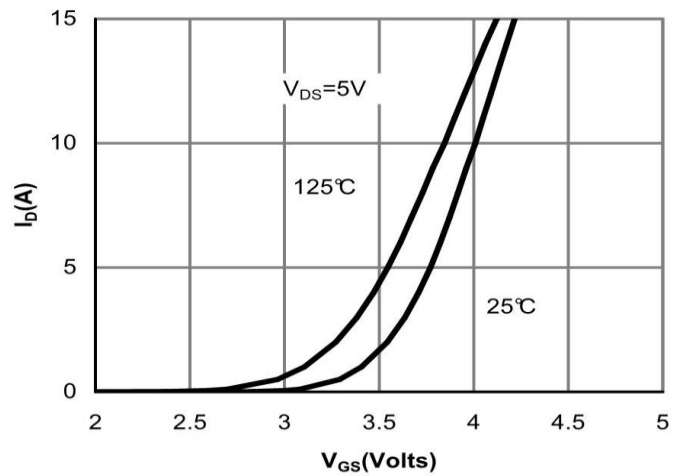
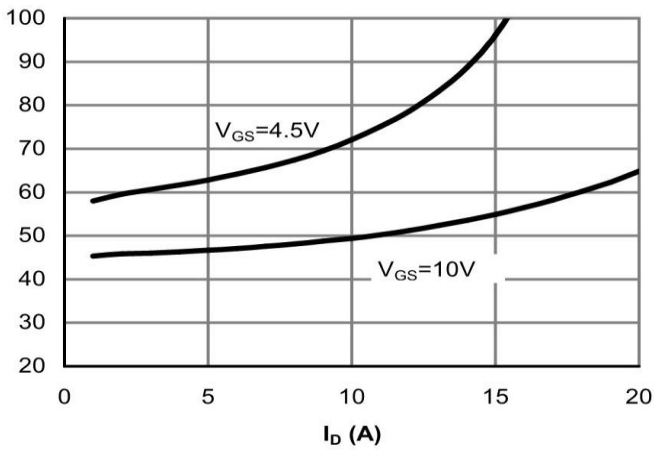
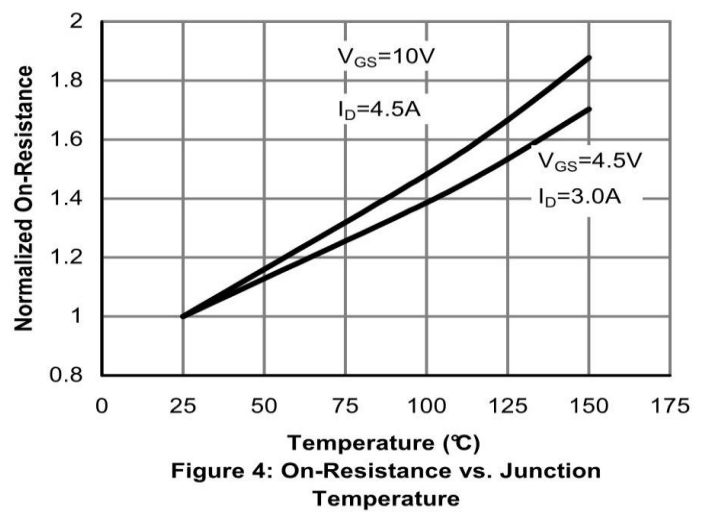
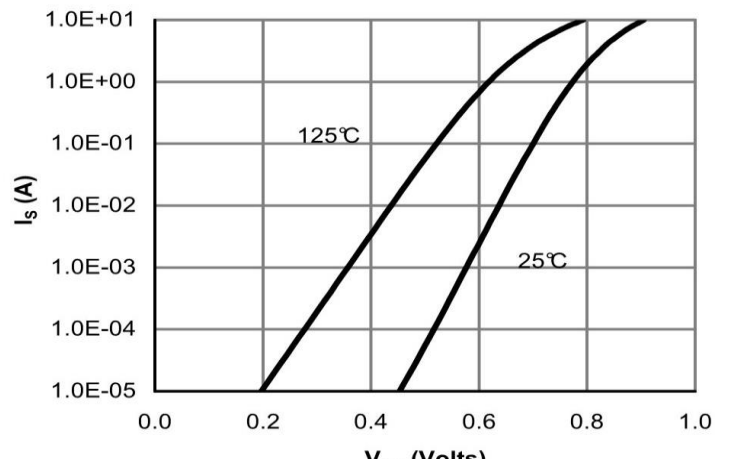
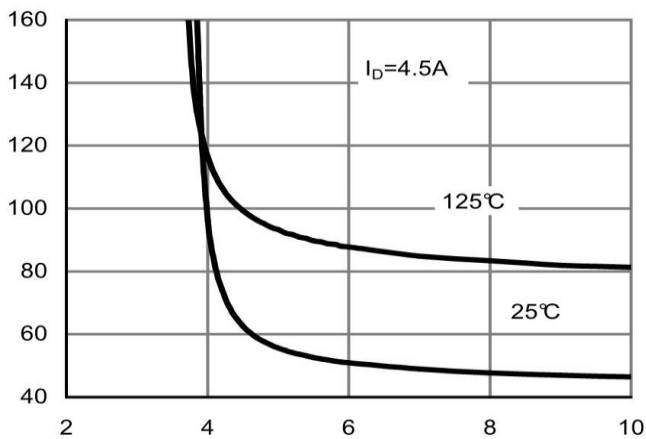
Symbol	Parameter	Conditions	Min	Typ	Max	Units
BV _{DSS}	Drain-Source Breakdown Voltage	I _D = -250uA, V _{GS} = 0V	60			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =60V, V _{GS} =0V			1 5	uA
I _{GSS}	Gate-Body leakage current	V _{DS} = 0V, V _{GS} = ±20V			±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} I _D = 250µA	1	1.5	2.5	V
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =10A V _{GS} =4.5V, I _D =10A		14.0 25.0	18.0 32.5	mΩ
g _{FS}	Forward Transconductance	V _{DS} =5V, I _D =4.5A		78		S
V _{SD}	Diode Forward Voltage	I _S =1A, V _{GS} =13V		0.72	1	V
I _S	Maximum Body-Diode Continuous Current				4.5	A

DYNAMIC PARAMETERS

Symbol	Parameter	Conditions	Min	Typ	Max	Units
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =15V, f=1MHz		450	549	pF
C _{oss}	Output Capacitance			60	73	pF
C _{rss}	Reverse Transfer Capacitance			25	29	pF
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz			1.1	Ω

SWITCHING PARAMETERS

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Q _g (10V)	Total Gate Charge	V _{GS} =10V, V _{DS} =15V, I _D =4.5A		4.3		nC
Q _g 4.5V)	Total Gate Charge			2.15		
Q _{gs}	Gate Source Charge			1.54		
Q _{gd}	Gate Drain Charge			2.2		
t _{D(on)}	Turn-On DelayTime	V _{GS} =10V, V _{DS} =15V, R _L =0.75Ω, R _{GEN} =3Ω		13.75		ns
t _r	Turn-On Rise Time			11		
t _{D(off)}	Turn-Off DelayTime			38.5		
t _f	Turn-Off Fall Time			12.375		
t _{rr}	Body Diode Reverse Recovery Time	I _F =-8A, dI/dt=500A/µs		27.5		ns
Q _{rr}	Body Diode Reverse Recovery Charge	I _F =18A, dI/dt=500A/µs		32		nC

DC ELECTRICAL AND THERMAL CHARACTERISTICS

Fig 1: On-Region Characteristics

Figure 2: Transfer Characteristics

Figure 3: On-Resistance vs. Drain Current and Gate Voltage

Figure 4: On-Resistance vs. Junction Temperature


GENERAL ELECTRICAL AND THERMAL CHARACTERISTICS

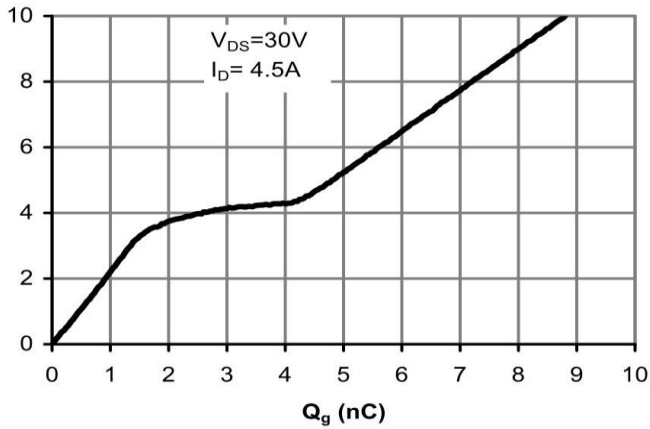


Figure 7: Gate-Charge Characteristics

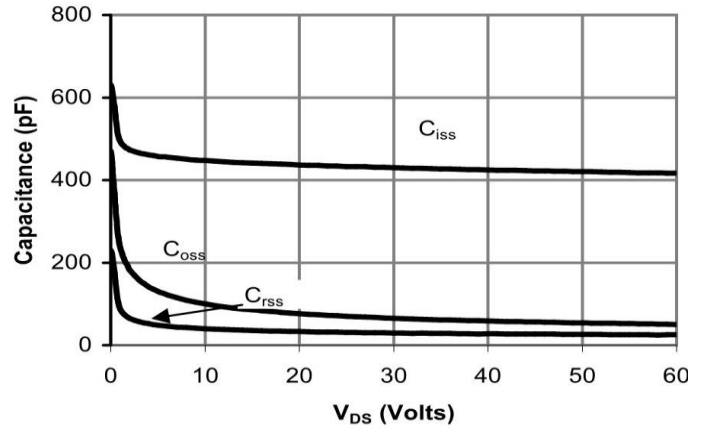


Figure 8: Capacitance Characteristics

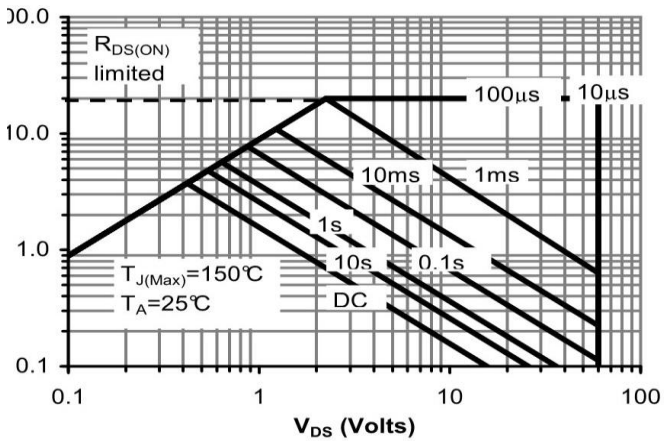


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

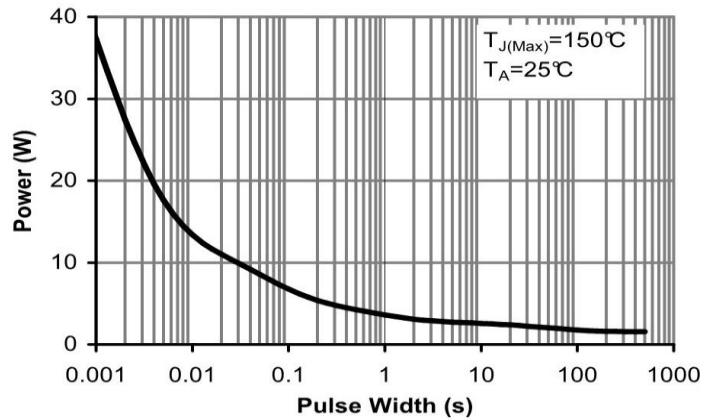
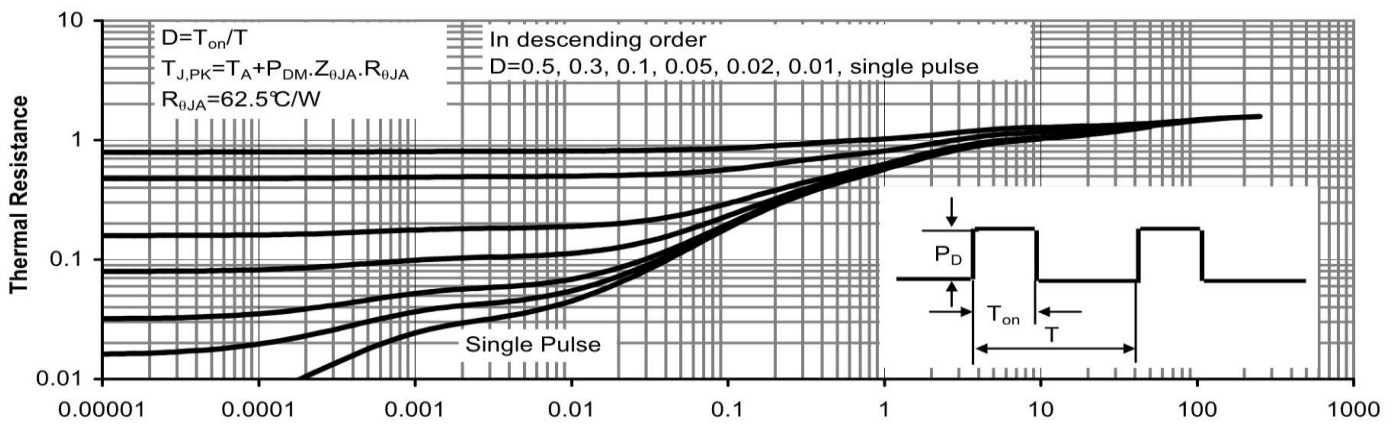


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)



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