

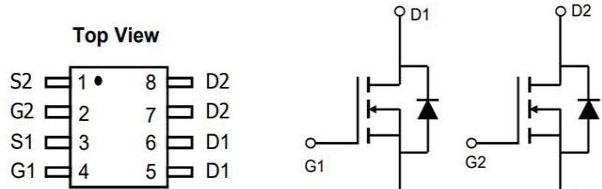
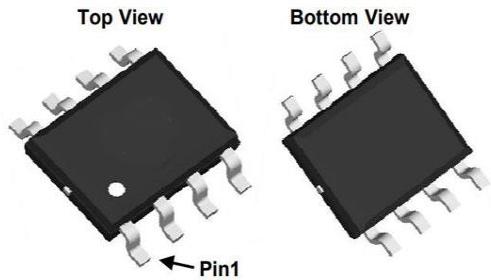
30V /7.7A Dual 2N Power MOSFET
General Description

30V /7.7A Dual 2N Power MOSFET

Very low on-resistance RDS(on) @ VGS=4.5 V

Pb-free lead plating; RoHS compliant

V_{DS}	30	V
R_{DS(on),TYP@VGS=10V}	21.0	mΩ
R_{DS(on),TYP@VGS=4.5}	33.0	mΩ
I_D	7.7	A



Part ID	Package Type	Marking	Tape and reel infomation
SM4842PRL	SOP8	4842	3000


 100% UIS Tested
100% RG Tested

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	20	±V
Continuous Drain Current A	I _D	7.7	A
		6.5	
Pulsed Drain Current B	I _{DM}	12.3	
Avalanche Current G	I _{AR}	2.5	
Repetitive avalanche energy L=0.1mH G	E _{AR}	5.7	mJ
Power Dissipation A	P _D	2	W
		1.44	
Junction and Storage Temperature Range	T _J , T _{STG}	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient A	R _{θJA}	75	112	°C/W
Maximum Junction-to-Ambient A		150	180	°C/W
Maximum Junction-to-Lead c	R _{θJL}	45	72	°C/W

STATIC PARAMETERS

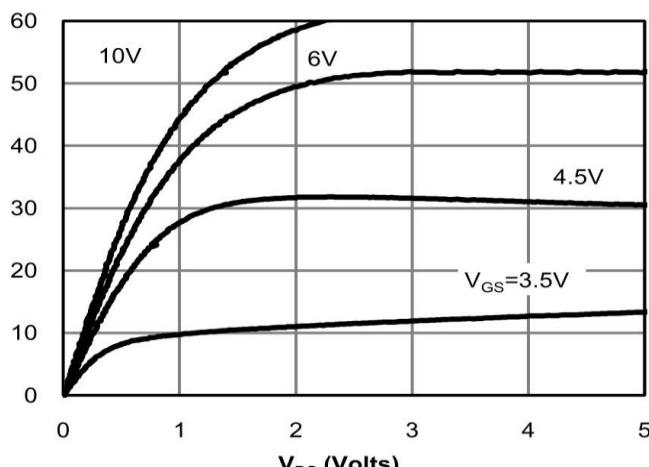
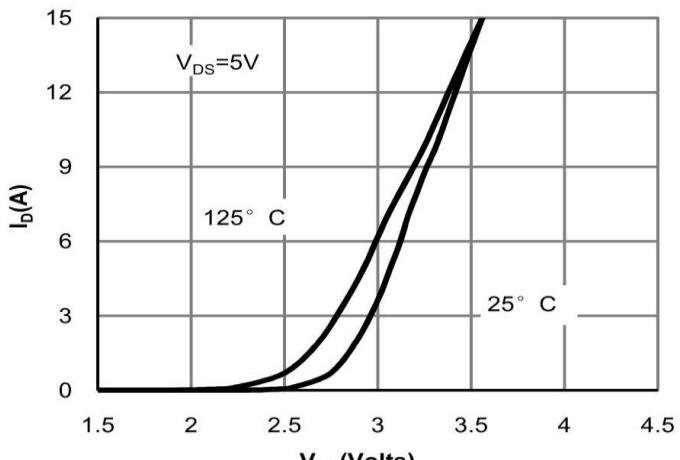
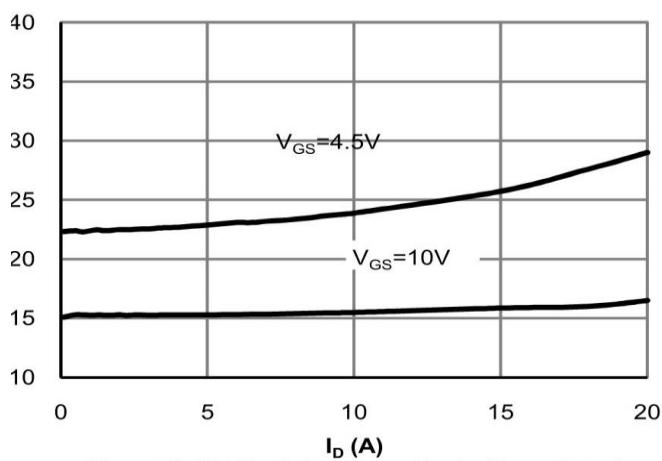
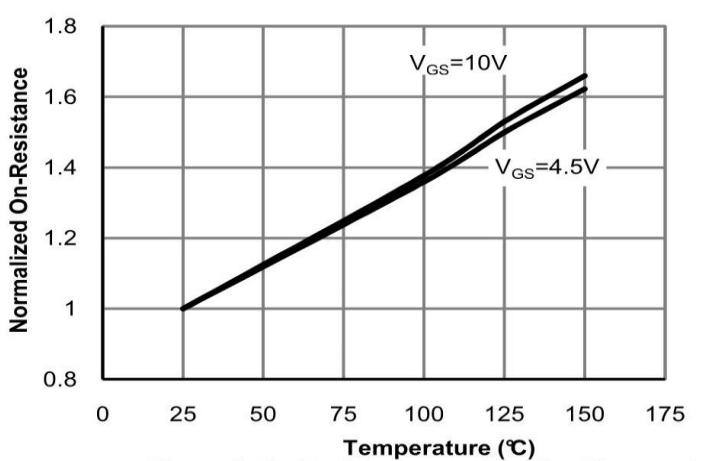
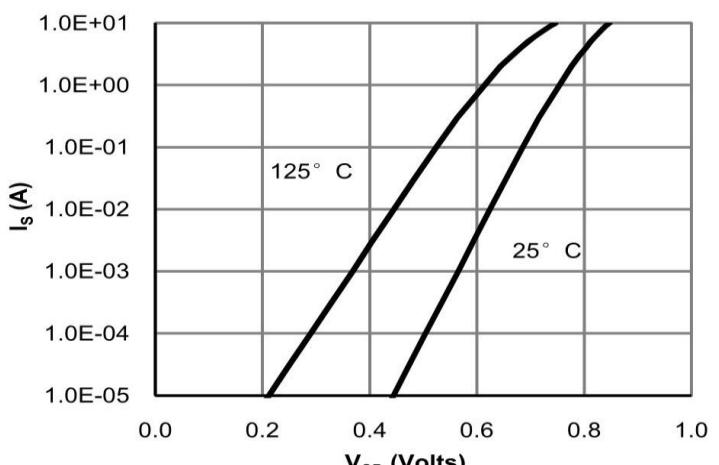
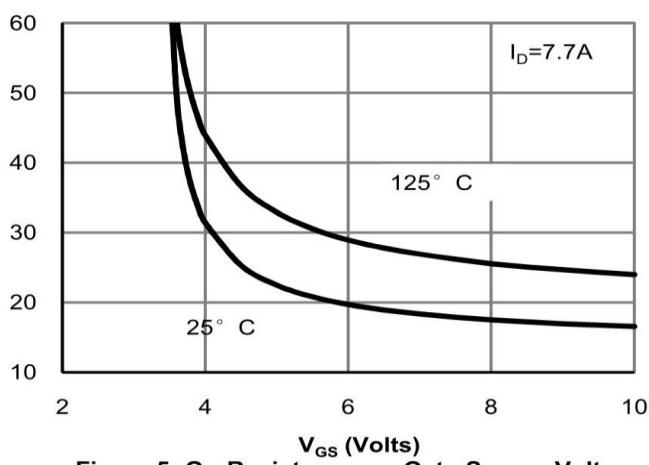
Symbol	Parameter	Conditions	Min	Typ	Max	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D = -250\mu A, V_{GS} = 0V$	30			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=30V, V_{GS}=0V$			1	uA
					5	
I_{GSS}	Gate-Body leakage current	$V_{DS} = 0V, V_{GS} = \pm 20V$			± 100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.3	2	2.6	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=-10V, I_D=7.7A$		21.0	30.0	$m\Omega$
		$V_{GS}=4.5V, I_D=7.7A$		33.0	42.9	
g_{FS}	Forward Transconductance	$V_{DS}=5V, I_D=7.7A$		40		S
V_{SD}	Diode Forward Voltage	$I_S=1A, V_{GS}=35V$		0.72	1	V
I_S	Maximum Body-Diode Continuous Current				7.7	A

DYNAMIC PARAMETERS

Symbol	Parameter	Conditions	Min	Typ	Max	Units
C_{iss}	Input Capacitance	$V_{GS}=0V, V_{DS}=15V, f=1MHz$		373	455	pF
C_{oss}	Output Capacitance			67	82	pF
C_{rss}	Reverse Transfer Capacitance			41	48	pF
R_g	Gate resistance	$V_{GS}=0V, V_{DS}=0V, f=1MHz$			0.85	Ω

SWITCHING PARAMETERS

Symbol	Parameter	Conditions	Min	Typ	Max	Units
$Q_g(10V)$	Total Gate Charge	$V_{GS}=10V, V_{DS}=15V, I_D=7.7A$		3.5		nC
$Q_g 4.5V)$	Total Gate Charge			1.75		
Q_{gs}	Gate Source Charge			1.19		
Q_{gd}	Gate Drain Charge			1.7		
$t_{D(on)}$	Turn-On Delay Time	$V_{GS}=10V, V_{DS}=15V, RL=0.75\Omega, R_{GEN}=3\Omega$		5.25		ns
t_r	Turn-On Rise Time			4.2		
$t_{D(off)}$	Turn-Off Delay Time			14.7		
t_f	Turn-Off Fall Time			4.725		
t_{rr}	Body Diode Reverse Recovery Time	$I_F=-8A, dI/dt=500A/\mu s$		10.5		ns
Q_{rr}	Body Diode Reverse Recovery Charge	$I_F=18A, dI/dt=500A/\mu s$		4.5		nC

CAL ELECTRICAL AND THERMAL CHARACTERISTICS

Fig 1: On-Region Characteristics

Figure 2: Transfer Characteristics

Figure 3: On-Resistance vs. Drain Current and Gate Voltage

Figure 4: On-Resistance vs. Junction Temperature


CAL ELECTRICAL AND THERMAL CHARACTERISTICS

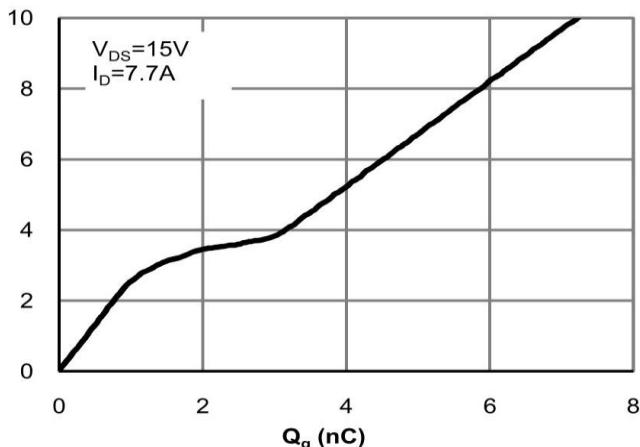
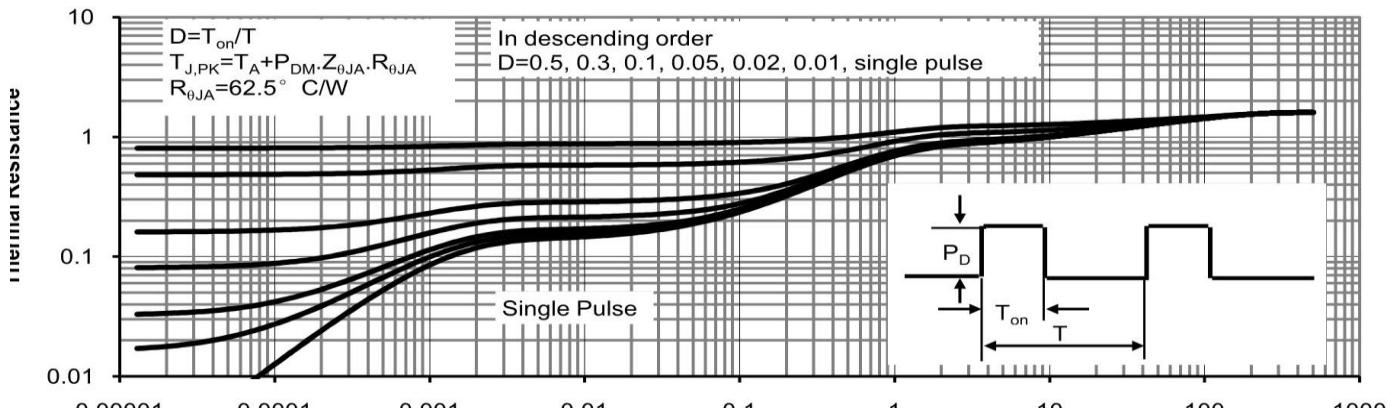
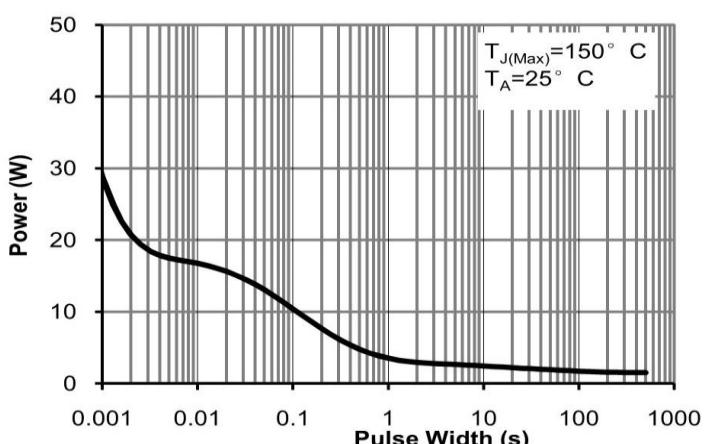
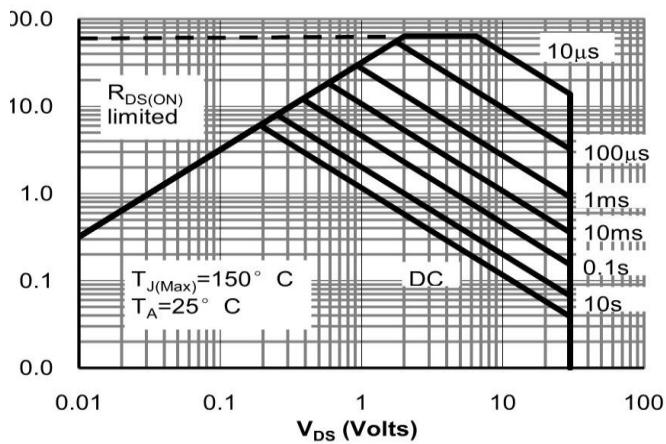
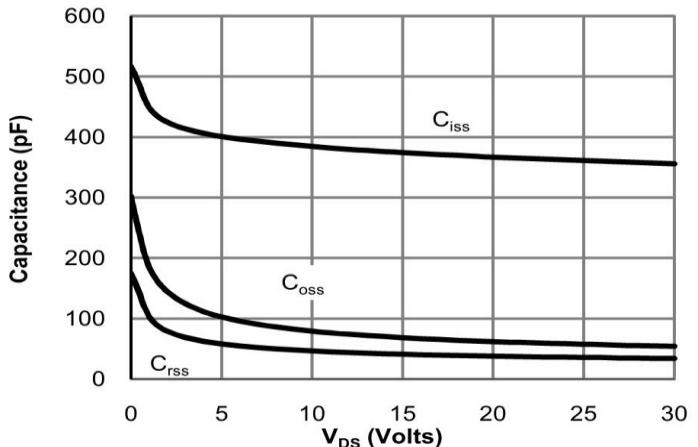


Figure 7: Gate-Charge Characteristics



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