## **SPTECH Silicon PNP Power Transistor**

## 2SA2198

### DESCRIPTION

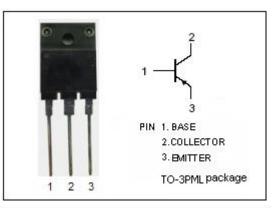
- Low Collector Saturation Voltage
  :V<sub>CE(sat)</sub>= -0.5(V)(Max)@I<sub>C</sub>= -5A
- Good Linearity of  $h_{FE}$

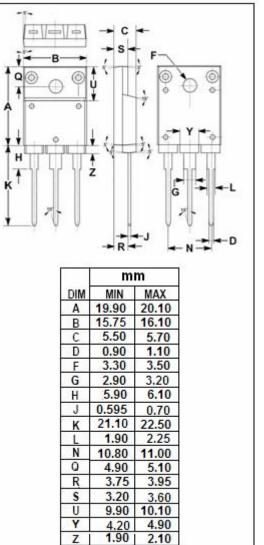
### **APPLICATIONS**

• Designed for chopper regulator, switch and general purpose applications

ABOOLO						
SYMBOL	PARAMETER	VALUE	UNIT			
Vсво	Collector-Base Voltage	-70	V			
V <sub>CEO</sub>	Collector-Emitter Voltage	-50	V			
V <sub>EBO</sub>	Emitter-Base Voltage	-6	V			
lc	Collector Current-Continuous	-12	А			
I <sub>CM</sub>	Collector Current-Peak	-20	A			
Ів	Base Current-Continuous	-4	A			
Pc	Collector Power Dissipation @ Tc=25℃	60	W			
TJ	Junction Temperature	150	°C			
T <sub>stg</sub>	Storage Temperature Range	-55~150	°C			

#### ABSOLUTE MAXIMUM RATINGS(Ta=25℃)





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### **ELECTRICAL CHARACTERISTICS**

Tc=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -25mA ; I <sub>B</sub> = 0	-50			V
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -5A; I <sub>B</sub> = -80mA			-0.5	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> = -5A; I <sub>B</sub> = -80mA			-1.2	v
Ісво	Collector Cutoff Current	V <sub>CB</sub> = -70V ; I <sub>E</sub> = 0			-10	μA
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = -6V; I <sub>C</sub> = 0			-10	μA
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = -5A ; V <sub>CE</sub> = -1V	50			
Сов	Output Capacitance	I <sub>E</sub> = 0 ; V <sub>CB</sub> = -10V;f= 1.0MHz		400		pF
f <sub>T</sub>	Current-Gain—Bandwidth Product	I <sub>E</sub> = 1A ; V <sub>CE</sub> = -12V		25		MHz

#### Switching Times

t <sub>on</sub>	Turn-on Time	I <sub>C</sub> = -5A , R <sub>L</sub> = 4 Ω , I <sub>B1</sub> = -I <sub>B2</sub> = -80mA,V <sub>CC</sub> = -20V	0.5	μs
t <sub>stg</sub>	Storage Time		0.6	μs
t <sub>f</sub>	Fall Time		0.3	μs

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