SPTECH Silicon PNP Power Transistor

NJW1302G

DESCRIPTION

- · With TO-3PN packaging
- · Reliable performance at higher powers
- · Accurate reproduction of Input signal
- · Greater dynamic range
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

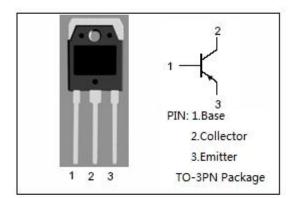
- Switching regulators
- · High frequency inverters
- · General purpose power amplifiers

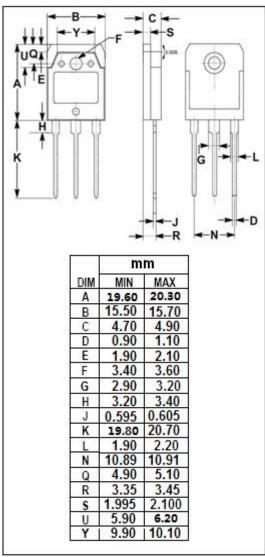
ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V _{CBO}	Collector-Base Voltage	-250	V	
V _{CEO}	Collector-Emitter Voltage	ctor-Emitter Voltage -250		
V _{CEX}	Collector-Emitter Voltage V _{EB} = 5V -250		V	
V _{EBO}	Emitter-Base Voltage -5		V	
Ic	Collector Current-Continuous	ollector Current-Continuous -15		
I _{CM}	Collector Current-Peak	-30	Α	
lв	Base Current-Continuous	-1.6	Α	
P _T	Total Power Dissipation @ T _C =25℃	200	W	
TJ	Junction Temperature	150		
T _{stg}	Storage Temperature Range	ature Range -65~150		

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER		UNIT
R _{th j-c}	Thermal Resistance,Junction to Case		°C/W





SPTECH Product Specification

SPTECH Silicon PNP Power Transistor

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ELECTRICAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	Ic=- 100mA; I _B = 0	-250			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -8A; I _B =- 0.8A			-0.6	V
V _{BE(on)}	Base-Emitter On Voltage	I _C = -8A;V _{CE} = -5V			-1.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} =- 250V			-50	mA
I _{CEO}	Collector Cutoff Current	V _{CE} =- 250V			-50	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} =- 5V			-5	mA
h _{FE-1}	DC Current Gain	I _C = -0.1A; V _{CE} =-5V	75		150	
h _{FE-2}	DC Current Gain	I _C = -1A; V _{CE} = -5V	75		150	
h _{FE-3}	DC Current Gain	I _C = -3A; V _{CE} = -5V	75		150	
h _{FE-4}	DC Current Gain	I _C =- 5A; V _{CE} = -5V	60			
h _{FE-5}	DC Current Gain	I _C =- 8A; V _{CE} = -5V	45			

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