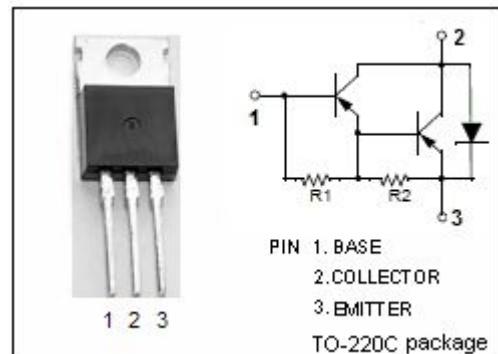


## SPTECH Silicon PNP Darlington Power Transistor

TIP147T

**DESCRIPTION**

- Collector-Emitter Sustaining Voltage-  
:  $V_{CEO(sus)} = -100V$ (Min)
- High DC Current Gain  
:  $h_{FE} = 750$ (Min) @  $I_C = -3A$
- Low Collector Saturation Voltage  
:  $V_{CE(sat)} = -2.0 V$  (Max) @  $I_C = -3.0 A$

**APPLICATIONS**

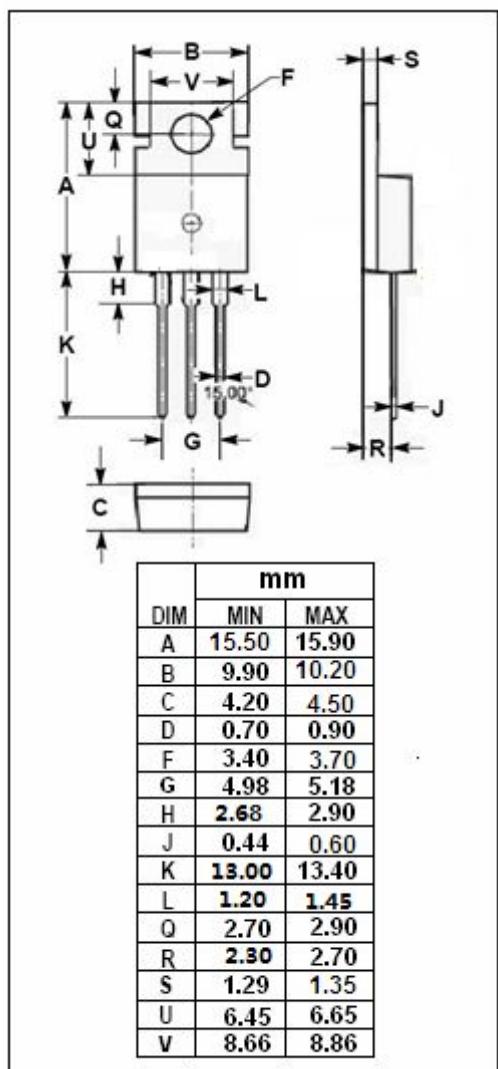
- Designed for general-purpose amplifier and low-speed switching applications.

**ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ C$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	-100	V
$V_{CEO}$	Collector-Emitter Voltage	-100	V
$V_{EBO}$	Emitter-Base Voltage	-5	V
$I_C$	Collector Current-Continuous	-8	A
$I_{CP}$	Collector Current-Peak	-12	A
$I_B$	Base Current-Continuous	-0.2	A
$P_c$	Collector Power Dissipation @ $T_c=25^\circ C$	60	W
$T_J$	Junction Temperature	150	°C
$T_{stg}$	Storage Temperature Range	-65~150	°C

**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(j-c)}$	Thermal Resistance,Junction to Case	1.92	°C/W



# SPTECH Product Specification

## SPTECH Silicon PNP Darlington Power Transistor

TIP147T

### ELECTRICAL CHARACTERISTICS

T<sub>c</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>CEO(sus)</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -50mA; I <sub>B</sub> = 0	-100			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -3A; I <sub>B</sub> = -12mA			-2.0	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> = -3A; I <sub>B</sub> = -12mA			-2.5	V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = -100V; I <sub>E</sub> = 0			-0.2	mA
I <sub>CEO</sub>	Collector Cutoff Current	V <sub>CE</sub> = -50V; I <sub>B</sub> = 0			-0.5	mA
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = -5V; I <sub>C</sub> = 0			-2	mA
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = -3A ; V <sub>CE</sub> = -3V	750			

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