

Low voltage PNP power transistor

Features

- Low saturation voltage
- PNP transistor

Application

■ Audio, power linear and switching equipment

Description

The device is manufactured in planar technology with "base island" layout. The resulting transistor shows exceptional high gain performance coupled with very low saturation voltage. The NPN type is the 2N5192.

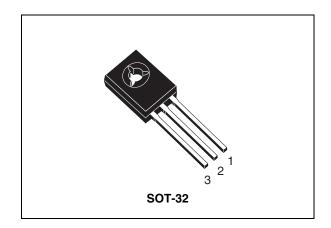


Figure 1. Internal schematic diagram

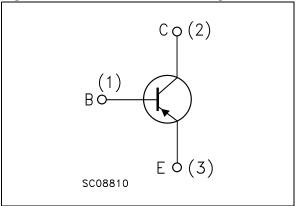


Table 1. Devices summary

Order code	Marking	Package	Packaging
2N5195	2N5195	SOT-32	Tube

Electrical ratings 2N5195

1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-base voltage (I _E = 0)	-80	V
V _{CEO}	Collector-emitter voltage (I _B = 0)	-80	V
V _{EBO}	Emitter-base voltage ($I_C = 0$)	-5	V
I _C	Collector current	-4	Α
I _{CM}	Collector peak current	-7	Α
I _B	Base current	-1	Α
P _{TOT}	Total dissipation at T _{case} = 25 °C	40	W
T _{STG}	Storage temperature -65 t		°C
TJ	Max. operating junction temperature	150	°C

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R _{thJC}	Thermal resistance junction-case Max	3.12	°C/W
R _{thJA}	Thermal resistance junction-ambient Max	100	°C/W

2 Electrical characteristics

 $T_{case} = 25$ °C unless otherwise specified.

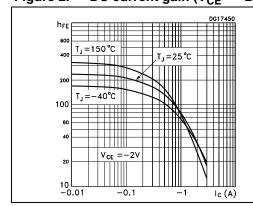
Table 4. Electrical characteristics

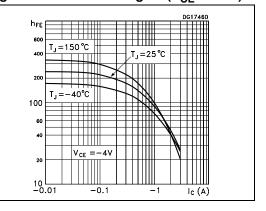
Symbol	Parameter	Test condition	ons Min.	Тур.	Max.	Unit
I _{CBO}	Collector cut-off current (I _E = 0)	V _{CB} = 80 V			-0.1	mA
I _{CEX}	Collector cut-off current (V _{BE} = - 1.5 V)	V _{CE} = 80 V V _{CE} = 80 V T _c =	= 125 °C		-0.1 -2	mA mA
I _{CEO}	Collector cut-off current (I _B = 0)	V _{CE} = 80 V			-1	mA
I _{EBO}	Emitter cut-off current (I _C = 0)	V _{EB} = - 5 V			-1	mA
V _{CEO(sus)} (1)	Collector-emitter sustaining voltage (I _B = 0)	I _C = - 100 mA	-80			V
V _{CE(sat)} (1)	Collector-emitter saturation voltage	$I_C = -1.5 \text{ A}$ $I_B = -1.5 \text{ A}$			-0.6 -1.2	V V
V _{BE(on)} (1)	Base-emitter on voltage	I _C = - 1.5 A V _{CE} :	= - 2 V		-1.2	V
h _{FE}	DC current gain	I _C = - 1.5 A V _{CE} = I _C = - 4 A V _{CE}			80	
f _T	Transition frequency	I _C = - 1 A V _{CE} :	= - 10 V 2			MHz

^{1.} Pulse test: pulse duration \leq 300 μ s, duty cycle \leq 2 %

2.1 Electrical characteristic (curves)

Figure 2. DC current gain $(V_{CE} = -2 V)$ Figure 3. DC current gain $(V_{CE} = -4 V)$

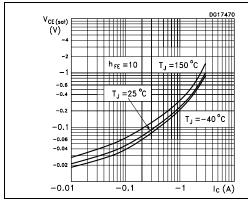




Electrical characteristics 2N5195

Figure 4. Collector-emitter saturation voltage

Figure 5. Base-emitter saturation voltage



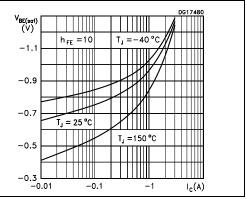
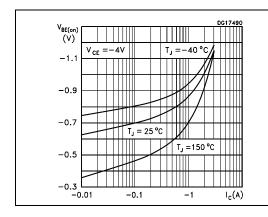


Figure 6. Base-emitter on voltage

Figure 7. Resistive load switching time (on)



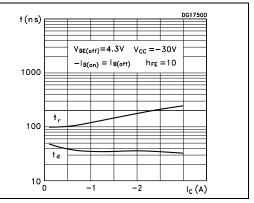
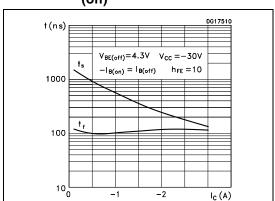
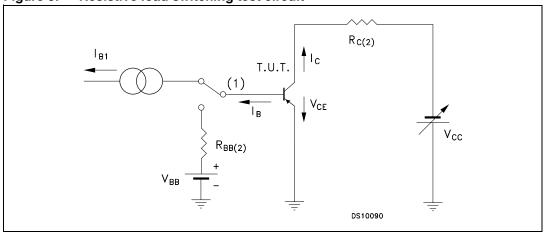


Figure 8. Resistive load switching time (off)



2.2 Test circuit

Figure 9. Resistive load switching test circuit

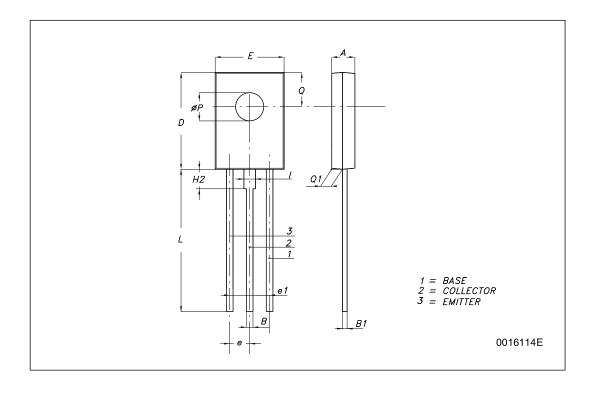


- 1. Fast electronic switch
- 2. Non-inductive resistor

3 Package mechanical data

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DIM.		mm.	
	MIN.	TYP	MAX.
Α	2.4		2.9
В	0.64		0.88
B1	0.39		0.63
D	10.5		11.05
E	7.4		7.8
е	2.04	2.29	2.54
e1	4.07	4.58	5.08
L	15.3		16
Р	2.9		3.2
Q		3.8	
Q1	1		1.52
H2		2.15	
I		1.27	



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Revision history 2N5195

4 Revision history

Table 5. Document revision history

Date	Revision	Changes
21-Jun-2004	3	Document migration, no content change.
02-Nov-2009	4	Updated SOT-32 package mechanical data.

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