## Single bilateral switch

## Features

- High speed:
$\mathrm{t}_{\mathrm{PD}}=0.3 \mathrm{~ns}$ (typ.) at $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$
$\mathrm{t}_{\mathrm{PD}}=0.4 \mathrm{~ns}$ (typ.) at $\mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V}$
- Low power dissipation:
$\mathrm{I}_{\mathrm{CC}}=1 \mu \mathrm{~A}$ (max.) at $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$
- Low "ON" resistance:
$\mathrm{R}_{\mathrm{ON}}=6.5 \Omega$ (typ.) at $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}_{\mathrm{I} / \mathrm{O}}=1 \mathrm{~mA}$
$\mathrm{R}_{\mathrm{ON}}=8.5 \Omega$ (typ.) at $\mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{~V} \mathrm{I}_{\text {I/O }}=1 \mathrm{~mA}$
- Sine wave distortion:
$0.04 \%$ at $\mathrm{V}_{\mathrm{CC}}=3.3 \mathrm{Vf}=1 \mathrm{kHz}$
- Wide operating range:
$\mathrm{V}_{\mathrm{CC}}$ (opr) $=2 \mathrm{~V}$ to 5.5 V
- Improved latch-up immunity


## Description

The 74V1G66 is an advanced high-speed CMOS single bilateral switch fabricated in silicon gate $\mathrm{C}^{2} \mathrm{MOS}$ technology. It achieves high speed propagation delay and very low ON resistances while maintaining true CMOS low power consumption. This bilateral switch handles rail to rail analog and digital signals that may vary across the full power supply range (from GND to $\mathrm{V}_{\mathrm{CC}}$ ).
The C input is provided to control the switch and it is compatible with standard CMOS outputs. The switch is ON (port I/O is connected to port O/l) when the C input is held high and OFF (high impedance state exists between the two ports) when C is held low. It can be used in many applications as battery powered systems or test equipments.


The 74V1G66 is available in the commercial and extended temperature range in SOT23-5L and SOT323-5L packages.
All inputs and outputs are equipped with protection circuits against static discharge, giving them ESD immunity and transient excess voltage.

Table 1. Device summary

| Order code | Package | Packaging |
| :---: | :---: | :---: |
| 74V1G66STR | SOT23-5L | Tape and reel |
| 74V1G66CTR | SOT323-5L | Tape and reel |

## 1

## Pin connection

Figure 1. Pin connection and IEC logic symbols


Table 2. Pin description

| Pin number | Symbol | Name and function |
| :---: | :---: | :--- |
| 1 | I/O | Independent input/output |
| 2 | O/I | Independent output/input |
| 3 | GND | Ground (0 V) |
| 4 | C | Enable input (active high) |
| 5 | $\mathrm{~V}_{\mathrm{CC}}$ | Positive supply voltage |

Figure 2. Input equivalent circuit


Table 3. Truth table

| Control | Switch function |
| :---: | :---: |
| H | ON |
| L | OFF $^{(1)}$ |

1. High impedance state.

## 2 Maximum rating

Stressing the device above the rating listed in the "Absolute Maximum Ratings" table may cause permanent damage to the device. These are stress ratings only and operation of the device at these or any other conditions above those indicated in the Operating sections of this specification is not implied. Exposure to Absolute Maximum Rating conditions for extended periods may affect device reliability.

Table 4. Absolute maximum ratings

| Symbol | Parameter | Value | Unit |
| :---: | :--- | :---: | :---: |
| $\mathrm{V}_{\mathrm{CC}}$ | Supply voltage | -0.5 to +7.0 | V |
| $\mathrm{~V}_{\mathrm{I}}$ | DC input voltage | -0.5 to $\mathrm{V}_{\mathrm{CC}}+0.5$ | V |
| $\mathrm{~V}_{\mathrm{IC}}$ | DC control input voltage | -0.5 to +7.0 | V |
| $\mathrm{~V}_{\mathrm{O}}$ | DC Output voltage | -0.5 to $\mathrm{V}_{\mathrm{CC}}+0.5$ | V |
| $\mathrm{I}_{\mathrm{IK}}$ | DC input diode current | $\pm 20$ | mA |
| $\mathrm{I}_{\mathrm{IK}}$ | DC control input diode current | -20 | mA |
| $\mathrm{I}_{\mathrm{OK}}$ | DC output diode current | $\pm 20$ | mA |
| $\mathrm{I}_{\mathrm{O}}$ | DC output current | $\pm 50$ | mA |
| $\mathrm{I}_{\mathrm{CC}}$ or |  |  |  |
| $\mathrm{I}_{\mathrm{GND}}$ | DC V $\mathrm{V}_{\mathrm{CC}}$ or ground current | $\pm 50$ | mA |
| $\mathrm{~T}_{\text {stg }}$ | Storage temperature | -65 to +150 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\mathrm{L}}$ | Lead temperature (10 sec) | 300 | ${ }^{\circ} \mathrm{C}$ |

Table 5. Recommended operating conditions

| Symbol | Parameter | Value | Unit |
| :---: | :--- | :---: | :---: |
| $\mathrm{V}_{\mathrm{CC}}$ | Supply voltage | 2 to 5.5 | V |
| $\mathrm{~V}_{\mathrm{I}}$ | Input voltage | 0 to $\mathrm{V}_{\mathrm{CC}}$ | V |
| $\mathrm{V}_{\mathrm{IC}}$ | Control input voltage | 0 to 5.5 | V |
| $\mathrm{~V}_{\mathrm{O}}$ | Output voltage | 0 to $\mathrm{V}_{\mathrm{CC}}$ | V |
| $\mathrm{T}_{\mathrm{op}}$ | Operating temperature | -55 to 125 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{dt} / \mathrm{dv}$ | Input rise and fall time ${ }^{(1)} \mathrm{V}_{\mathrm{CC}}=5.0 \mathrm{~V}$ | 0 to 20 | $\mathrm{~ns} / \mathrm{V}$ |

1. $\mathrm{V}_{\mathrm{IN}}$ from $30 \%$ to $70 \%$ of $\mathrm{V}_{\mathrm{CC}}$ on control pin.

Table 6. DC specifications

| Symbol | Parameter | Test condition |  | Value |  |  |  |  |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $V_{c c}$ <br> (V) |  | $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |  |  | -40 to $85^{\circ} \mathrm{C}$ |  | $\begin{aligned} & -55 \text { to } \\ & 125^{\circ} \mathrm{C} \end{aligned}$ |  |  |
|  |  |  |  | Min | Typ | Max | Min | Max | Min | Max |  |
| $\mathrm{V}_{\mathrm{IH}}$ | High level input voltage | 2.0 |  | 1.5 |  |  | 1.5 |  | 1.5 |  | V |
|  |  | $\begin{array}{\|c} 2.7 \text { to } \\ 5.5 \end{array}$ |  | $\begin{gathered} 0.7 \mathrm{~V}_{\mathrm{C}} \\ \mathrm{C} \end{gathered}$ |  |  | $\begin{gathered} 0.7 \mathrm{~V} \\ \mathrm{cc} \\ \hline \end{gathered}$ |  | $\begin{gathered} 0.7 \mathrm{~V} \\ c c \end{gathered}$ |  |  |
| VIL | Low level input voltage | 2.0 |  |  |  | 0.5 |  | 0.5 |  | 0.5 | V |
|  |  | $\begin{array}{\|c} 2.7 \text { to } \\ 5.5 \end{array}$ |  |  |  | $\begin{gathered} 0.3 \mathrm{~V}_{\mathrm{C}} \\ \mathrm{C} \end{gathered}$ |  | $\begin{gathered} 0.3 \mathrm{~V} \\ \mathrm{cc} \end{gathered}$ |  | $\begin{gathered} 0.3 \mathrm{~V} \\ \mathrm{cc} \end{gathered}$ |  |
| $\mathrm{R}_{\mathrm{ON}}$ | ON resistance | $3.3{ }^{(1)}$ | $\begin{gathered} \mathrm{V}_{\mathrm{IC}}=\mathrm{V}_{\mathrm{IH}} \\ \mathrm{~V}_{\mathrm{I} / \mathrm{O}}=\mathrm{V}_{\mathrm{CC}} \text { to } \\ \mathrm{GND} \\ \mathrm{I}_{1 / \mathrm{O}} \leq 1 \mathrm{~mA} \end{gathered}$ |  | 12.5 | 21 |  | 23 |  | 27 | $\Omega$ |
|  |  | $5.0^{(2)}$ |  |  | 7.5 | 10 |  | 12 |  | 14 |  |
| $\mathrm{R}_{\mathrm{ON}}$ | ON resistance | $3.3{ }^{(1)}$ | $\begin{gathered} \mathrm{V}_{\mathrm{IC}}=\mathrm{V}_{\mathrm{IH}} \\ \mathrm{~V}_{1 / \mathrm{O}}=\mathrm{V}_{\mathrm{CC}} \text { or } \\ \text { GND } \\ \mathrm{I}_{\mathrm{I} / \mathrm{O}} \leq 1 \mathrm{~mA} \end{gathered}$ |  | 8.5 | 11.5 |  | 12.5 |  | 15 | $\Omega$ |
|  |  | $5.0^{(2)}$ |  |  | 6.5 | 8.5 |  | 10 |  | 12 |  |
| IOFF | Input/output leakage current (switch OFF) | 5.5 | $\mathrm{V}_{\mathrm{OS}}=\mathrm{V}_{\mathrm{CC}} \text { to }$ GND <br> $V_{\text {IS }}=V_{C C}$ to GND $\mathrm{V}_{\mathrm{IC}}=\mathrm{V}_{\mathrm{IL}}$ |  |  | $\pm 0.1$ |  | $\pm 1$ |  | $\pm 5$ | $\mu \mathrm{A}$ |
| $I_{I Z}$ | Switch input leakage current (switch ON, output open) | 5.5 | $\begin{gathered} V_{O S}=V_{C C} \text { to } \\ G N D \\ V_{I C}=V_{I H} \end{gathered}$ |  |  | $\pm 0.1$ |  | $\pm 1$ |  | $\pm 5$ | $\mu \mathrm{A}$ |
| $\mathrm{I}_{\mathrm{N}}$ | Control input leakage current | $\begin{aligned} & 0 \text { to } \\ & 5.5 \end{aligned}$ | $\begin{gathered} \mathrm{V}_{\mathrm{IC}}=5.5 \mathrm{~V} \text { or } \\ \text { GND } \end{gathered}$ |  |  | $\pm 0.1$ |  | $\pm 1.0$ |  | $\pm 1.0$ | $\mu \mathrm{A}$ |
| $\mathrm{I}_{\mathrm{CC}}$ | Quiescent supply current | 5.5 | $\begin{gathered} V_{\mathrm{I}}=\mathrm{V}_{\mathrm{CC}} \text { or } \\ \text { GND } \end{gathered}$ |  |  | 1 |  | 10 |  | 20 | $\mu \mathrm{A}$ |

1. Voltage range is $3.3 \mathrm{~V} \pm 0.3 \mathrm{~V}$.
2. Voltage range is $5 \mathrm{~V} \pm 0.5 \mathrm{~V}$.

Table 7. AC electrical characteristics ( $C_{L}=50 \mathrm{pF}$, input $\left.\mathrm{t}_{\mathrm{r}}=\mathrm{t}_{\mathrm{f}}=3 \mathrm{~ns}\right)$

| Symbol | Parameter | Test condition |  | Value |  |  |  |  |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $V_{c c}$ <br> (V) |  | $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |  |  | -40 to $85^{\circ} \mathrm{C}$ |  | $\begin{aligned} & -55 \text { to } \\ & 125^{\circ} \mathrm{C} \end{aligned}$ |  |  |
|  |  |  |  | Min | Typ | Max | Min | Max | Min | Max |  |
| $t_{\text {PD }}$ | Delay time | $3.3{ }^{(1)}$ | $\mathrm{t}_{\mathrm{r}}=\mathrm{t}_{\mathrm{f}}=6 \mathrm{~ns}$ |  | 0.4 | 0.8 |  | 1.2 |  | 2.4 | ns |
|  |  | $5.0^{(2)}$ |  |  | 0.3 | 0.6 |  | 1.0 |  | 2.0 |  |
| $\begin{gathered} \mathrm{t}_{\mathrm{PLZ}} \\ \mathrm{t}_{\mathrm{PHZ}} \end{gathered}$ | Output disable time | $3.3^{(1)}$ | $\mathrm{R}_{\mathrm{L}}=500 \Omega$ |  | 5.0 | 7.5 |  | 9.0 |  | 10.0 | ns |
|  |  | $5.0^{(2)}$ |  |  | 5.0 | 7.5 |  | 9.0 |  | 10.0 |  |
| $\begin{aligned} & \mathrm{t}_{\mathrm{PZL}} \\ & \mathrm{t}_{\mathrm{PZH}} \end{aligned}$ | Output enable time | $3.3^{(1)}$ | $\mathrm{R}_{\mathrm{L}}=1 \mathrm{~K} \Omega$ |  | 2.5 | 4.0 |  | 5.0 |  | 7.0 | ns |
|  |  | $5.0^{(2)}$ |  |  | 2.0 | 4.0 |  | 5.0 |  | 7.0 |  |

1. Voltage range is $3.3 \mathrm{~V} \pm 0.3 \mathrm{~V}$.
2. Voltage range is $5 \mathrm{~V} \pm 0.5 \mathrm{~V}$.

Table 8. Capacitive characteristics

| Symbol | Parameter | Test condition |  | Value |  |  |  |  |  |  | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | $\mathrm{V}_{\mathrm{Cc}}$ <br> (V) |  | $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ |  |  | -40 to $85^{\circ} \mathrm{C}$ |  | $\begin{aligned} & -55 \text { to } \\ & 125^{\circ} \mathrm{C} \end{aligned}$ |  |  |
|  |  |  |  | Min | Typ | Max | Min | Max | Min | Max |  |
| $\mathrm{C}_{\mathrm{IN}}$ | Input capacitance |  |  |  | 3 | 10 |  | 10 |  | 10 | pF |
| $\mathrm{C}_{\text {/ }}$ | Output capacitance |  |  |  | 10 |  |  |  |  |  | pF |
| $\mathrm{C}_{\text {PD }}$ | Power dissipation capacitance ${ }^{(1)}$ | 3.3 |  |  | 2.5 |  |  |  |  |  | pF |
|  |  | 5.0 |  |  | 3 |  |  |  |  |  |  |

1. $\mathrm{C}_{P D}$ is defined as the value of the IC's internal equivalent capacitance which is calculated from the operating current consumption without load. (Refer to the test circuit). Average operating current can be obtained by the following equation. $\mathrm{I}_{\mathrm{CC}}(\mathrm{opr})=\mathrm{C}_{\mathrm{PD}} \times \mathrm{V}_{\mathrm{CC}} \times \mathrm{f}_{\mathrm{IN}}+\mathrm{I}_{\mathrm{CC}}$.

Table 9. Analog switch characteristics (GND =0 V; $\mathrm{T}_{\mathrm{A}}=\mathbf{2 5}{ }^{\circ} \mathrm{C}$ )


1. Voltage range is $3.3 \mathrm{~V} \pm 0.3 \mathrm{~V}$
2. Voltage range is $5.0 \mathrm{~V} \pm 0.5 \mathrm{~V}$

## 3 Switching characteristics test circuit

Figure 3. Enable and disable timing (circuit) Figure 4. Enable and disable timing (waveform)


Figure 5. Resistive feedthrough attenuation
Figure 6. Capacitive feedthrough attenuation


AM00744V1

Figure 7. Bandwidth attenuation
Figure 8. Maximum control frequency


Figure 9. Crosstalk (control to output)


Figure 10. Channel resistance ( $\mathrm{R}_{\mathrm{ON}}$ )


Figure 11. $\mathrm{I}_{\mathrm{CC}}$ (opr)
$\square$

## 4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK ${ }^{\circledR}$ packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.
SOT23-5L MECHANICAL DATA

| DIM. | mm. |  |  | mils |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | MIN. | TYP | MAX. | MIN. | TYP. | MAX. |
| A | 0.90 |  | 1.45 | 35.4 |  | 57.1 |
| A1 | 0.00 |  | 0.10 | 0.0 |  | 3.9 |
| A2 | 0.90 |  | 1.30 | 35.4 |  | 51.2 |
| b | 0.35 |  | 0.50 | 13.7 |  | 19.7 |
| C | 0.09 |  | 0.20 | 3.5 |  | 7.8 |
| D | 2.80 |  | 3.00 | 110.2 |  | 118.1 |
| E | 1.50 |  | 1.75 | 59.0 |  | 68.8 |
| e |  | 0.95 |  |  | 37.4 |  |
| H | 2.60 |  | 3.00 | 102.3 |  | 118.1 |
| L | 0.10 |  | 0.60 | 3.9 |  | 23.6 |



## SOT323-5L MECHANICAL DATA

| DIM. | mm. |  |  | mils |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | MIN. | TYP | MAX. | MIN. | TYP. | MAX. |
| A | 0.80 |  | 1.10 | 31.5 |  | 43.3 |
| A1 | 0.00 |  | 0.10 | 0.0 |  | 3.9 |
| A2 | 0.80 |  | 1.00 | 31.5 |  | 39.4 |
| b | 0.15 |  | 0.30 | 5.9 |  | 11.8 |
| C | 0.10 |  | 0.18 | 3.9 |  | 7.1 |
| D | 1.80 |  | 2.20 | 70.9 |  | 86.6 |
| E | 1.80 |  | 2.40 | 70.9 |  | 94.5 |
| E1 | 1.15 |  | 1.35 | 45.3 |  |  |
| e |  | 0.65 |  |  | 25.6 |  |
| e1 |  | 1.3 |  | 51.2 |  |  |
| L | 0.10 |  | 0.30 | 3.9 |  | 11.8 |



Tape \& Reel SOT23-xL MECHANICAL DATA

| DIM. | mm. |  |  | inch |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | MIN. | TYP | MAX. | MIN. | TYP. | MAX. |
| A |  |  | 180 |  |  | 7.086 |
| C | 12.8 | 13.0 | 13.2 | 0.504 | 0.512 | 0.519 |
| D | 20.2 |  |  | 0.795 |  |  |
| N | 60 |  |  | 2.362 |  |  |
| T |  |  | 14.4 |  |  | 0.567 |
| Ao | 3.13 | 3.23 | 3.33 | 0.123 | 0.127 | 0.131 |
| Bo | 3.07 | 3.17 | 3.27 | 0.120 | 0.124 | 0.128 |
| Ko | 1.27 | 1.37 | 1.47 | 0.050 | 0.054 | 0.058 |
| Po | 3.9 | 4.0 | 4.1 | 0.153 | 0.157 | 0.161 |
| P | 3.9 | 4.0 | 4.1 | 0.153 | 0.157 | 0.161 |



Tape \& Reel SOT323-xL MECHANICAL DATA

| DIM. | mm. |  |  | inch |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | MIN. | TYP | MAX. | MIN. | TYP. | MAX. |
| A | 175 | 180 | 185 | 6.889 | 7.086 | 7.283 |
| C | 12.8 | 13 | 13.2 | 0.504 | 0.512 | 0.519 |
| D | 20.2 |  |  | 0.795 |  |  |
| N | 59.5 | 60 | 60.5 |  | 2.362 |  |
| T |  |  | 14.4 |  |  | 0.567 |
| Ao |  | 2.25 |  |  | 0.106 |  |
| Bo |  | 2.7 |  |  | 0.047 |  |
| Ko |  | 1.2 |  |  | 0.157 | 0.161 |
| Po | 3.9 | 4 | 4.1 | 0.153 | 0.149 | 0.157 |
| P | 3.8 | 4 | 4.2 | 0.165 |  |  |



Note: Drawing not in scale

## 5 Revision history

Table 10. Document revision history

| Date | Revision | Changes |
| :---: | :---: | :--- |
| 01-Apr-2004 | 7 | Updated data reel information. |
| 28-Oct-2008 | 8 | Document reformatted to improve readability. <br> Updated Table 6: $D C$ specifications on page 4. |

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