## Features

- On-state rms current: 30 A
- Blocking voltage: up to 1200 V
- Gate current: 50 mA
- UL 2500 V insulation (file ref E81734)


## Description

Available in a high power insulated package, the BTW68 series is suitable for applications where power handling and power dissipation are critical such as solid state relays, welding equipment and high power motor control.
Based on a clip assembly technology, this device offers a superior performance in surge current handling capabilities.
Thanks to the internal ceramic pad, the device provides high voltage insulation ( $2500 \mathrm{~V}_{\mathrm{RMS}}$ ) and complies with UL standards (file ref: E81734).


Table 1. Device summary

| Symbol | Value |
| :---: | :---: |
| $\mathrm{I}_{\mathrm{T} \text { RMS })}$ | 30 A |
| $\mathrm{~V}_{\text {DRM }} / \mathrm{V}_{\text {RRM }}$ | 600 to 1200 V |
| $\mathrm{I}_{\mathrm{GT}}$ | 50 mA |

## Characteristics

Table 2. Absolute maximum ratings (limiting values)

| Symbol | Parameter |  |  | Value | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{I}_{\text {(RMS) }}$ | On-state current rms ( $180^{\circ}$ conduction angle) |  | $\mathrm{T}_{\mathrm{C}}=80^{\circ} \mathrm{C}$ | 30 | A |
| $\mathrm{IT}_{(\mathrm{AV})}$ | Average on-state current ( $180^{\circ}$ conduction angle) |  | $\mathrm{T}_{\mathrm{C}}=80^{\circ} \mathrm{C}$ | 19 | A |
| $I_{\text {TSM }}$ | Non repetitive surge peak on-state current | $\mathrm{t}_{\mathrm{p}}=8.3 \mathrm{~ms}$ | $\mathrm{T}_{\mathrm{j}}=25^{\circ} \mathrm{C}$ | 420 | A |
|  |  | $\mathrm{t}_{\mathrm{p}}=10 \mathrm{~ms}$ |  | 400 |  |
| $I^{2} t$ | $I^{2}$ t Value for fusing |  | $\mathrm{T}_{\mathrm{j}}=25^{\circ} \mathrm{C}$ | 800 | $A^{2} s$ |
| dl/dt | Critical rate of rise of on-state current $\mathrm{I}_{\mathrm{G}}=2 \times \mathrm{I}_{\mathrm{GT}}, \mathrm{t}_{\mathrm{r}} \leq 100 \mathrm{~ns}$ | $\mathrm{F}=60 \mathrm{~Hz}$ | $\mathrm{T}_{\mathrm{j}}=125^{\circ} \mathrm{C}$ | 100 | A/ $/ \mathrm{s}$ |
| $\mathrm{I}_{\mathrm{GM}}$ | Peak gate current | $\mathrm{t}_{\mathrm{p}}=20 \mu \mathrm{~s}$ | $\mathrm{T}_{\mathrm{j}}=125^{\circ} \mathrm{C}$ | 8 | A |
| $\mathrm{P}_{\mathrm{G}(\mathrm{AV})}$ | Average gate power dissipation |  | $\mathrm{T}_{\mathrm{j}}=125^{\circ} \mathrm{C}$ | 1 | W |
| $\begin{aligned} & \mathrm{T}_{\mathrm{stg}} \\ & \mathrm{~T}_{\mathrm{j}} \end{aligned}$ | Storage junction temperature range Operating junction temperature range |  |  | $\begin{aligned} & -40 \text { to }+150 \\ & -40 \text { to }+125 \end{aligned}$ | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{V}_{\text {RGM }}$ | Maximum peak reverse gate voltage |  |  | 5 | V |

Table 3. Electrical characteristics ( $\mathrm{T}_{\mathrm{j}}=\mathbf{2 5}{ }^{\circ} \mathrm{C}$, unless otherwise specified)

| Symbol | Test conditions |  |  |  | Value | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{I}_{\text {GT }}$ | $\mathrm{V}_{\mathrm{D}}=12 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=33 \Omega$ |  |  | MIN. | 50 | mA |
| $V_{G T}$ |  |  |  | MAX. | 1.5 | V |
| $V_{G D}$ | $\mathrm{V}_{\mathrm{D}}=\mathrm{V}_{\mathrm{DRM},} \mathrm{R}_{\mathrm{L}}=3.3 \mathrm{k} \Omega$ |  | $\mathrm{T}_{\mathrm{j}}=125^{\circ} \mathrm{C}$ | MIN. | 0.2 | V |
| $\mathrm{t}_{\mathrm{gt}}$ | $\mathrm{V}_{\mathrm{D}}=\mathrm{V}_{\mathrm{DRM}}, \mathrm{I}_{\mathrm{G}}=200 \mathrm{~mA}, \mathrm{dl}_{\mathrm{G}} / \mathrm{dt}=1.5 \mathrm{~A} / \mu \mathrm{s}$ |  |  | TYP. | 2 | $\mu \mathrm{s}$ |
| $\mathrm{I}_{\mathrm{H}}$ | $\mathrm{I}_{\mathrm{T}}=500 \mathrm{~mA}$, gate open |  |  | MAX. | 75 | mA |
| $\mathrm{I}_{\mathrm{L}}$ | $\mathrm{I}_{\mathrm{G}}=1.2 \times \mathrm{I}_{\mathrm{GT}}$ |  |  | TYP. | 40 | mA |
| dV/dt | $V_{D}=67 \% V_{D R M}$ gate open | $\mathrm{V}_{\text {DRM }}=800 \mathrm{~V}$ | $\mathrm{T}_{\mathrm{j}}=125^{\circ} \mathrm{C}$ | MIN. | 500 | V/ $/ \mathrm{s}$ |
|  |  | $\mathrm{V}_{\text {DRM }}=1000 \mathrm{~V}$ |  |  | 250 |  |
| $\mathrm{V}_{\text {TM }}$ | $\mathrm{I}_{\text {TM }}=60 \mathrm{~A}, \mathrm{t}_{\mathrm{p}}=380 \mu \mathrm{~s}$ |  |  | MAX. | 2.1 | V |
| IDRM <br> IRRM | $\mathrm{V}_{\text {DRM }}=\mathrm{V}_{\text {RRM }}$ |  | $\mathrm{T}_{\mathrm{j}}=25^{\circ} \mathrm{C}$ | MAX. | 20 | $\mu \mathrm{A}$ |
|  |  |  | $\mathrm{T}_{\mathrm{j}}=125^{\circ} \mathrm{C}$ |  | 6 | mA |
| $\mathrm{t}_{\mathrm{q}}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{D}}=67 \% \mathrm{~V}_{\mathrm{DRM}}, \mathrm{I}_{\mathrm{TM}}=60 \mathrm{~A}, \mathrm{~V}_{\mathrm{R}}=75 \mathrm{~V} \\ & \mathrm{dl}_{\mathrm{TM}} / \mathrm{dt}=30 \mathrm{~A} / \mu \mathrm{s}, \mathrm{dV}_{\mathrm{D}} / \mathrm{dt}=20 \mathrm{~V} / \mu \mathrm{s} \end{aligned}$ |  | $\mathrm{T}_{\mathrm{j}}=125^{\circ} \mathrm{C}$ | TYP. | 100 | $\mu \mathrm{s}$ |

Table 4. Thermal resistance

| Symbol | Parameter | Value | Unit |
| :---: | :--- | :---: | :---: |
| $\mathrm{R}_{\text {th(j-c) }}$ | Junction to case (D.C.) | 1.1 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| $\mathrm{R}_{\text {th(j-a) }}$ | Junction to ambient | 50 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

Figure 1. Maximum average power dissipation versus average on-state current


Figure 2. Correlation between maximum average power dissipation and maximum allowable temperature


Figure 3. Average on-state current versus case Figure 4. Relative variation of thermal temperature impedance versus pulse duration


Figure 5. Relative variation of gate trigger current versus junction temperature



Figure 6. Surge peak on-state current versus number of cycles


Figure 7. Non repetitive surge peak on-state Figure 8. On-state characteristics current and corresponding value of (maximum values) $I^{2} t$ versus sinusoidal pulse width


## 2 Ordering information scheme

Figure 9. Ordering information scheme


Table 5. Product Selector

| Part numbers | Voltage (xxx) |  |  |  | Sensitivity | Package |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | 600 V | 800 V | 1000 V | 1200 V |  |  |
| BTW68-600RG | X |  |  |  | 50 mA | TOP3 Ins. |
| BTW68-800RG |  | X |  |  |  |  |
| BTW68-1000RG |  |  | X |  |  |  |
| BTW68-1200RG |  |  |  | X |  |  |

## 3 Package information

- Epoxy meets UL94,V0
- Lead-free packages

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK ${ }^{\circledR}$ packages, depending on their level of environmental compliance. ECOPACK ${ }^{\circledR}$ specifications, grade definitions and product status are available at: www.st.com.
ECOPACK ${ }^{\circledR}$ is an ST trademark.
Table 6. TOP3 ins. dimensions


## 4 Ordering information

Table 7. Ordering information

| Order code | Marking | Package | Weight | Base qty | Delivery mode |
| :---: | :---: | :---: | :---: | :---: | :---: |
| BTW68-600RG | BTW68-600 | TOP3 ins. | 4.5 g | 30 | Tube |
| BTW68-800RG | BTW68-800 |  |  |  |  |
| BTW68-1000RG | BTW68-1000 |  |  |  |  |
| BTW68-1200RG | BTW68-1200 |  |  |  |  |

## 5 Revision history

Table 8. Document revision history

| Date | Revision | Changes |
| :---: | :---: | :--- |
| Mar-1995 | 1 | Initial release. |
| 13-Feb-2006 | 2 | TOP3 Insulated delivery mode changed from bulk to tube. <br> ECOPACK statement added. |
| 29-Jul-2010 | 3 | Deleted part number BTW68-200RG. Updated Table 2, Figure 7 and <br> alpha angle in Figure 1. |

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