

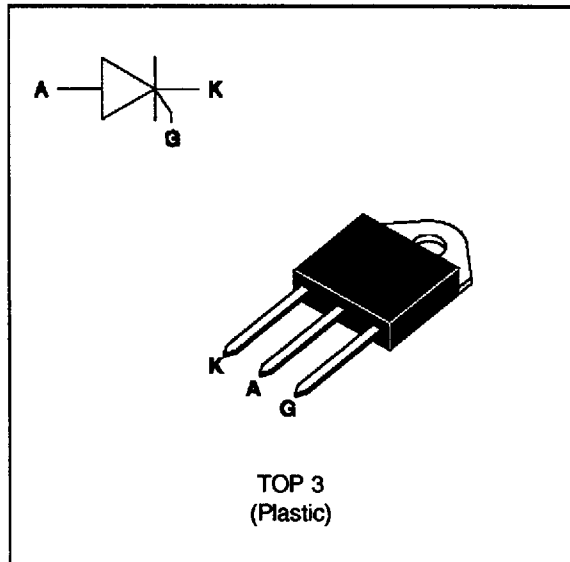
FEATURES

- HIGH SURGE CAPABILITY
- HIGH ON-STATE CURRENT
- HIGH STABILITY AND RELIABILITY
- BTW69 Serie :
 INSULATED VOLTAGE = 2500V_(RMS)
 (UL RECOGNIZED : E81734)

DESCRIPTION

The BTW 69 (N) Family of Silicon Controlled Rectifiers uses a high performance glass passivated technology.

This general purpose Family of Silicon Controlled Rectifiers is designed for power supplies up to 400Hz on resistive or inductive load.


ABSOLUTE RATINGS (limiting values)

Symbol	Parameter		Value	Unit	
$I_T(RMS)$	RMS on-state current (180° conduction angle)	BTW 69 BTW 69 N	$T_C=70^\circ C$ 50 $T_C=75^\circ C$ 55	A	
$I_T(AV)$	Average on-state current (180° conduction angle, single phase circuit)	BTW 69 BTW 69 N	$T_C=70^\circ C$ 32 $T_C=75^\circ C$ 35	A	
I_{TSM}	Non repetitive surge peak on-state current (T_j initial = 25°C)		$t_p=8.3$ ms	525	A
			$t_p=10$ ms	500	
i^2t	i^2t value		$t_p=10$ ms	1250	A ² s
di/dt	Critical rate of rise of on-state current Gate supply : $I_G = 100$ mA $di_G/dt = 1$ A/ μ s			100	A/ μ s
T_{stg} T_j	Storage and operating junction temperature range			- 40 to + 150 - 40 to + 125	°C °C
T_l	Maximum lead temperature for soldering during 10 s at 4.5 mm from case			230	°C

Symbol	Parameter	BTW 69		BTW 69 / BTW 69 N				Unit
		200	400	600	800	1000	1200	
V_{DRM} V_{RRM}	Repetitive peak off-state voltage $T_j = 125$ °C	200	400	600	800	1000	1200	V

THERMAL RESISTANCES

Symbol	Parameter		Value	Unit
Rth (j-a)	Junction to ambient		50	°C/W
Rth (j-c) DC	Junction to case for DC	BTW 69	0.9	°C/W
		BTW 69 N	0.8	

GATE CHARACTERISTICS (maximum values)

PG (AV) = 1W PGM = 40W (tp = 20 μs) IFGM = 8A (tp = 20 μs) VRGM = 5 V.

ELECTRICAL CHARACTERISTICS

Symbol	Test Conditions				Value		Unit
					BTW 69	BTW 69 N	
IGT	VD=12V (DC) RL=33Ω	Tj=25°C	MAX	80		mA	
VGT	VD=12V (DC) RL=33Ω	Tj=25°C	MAX	1.5			
VGD	VD=VDRM RL=3.3kΩ	Tj= 125°C	MIN	0.2		V	
tgt	VD=VDRM IG = 200mA dIG/dt = 1.5A/μs	Tj=25°C	TYP	2		μs	
iL	IG= 1.2 IGT	Tj=25°C	TYP	50		mA	
IH	IT= 500mA gate open	Tj=25°C	MAX	150			
VTM	BTW 69 ITM= 100A BTW 69 N ITM= 110A tp= 380μs	Tj=25°C	MAX	1.9	2.0	V	
I _{DRM} I _{RRM}	V _{DRM} Rated V _{RRM} Rated	Tj=25°C Tj= 125°C	MAX	0.02 6			
dV/dt	Linear slope up to VD=67%VDRM gate open	VDRM ≤ 800V VDRM ≥ 1000V		Tj= 125°C	MIN	500 250	V/μs
tq	VD=67%VDRM ITM= 110A VR= 75V dITM/dt=30 A/μs dVD/dt= 20V/μs	Tj= 125°C	TYP	100		μs	

Package	$I_T(\text{RMS})$	$V_{\text{DRM}} / V_{\text{RRM}}$	Sensitivity Specification
	A	V	BTW
BTW 69 (Insulated)	50	200	X
		400	X
		600	X
		800	X
		1000	X
		1200	X
BTW 69 N (Uninsulated)	55	600	X
		800	X
		1000	X
		1200	X

Fig.1 : Maximum average power dissipation versus average on-state current (BTW 69).

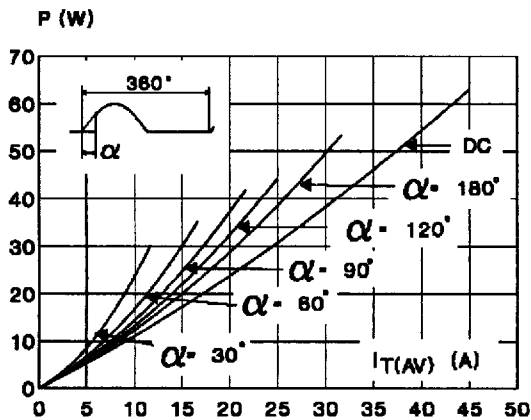


Fig.3 : Maximum average power dissipation versus average on-state current (BTW 69 N).

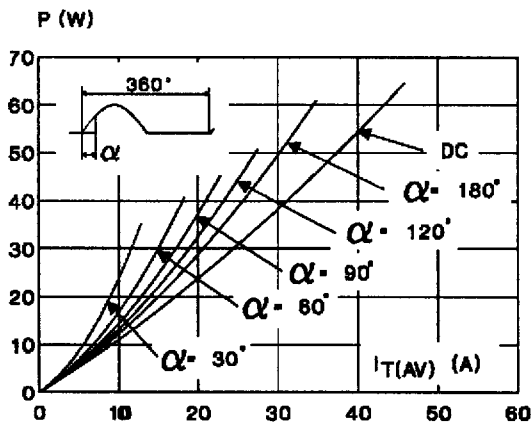


Fig.2 : Correlation between maximum average power dissipation and maximum allowable temperatures (T_{amb} and T_{case}) for different thermal resistances heatsink + contact (BTW 69).

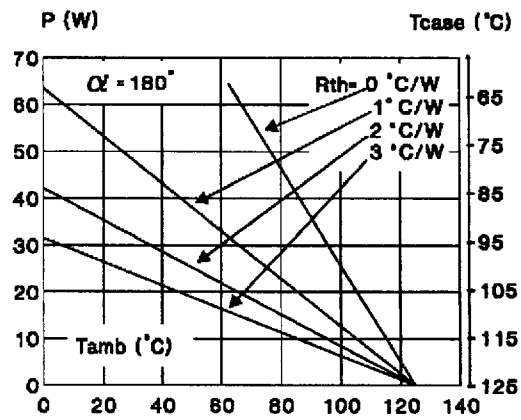


Fig.4 : Correlation between maximum average power dissipation and maximum allowable temperatures (T_{amb} and T_{case}) for different thermal resistances heatsink + contact (BTW 69 N).

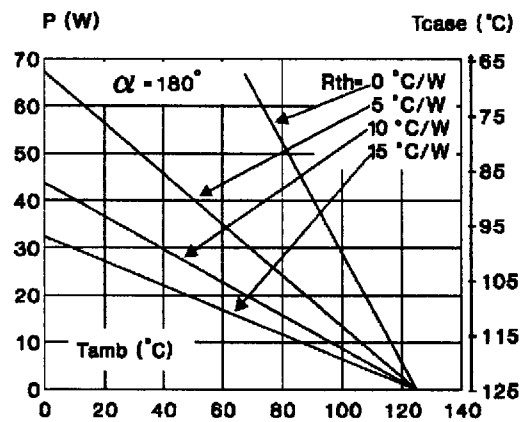


Fig.5 : Average on-state current versus case temperature (BTW 69).

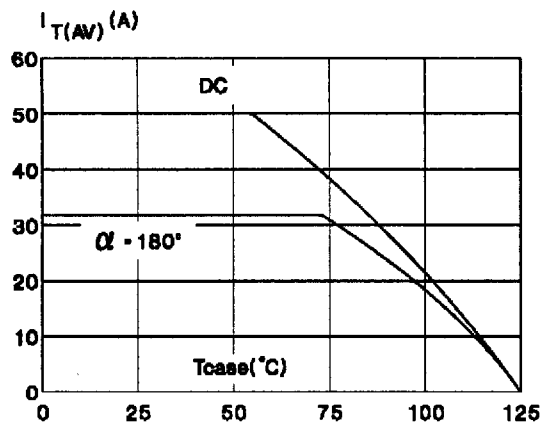


Fig.6 : Average on-state current versus case temperature (BTW 69 N).

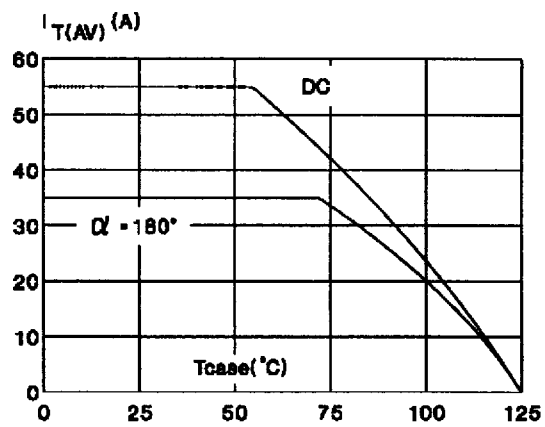


Fig.7 : Relative variation of thermal impedance versus pulse duration.

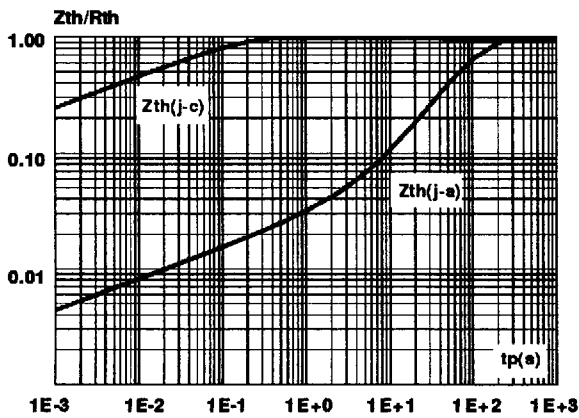


Fig.8 : Relative variation of gate trigger current versus junction temperature.

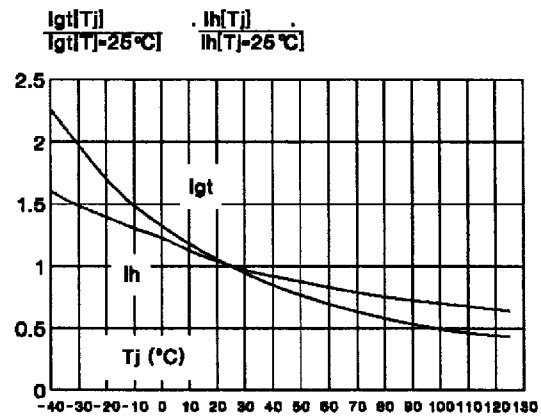


Fig.9 : Non repetitive surge peak on-state current versus number of cycles.

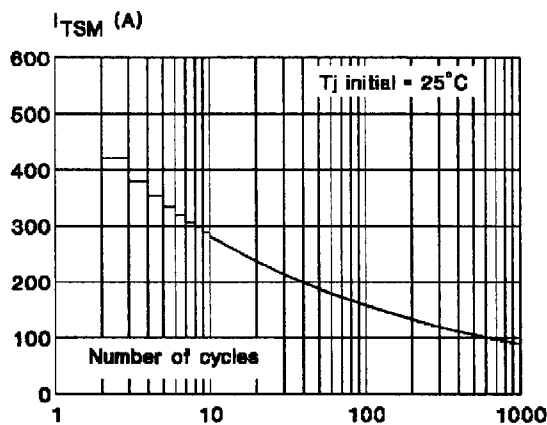


Fig.10 : Non repetitive surge peak on-state current for a sinusoidal pulse with width : $t \leq 10$ ms, and corresponding value of i^2t .

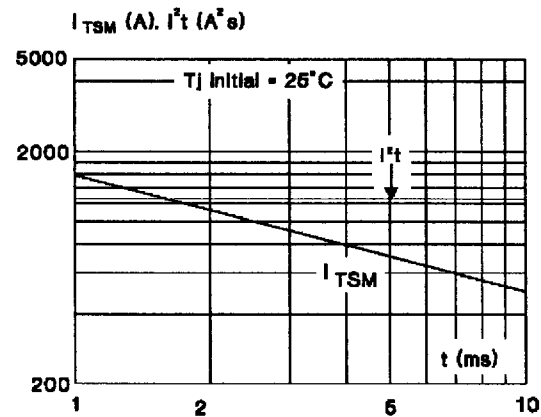
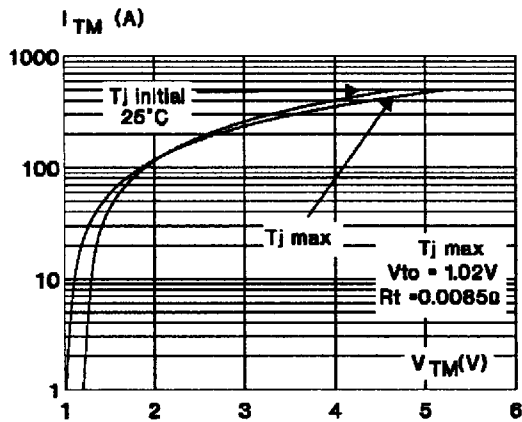
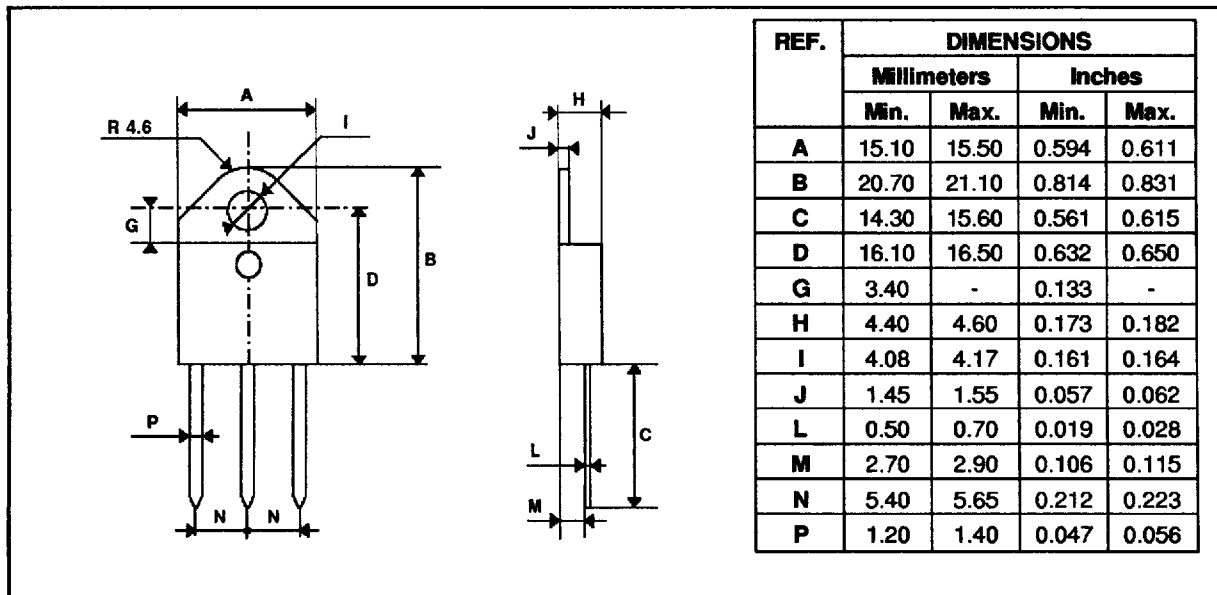


Fig11 : On-state characteristics (maximum values).



PACKAGE MECHANICAL DATA

TOP 3 Plastic



Cooling method : C
 Marking : type number
 Weight : 4.7 g

Recommended torque value : 0.8 m.N.
 Maximum torque value : 1 m.N.

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