

High voltage fast-switching NPN power transistor

General features

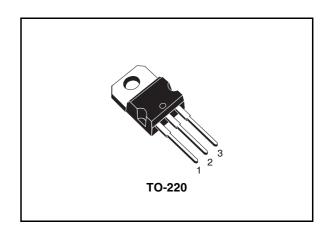
- High voltage capability
- Minimum lot-to-lot spread for reliable operation
- Low base drive requirements
- Very high switching speed
- Fully characterized at 125 °C
- In compliance with the 2002/93/EC European Directive

Applications

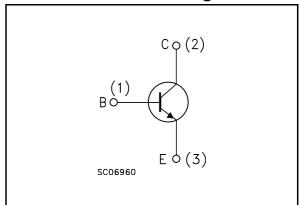
- Electronic transformers for halogen lamps
- Switch mode power supply

Description

The device is manufactured using high voltage Multi-Epitaxial Planar technology for cost-effective high performance. It uses a Hollow Emitter structure to enhance switching speeds. The device is designed for use in lighting applications and low cost switch-mode power supplies.



Internal schematic diagram



Order code

| Part Number | Marking | Package | Packing |
|-------------|---------|---------|---------|
| BUL98 | BUL98 | TO-220 | Tube |

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BUL98 Electrical ratings

1 Electrical ratings

Table 1. Absolute maximum ratings

| Symbol | Parameter | Value | Unit |
|------------------|--|------------|------|
| V _{CES} | Collector-emitter voltage (V _{BE} =0) | 800 | V |
| V _{CEO} | Collector-emitter voltage (I _B =0) | 450 | V |
| V _{EBO} | Emitter-base voltage (I _C =0) | 9 | V |
| I _C | Collector current | 12 | Α |
| I _{CM} | Collector peak current (t _P < 5ms) | 25 | Α |
| I _B | Base current | 6 | Α |
| I _{BM} | Base peak current (t _P < 5ms) | 12 | Α |
| P _{tot} | Total dissipation at T _c ≤ 25°C | 110 | W |
| T _{stg} | Storage temperature | -65 to 150 | °C |
| TJ | Max. operating junction temperature | 150 | °C |

Table 2. Thermal data

| Symbol | Parameter | Value Unit |
|-----------------------|--------------------------------------|------------|
| R _{thj-case} | Thermal resistance junction-case max | 1.14 °C/W |

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Electrical characteristics BUL98

2 Electrical characteristics

 $(T_{case} = 25^{\circ}C \text{ unless otherwise specified})$

Table 3. Electrical characteristics

| Symbol | Parameter | Test Conditions | Min. | Тур. | Max. | Unit |
|--------------------------------------|---|---|----------|--------------------|-------------------|--------------------------|
| I _{CES} | Collector cut-off current (V _{BE} =0) | V _{CE} = 800V V _{CE} = 800V T _j = 125°C | | | 100 500 | μ Α μ Α |
| I _{CEO} | Collector-emitter current (I _B =0) | V _{CE} = 450V | | | 100 | μА |
| V _{CEO(sus)} ⁽¹⁾ | Collector-emitter sustaining voltage (I _B = 0) | I _C =10mA L = 25mH | 450 | | | V |
| V _{EBO} | Emitter-base voltage (I _C = 0) | I _E =10mA | 9 | | | V |
| V _{CE(sat)} (1) | Collector-emitter saturation voltage | $I_{C} = 5A$ $I_{B} = 1A$ $I_{C} = 9A$ $I_{B} = 1.8A$ $I_{C} = 12A$ $I_{B} = 2.4A$ | | 0.15 0.3 0.5 | 0.5 0.8 1.5 | V V V |
| V _{BE(sat)} (1) | Base-emitter saturation voltage | $I_{C} = 5A$ $I_{B} = 1A$ $I_{C} = 9A$ $I_{B} = 1.8A$ $I_{C} = 12A$ $I_{B} = 2.4A$ | | 0.95 1 1.1 | 1.2 1.3 1.4 | V V V |
| h _{FE} ⁽¹⁾ | DC current gain | I _C =10mA V _{CE} =5V I _C =5A V _{CE} =5V | 10 15 | | 35 | |
| t _s | Inductive load Storage time Fall time | $\begin{array}{lll} V_{CL}\!=\!350V & I_{C}\!=\!9A \\ V_{BE(off)}\!=\!-5V & I_{B1}\!=\!1.8A \\ L\!=\!200\mu H & R_{BB(off)}\!=\!0\Omega \\ (\text{see figure 8}) \end{array}$ | | 1.1 55 | 1.8 100 | μs ns |
| t _s | Inductive load Storage time Fall time | $\begin{split} &V_{CL}\!=\!350V & I_{C}\!=\!9A \\ &V_{BE(off)}\!=\!\!-5V & I_{B1}\!=\!1.8A \\ &L=\!200\mu H & R_{BB(off)}\!=\!0\Omega \\ &T_{j}\!=\!100^{\circ}C & (\text{see figure 8}) \end{split}$ | | 1.5 80 | | μs ns |

Note (1) Pulsed duration = 300 μ s, duty cycle \leq 1.5%

2.1 Electrical characteristics (curves)

Figure 1. Safe operating area

Figure 2. Derating curve

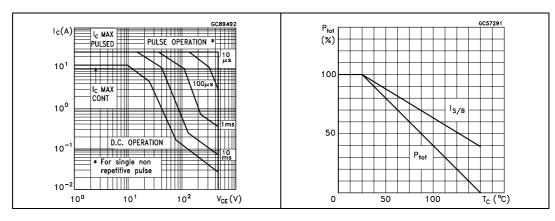


Figure 3. Collector-emitter saturation voltage

Figure 4. Base-emitter saturation voltage

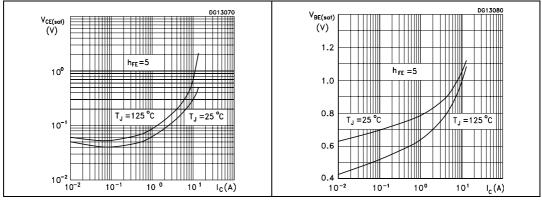
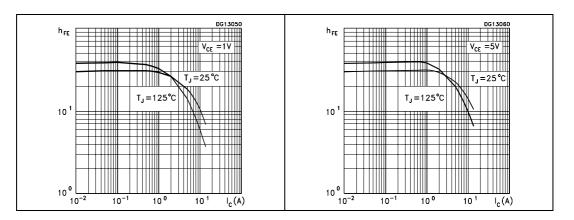


Figure 5. DC current gain

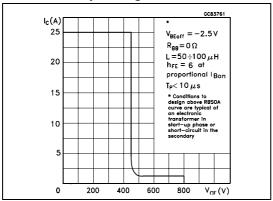
Figure 6. DC current gain



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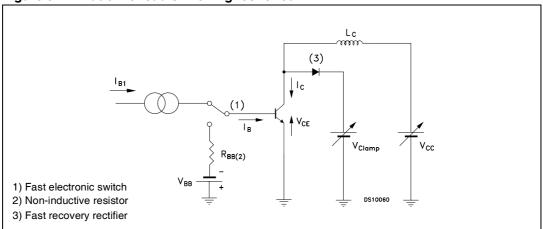
Electrical characteristics BUL98

Figure 7. Reverse biased safe operating area



2.2 Test circuits

Figure 8. Inductive load switching test circuit



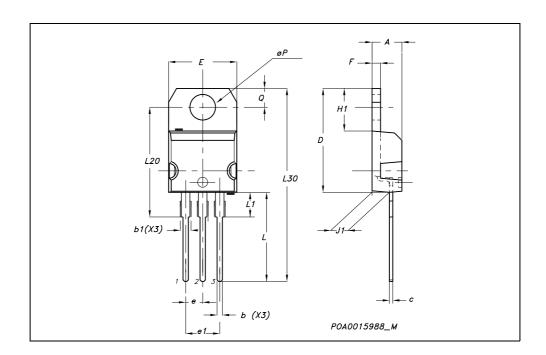
3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

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TO-220 MECHANICAL DATA

| DIM. | mm. | | | inch | | | |
|------|-------|-------|-------|-------|-------|-------|--|
| DIW. | MIN. | TYP | MAX. | MIN. | TYP. | MAX. | |
| Α | 4.40 | | 4.60 | 0.173 | | 0.181 | |
| b | 0.61 | | 0.88 | 0.024 | | 0.034 | |
| b1 | 1.15 | | 1.70 | 0.045 | | 0.066 | |
| С | 0.49 | | 0.70 | 0.019 | | 0.027 | |
| D | 15.25 | | 15.75 | 0.60 | | 0.620 | |
| Е | 10 | | 10.40 | 0.393 | | 0.409 | |
| е | 2.40 | | 2.70 | 0.094 | | 0.106 | |
| e1 | 4.95 | | 5.15 | 0.194 | | 0.202 | |
| F | 1.23 | | 1.32 | 0.048 | | 0.052 | |
| H1 | 6.20 | | 6.60 | 0.244 | 0.2 | | |
| J1 | 2.40 | | 2.72 | 0.094 | | 0.107 | |
| L | 13 | | 14 | 0.511 | | 0.551 | |
| L1 | 3.50 | | 3.93 | 0.137 | | 0.154 | |
| L20 | | 16.40 | | | 0.645 | | |
| L30 | | 28.90 | | | 1.137 | | |
| øΡ | 3.75 | | 3.85 | 0.147 | | 0.151 | |
| Q | 2.65 | | 2.95 | 0.104 | | 0.116 | |



BUL98 Revision history

4 Revision history

Table 4. Revision history

| Date | Revision | Changes | |
|-------------|----------|-----------------------------------|--|
| 30-Jun-2005 | 1 | First release. | |
| 07-Nov-2006 | 2 | The document has been reformatted | |

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