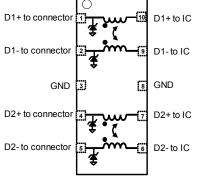


ECMF4-2450A60N10

Datasheet

Common mode filter with ESD protection for high speed serial interface





Product status ECMF4-2450A60N10

Features

- 6.4 GHz differential bandwidth to comply with HDMI 2.1, HDMI 2.0, HDMI 1.4, USB 4, USB 3.2 Gen 1 and Gen 2, MIPI, display port, etc.
- High common mode attenuation on WLAN frequencies :
 - From 30 dB to 33 dB at 2.4 2.47 GHz: Wi-Fi CH1-14 frequencies
 - From 20 dB to 15 dB at 5.18 5.82 GHz: Wi-Fi CH36-165 frequencies
- Very low PCB space consumption
- Thin package: 0.5 mm max.
- Lead free and RoHS package
- High reduction of parasitic elements through integration
- Exceeds IEC 61000-4-2 level 4 standards:
 - ±25 kV (air discharge)
 - ±10 kV (contact discharge)

Applications

- Notebook, laptop
- Streaming box
- Set top box
- Portable devices
- Tablet

Description

The ECMF4-2450A60N10 is a highly integrated common mode filter designed to suppress EMI/RFI common mode noise on high speed differential serial buses like HDMI 2.1, HDMI 2.0, HDMI1.4, USB 4, USB 3.2 Gen 1 and Gen 2, ethernet, MIPI, display port and other high speed serial interfaces.

It has a very large differential bandwidth to comply with these standards and can also protect and filter 2 differential lanes.

1 Characteristics

Symbol	Parameter		Value	Unit
		IEC 61000-4-2:		
V _{PP}	Peak pulse voltage	Contact discharge	10	kV
		Air discharge	25	
I _{RMS}	Maximum RMS current		100	mA
T _{op}	Maximum operating temperature range		-55 to +125	
T _{stg}	Storage temperature range		-55 to +150	°C
TL	Maximum temperature for soldering during 10 s		260	

Table 1. Absolute maximun	n ratings	(T _{amb} = 25 °	C)
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Figure 1. Electrical characteristics (definitions)

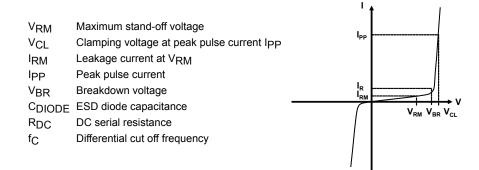


Table 2. Electrical characteristics (T_{amb} = 25 °C)

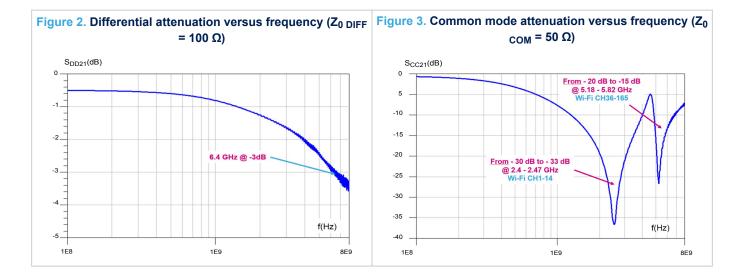
Symbol	Test conditions	Min.	Тур.	Max.	Unit
V _{BR}	I _R = 1 mA	5.3	5.8		V
1	V _{RM} = 3.6 V per line		< 1	50	nA
IRM	V _{RM} = 5 V per line		3	70	nA
R _{DC}	DC serial resistance, I _{DC} = 20 mA		5		Ω
f _c	Differential mode cut-off frequency at -3 dB		6.4		GHz
V _{CL}	8 kV contact discharge after 30 ns, IEC 61000-4-2		17.3		V
C _{DIODE}	V_{BIAS} = 0 V, 2.5 GHz \leq f \leq 6 GHz, V_{OSC} = 30 mV		0.25	0.40	pF

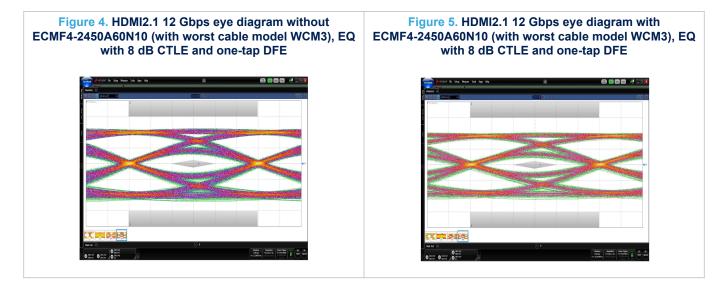
Table 3. Pin description

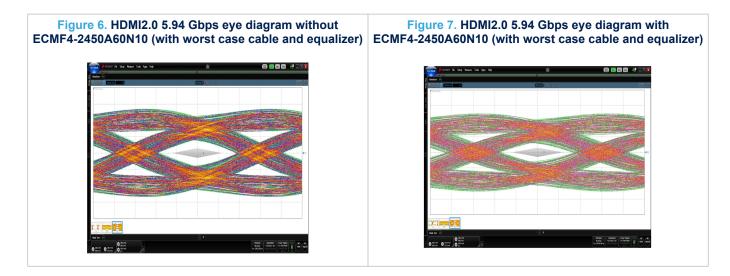
Pin number	Description	Pin number	Description
1	D1+ to connector	6	D2- to IC
2	D1- to connector	7	D2+ to IC
3	GND	8	GND
4	D2+ to connector	9	D1- to IC
5	D2- to connector	10	D1+ to IC



1.1 Characteristics curves









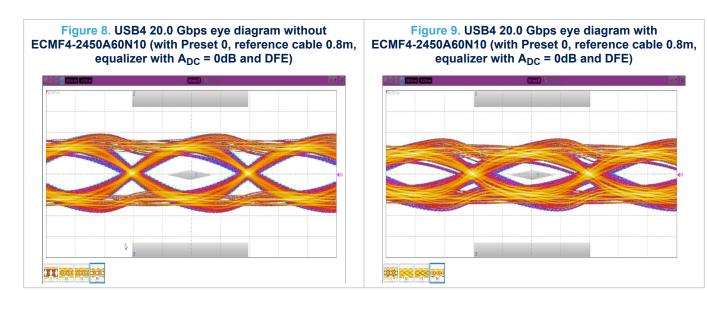


Figure 10. USB3.2 Gen 2 10.0 Gbps eye diagram without ECMF4-2450A60N10 (with type C connector, reference cable, equalizer with A_{DC} = 6 dB and DFE)

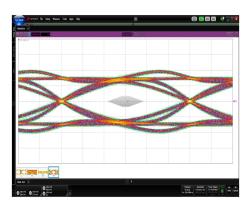
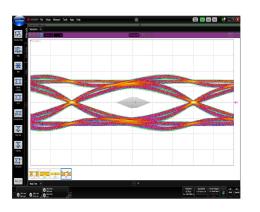
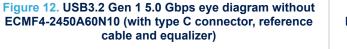


Figure 11. USB3.2 Gen 2 10.0 Gbps eye diagram with ECMF4-2450A60N10 (with type C connector, reference cable, equalizer with A_{DC} = 6 dB and DFE)





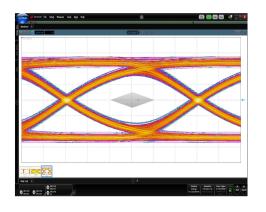
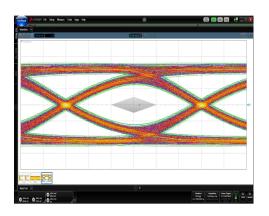
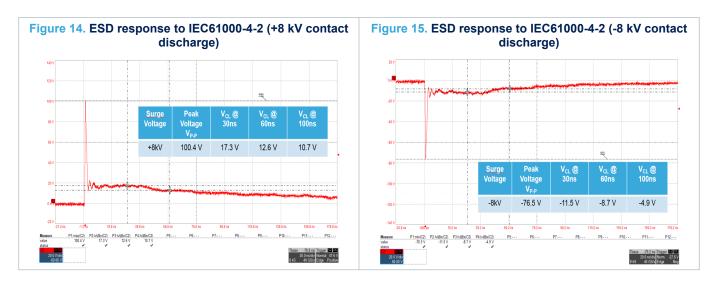
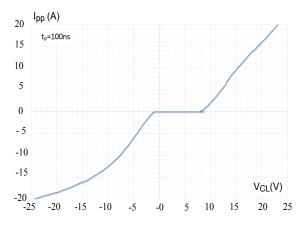


Figure 13. USB3.2 Gen 1 5.0 Gbps eye diagram with ECMF4-2450A60N10 (with type C connector, reference cable and equalizer)









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2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

2.1 µQFN10L package information

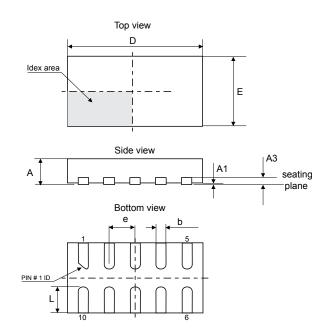
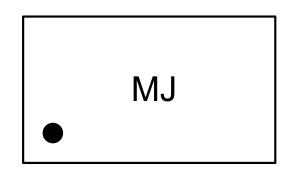


Figure 17. µQFN10L package outline

Table 4. µQFN10L package mechanical data

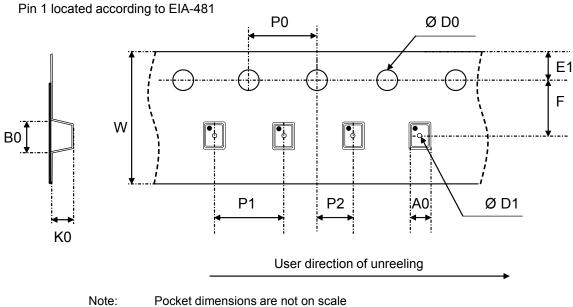
	Dimensions				
Ref.	Millimeters				
	Min.	Тур.	Max.		
А	0.41	0.45	0.50		
A1	0.00	0.02	0.05		
A3		0.127			
b	0.15	0.20	0.25		
D	2.15	2.20	2.25		
E	1.30	1.35	1.40		
e		0.40			
L	0.40	0.50	0.60		

Figure 18. Marking layout



Note: The marking codes can be rotated by 90 ° or 180° to differentiate assembly location. In no case should this product marking be used to orient the component for its placement on a PCB. Only pin 1 mark is to be used for this purpose.

Figure 19. Tape and reel outline



Pocket shape may vary depending on package

Ref.	Dimensions (millimeters)				
Kel.	Min.	Тур.	Max.		
P1	3.9	4.0	4.1		
P0	3.9	4.0	4.1		
Ø D0	1.4	1.5	1.6		
Ø D1	0.35	0.40	0.45		
F	3.45	3.5	3.55		
E1	1.65	1.75	1.85		
К0	0.6	0.65	0.7		
P2	1.95	2	2.05		
W	7.9	8	8.1		
A0	1.50	1.55	1.60		
В0	2.35	2.40	2.45		

3 Recommendation PCB assembly

3.1 Footprint

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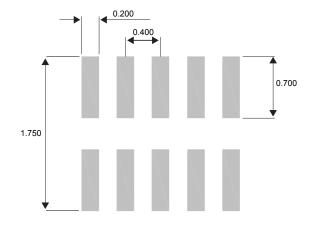


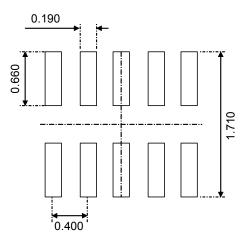
Figure 20. Footprint in mm

SMD footprint design is recommended.

3.2 Stencil opening design

Recommended design reference: stencil opening thickness: 100 µm





3.3 Solder paste

- 1. Halide-free flux qualification ROL0 according to ANSI/J-STD-004.
- 2. "No clean" solder paste is recommended.
- 3. Offers a high tack force to resist component movement during PCB movement.
- 4. Solder paste with fine particles: powder particle size is 20-38 µm.

3.4 Placement

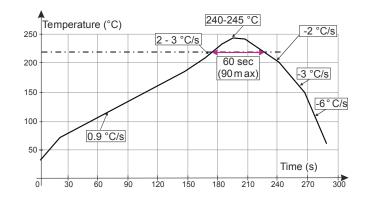
- 1. Manual positioning is not recommended.
- 2. It is recommended to use the lead recognition capabilities of the placement system, not the outline centering
- 3. Standard tolerance of ±0.05 mm is recommended.
- 4. 3.5 N placement force is recommended. Too much placement force can lead to squeezed out solder paste and cause solder joints to short. Too low placement force can lead to insufficient contact between package and solder paste that could cause open solder joints or badly centered packages.
- 5. To improve the package placement accuracy, a bottom side optical control should be performed with a high resolution tool.
- For assembly, a perfect supporting of the PCB (all the more on flexible PCB) is recommended during solder paste printing, pick and place and reflow soldering by using optimized tools.

3.5 PCB design preference

- 1. To control the solder paste amount, the closed via is recommended instead of open vias.
- 2. The position of tracks and open vias in the solder area should be well balanced. A symmetrical layout is recommended, to avoid any tilt phenomena caused by asymmetrical solder paste due to solder flow away.

3.6 Reflow profile

Figure 22. ST ECOPACK[®] recommended soldering reflow profile for PCB mounting



Note:

Minimize air convection currents in the reflow oven to avoid component movement. Maximum soldering profile corresponds to the latest IPC/JEDEC J-STD-020.

4 Ordering information

Figure 23. Ordering information scheme

Table 6. Ordering information

Order code	Marking	Package	Weight	Base qty.	Delivery mode
ECMF4-2450A60N10	MJ ⁽¹⁾	µQFN-10L	3.9 mg	3000	Tape and reel

1. The marking can be rotated by 90° to differentiate assembly location

Revision history

Date	Revision	Changes
27-Jul-2018	1	Initial release.
24-Apr-2019	2	Updated Table 6.
02-Jun-2021	3	Added Figure 8 and Figure 9.

Table 7. Document revision history

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