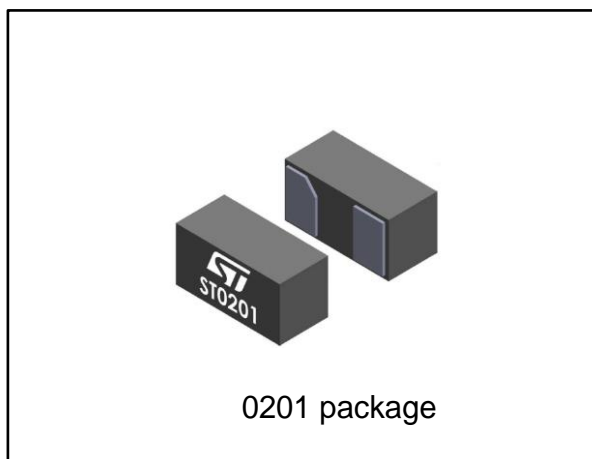


## Low clamping and low capacitance bidirectional single line ESD protection

Datasheet - production data



### Features

- Low clamping voltage:  $V_{CL} = 18\text{ V}$
- Bidirectional device
- Low leakage current
- 0201 package
- Ultra-low PCB area:  $0.18\text{ mm}^2$
- ECOPACK<sup>®2</sup> compliant component
- Exceeds IEC 61000-4-2 level 4 standard:
  - $\pm 15\text{ kV}$  (air discharge)
  - $\pm 8\text{ kV}$  (contact discharge)

### Applications

Where transient over voltage protection in ESD sensitive equipment is required, such as:

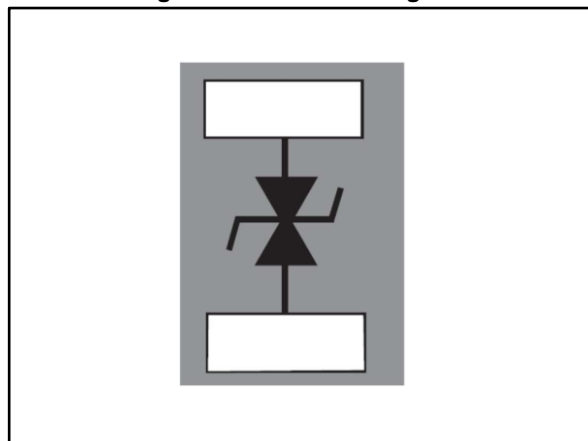
- Smartphones, mobile phones and accessories
- Tablet, PC, netbooks and notebooks
- Portable multimedia devices and accessories
- Digital cameras and camcorders
- Communication and highly integrated systems

### Description

The ESDALC14-1BF4 is a bidirectional single line TVS diode designed to protect the data line or other I/O ports against ESD transients.

The device is ideal for applications where both reduced line capacitance and board space saving are required.

Figure 1: Functional diagram



# 1 Characteristics

Table 1: Absolute ratings ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ )

| Symbol    | Parameter  |                                 | Value       | Unit               |
|-----------|--|---------------------------------|-------------|--------------------|
| $V_{PP}$  | Peak pulse voltage                                 | IEC 61000-4-2 contact discharge | 25          | kV                 |
|           |  | IEC 61000-4-2 air discharge     | 30          |                    |
| $P_{PP}$  | Peak pulse power dissipation (8/20 $\mu\text{s}$ ) |                                 | 100         | W                  |
| $I_{PP}$  | Peak pulse current (8/20 $\mu\text{s}$ )           |                                 | 5           | A                  |
| $T_j$     | Operating junction temperature range               |                                 | -40 to +150 | $^{\circ}\text{C}$ |
| $T_{stg}$ | Storage temperature range                          |                                 | -65 to +150 | $^{\circ}\text{C}$ |
| $T_L$     | Maximum lead temperature for soldering during 10 s |                                 | 260         | $^{\circ}\text{C}$ |

Figure 2: Electrical characteristics (definitions)

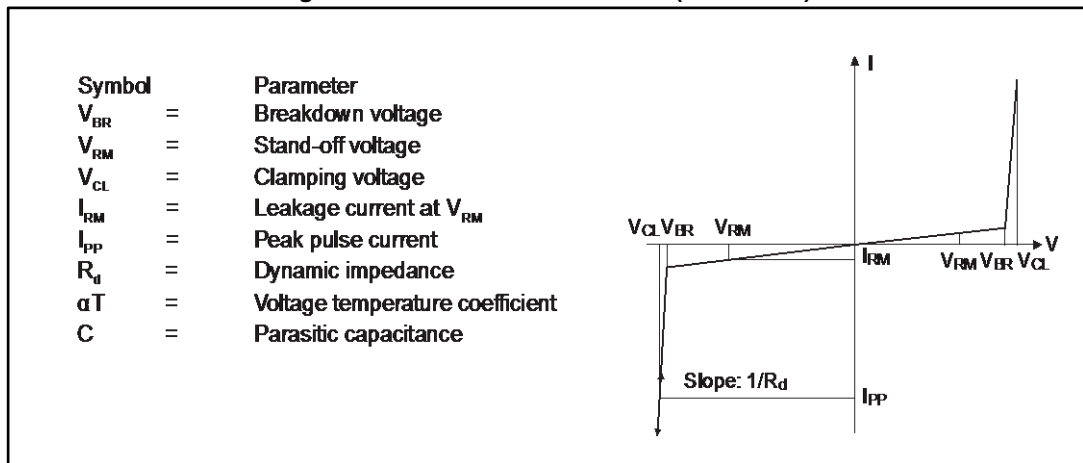
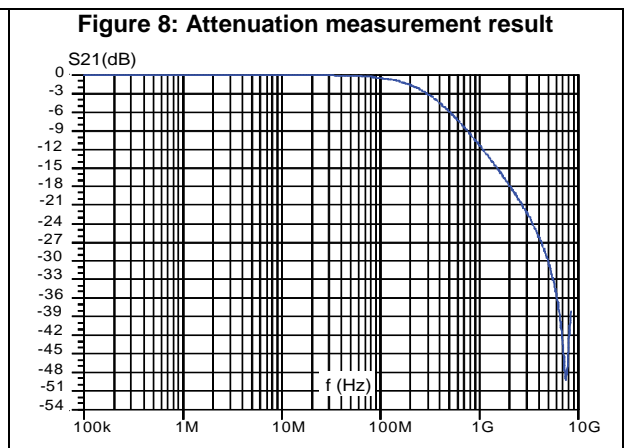
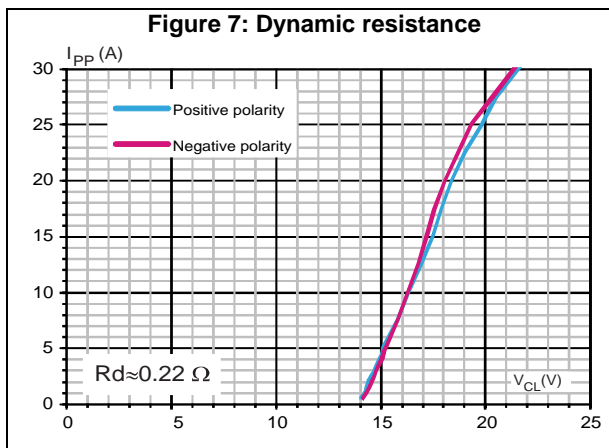
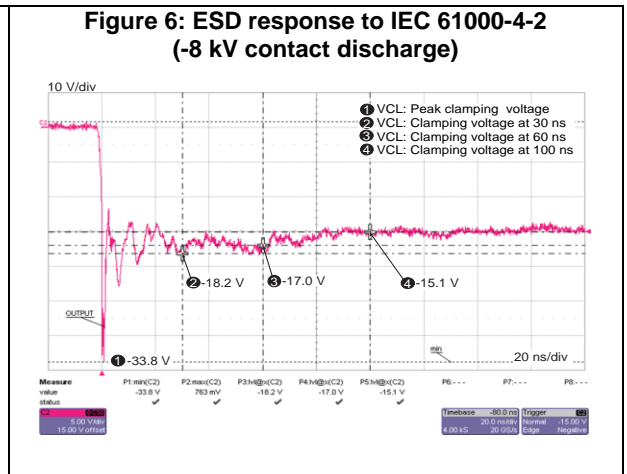
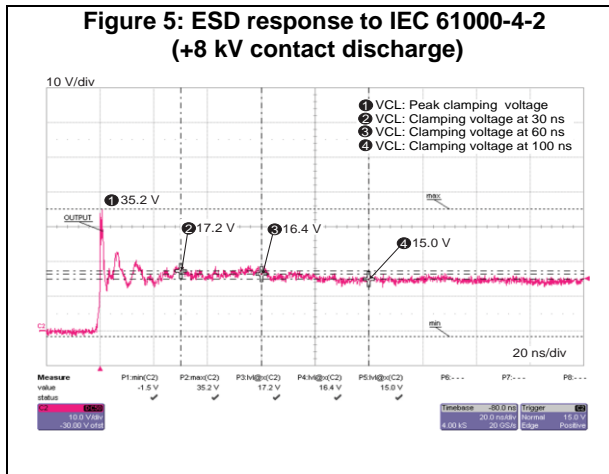
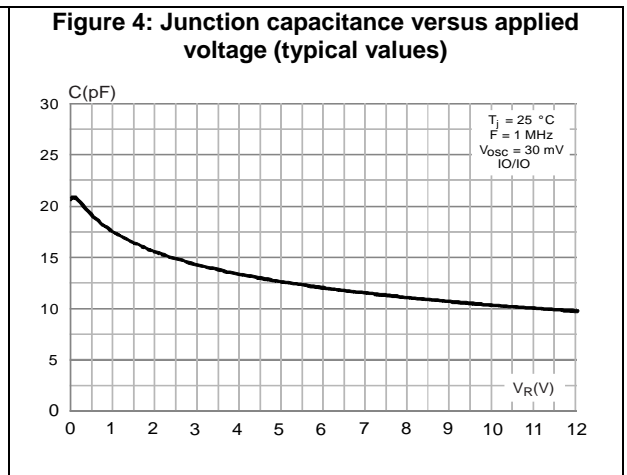
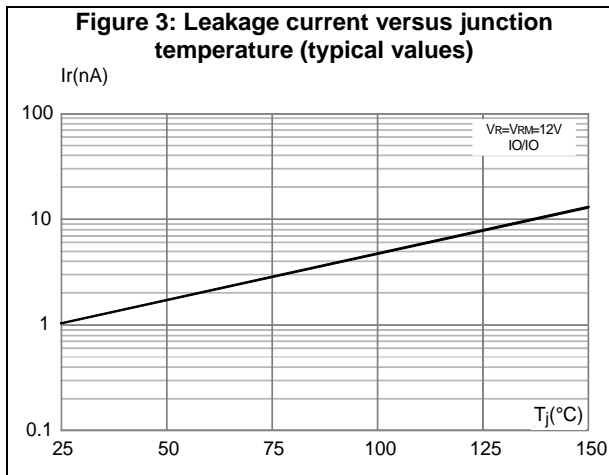


Table 2: Electrical characteristics ( $T_{amb} = 25\text{ }^{\circ}\text{C}$ )

| Symbol     | Test condition  | Min. | Typ. | Max. | Unit |
|------------|---|------|------|------|------|
| $V_{BR}$   | $I_R = 1\text{ mA}$   | 13   |      |      | V    |
| $I_{RM}$   | $V_{RM} = 12\text{ V}$  |      |      | 100  | nA   |
| $V_{CL}$   | 8 kV contact discharge after 30 ns, IEC 61000-4-2                       |      | 18   |      | V    |
| $C_{LINE}$ | $F = 1\text{ MHz}$ , $V_{LINE} = 0\text{ V}$ , $V_{OSC} = 30\text{ mV}$ |      | 22   | 25   | pF   |

# 1.1 Characteristics (curves)

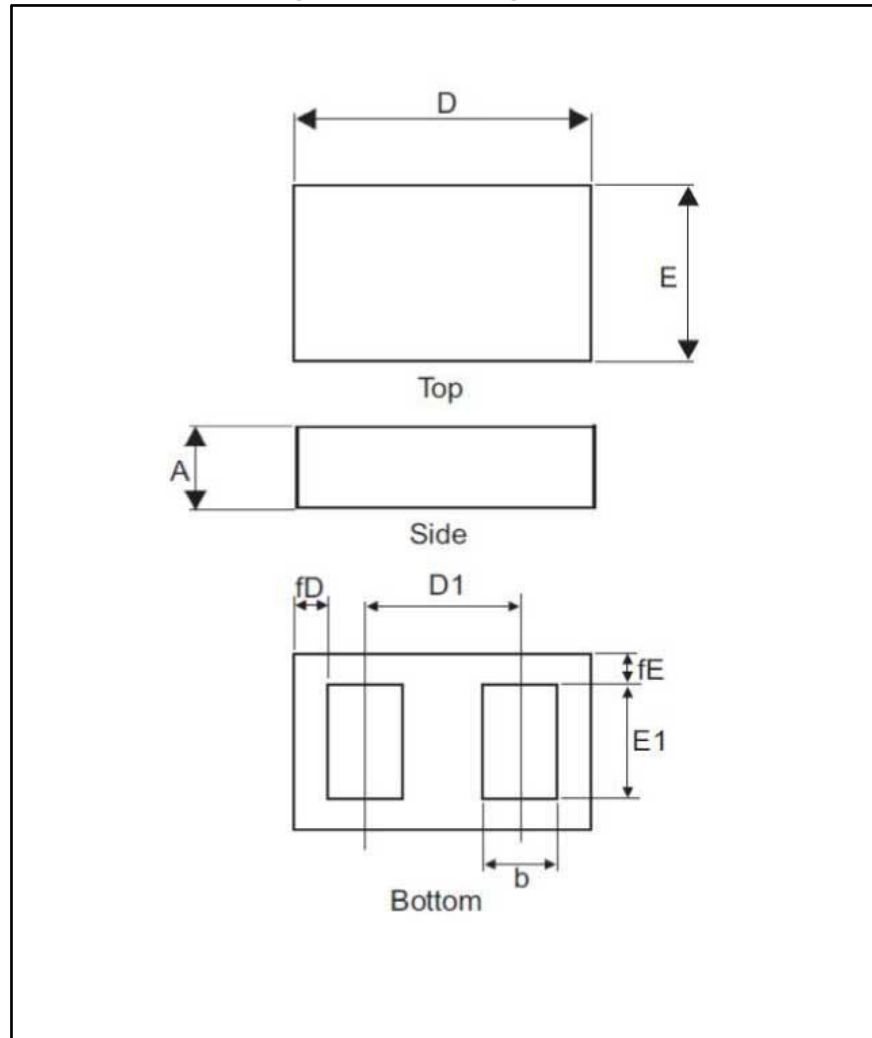


## 2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: [www.st.com](http://www.st.com). ECOPACK® is an ST trademark.

### 2.1 0201 package information

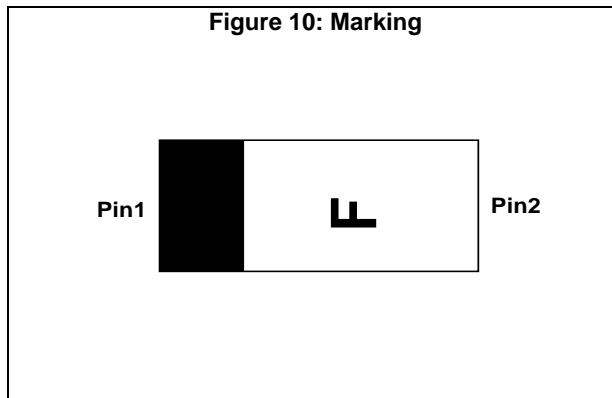
Figure 9: 0201 package outline



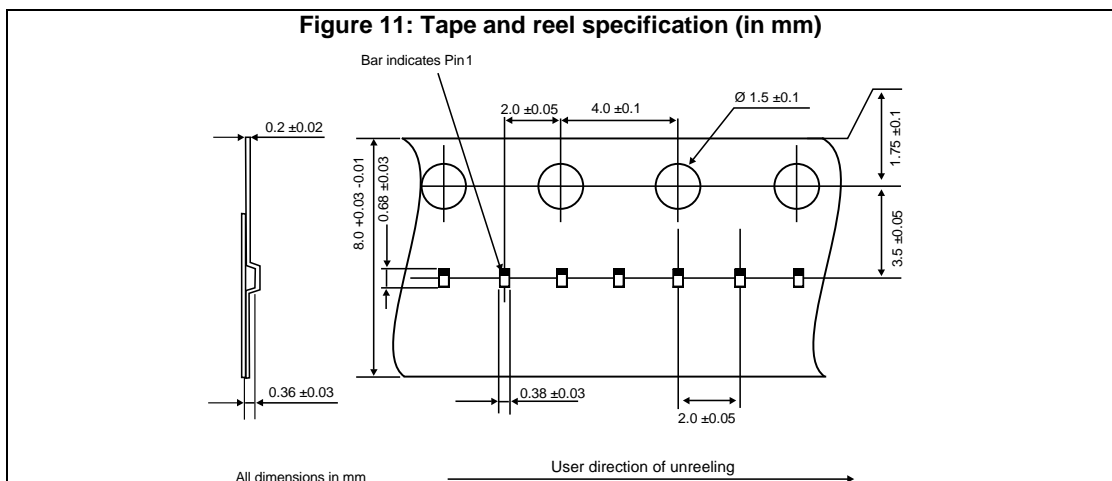
The marking codes can be rotated by 90° or 180° to differentiate assembly location. In no case should this product marking be used to orient the component for its placement on a PCB. Only pin 1 mark is to be used for this purpose.

Table 3: 0201 package mechanical data

| Ref. | Dimensions  |       |       |
|------|-------------|-------|-------|
|      | Millimeters |       |       |
|      | Min.        | Typ.  | Max.  |
| A    | 0.280       | 0.300 | 0.320 |
| b    | 0.125       | 0.140 | 0.155 |
| D    | 0.570       | 0.600 | 0.630 |
| D1   |             | 0.350 |       |
| E    | 0.270       | 0.300 | 0.330 |
| E1   | 0.175       | 0.190 | 0.205 |
| fD   | 0.110       | 0.125 | 0.140 |
| fE   | 0.040       | 0.055 | 0.070 |



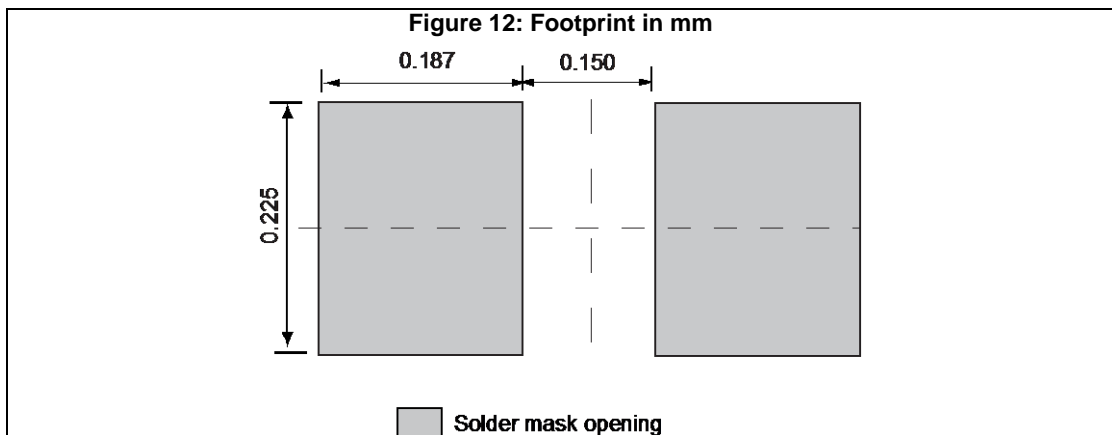
The marking codes can be rotated by 90° or 180° to differentiate assembly location. In no case should this product marking be used to orient the component for its placement on a PCB. Only pin 1 mark is to be used for this purpose.



### 3 Recommendation on PCB assembly

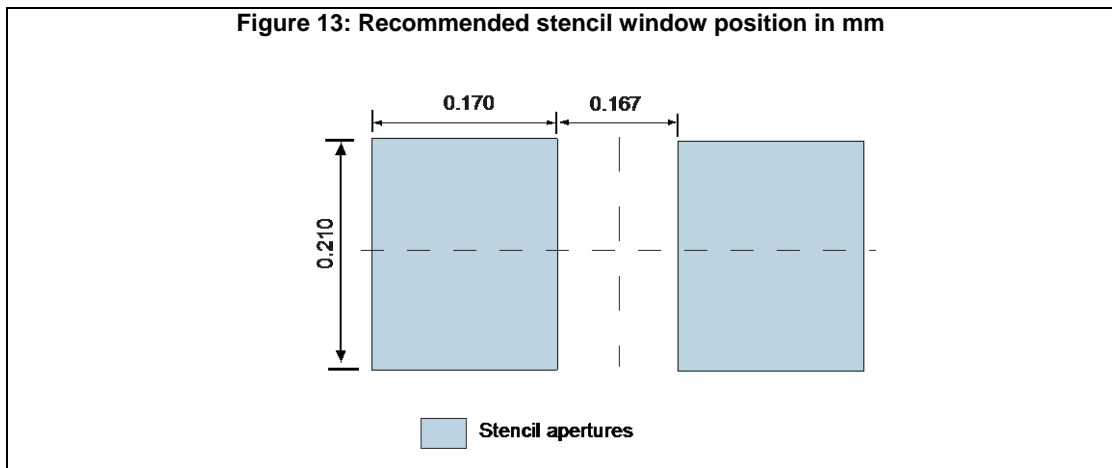
#### 3.1 Footprint

1. Footprint in mm
  - a. SMD footprint design is recommended.



#### 3.2 Stencil opening design

1. Reference design
  - a. Stencil opening thickness: 75  $\mu\text{m}$  / 3 mils



#### 3.3 Solder paste

1. Halide-free flux qualification ROL0 according to ANSI/J-STD-004.
2. "No clean" solder paste is recommended.
3. Offers a high tack force to resist component movement during high speed.
4. Use solder paste with fine particles: powder particle size 20-38  $\mu\text{m}$ .

### 3.4 Placement

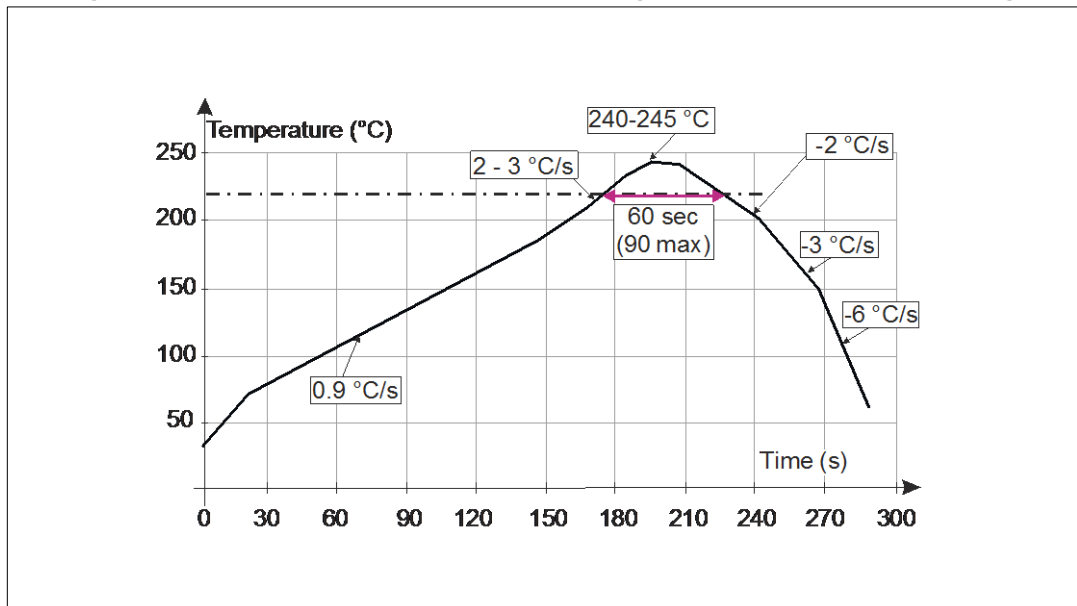
1. Manual positioning is not recommended.
2. It is recommended to use the lead recognition capabilities of the placement system, not the outline centering
3. Standard tolerance of  $\pm 0.05$  mm is recommended.
4. 1.0 N placement force is recommended. Too much placement force can lead to squeezed out solder paste and cause solder joints to short. Too low placement force can lead to insufficient contact between package and solder paste that could cause open solder joints or badly centered packages.
5. To improve the package placement accuracy, a bottom side optical control should be performed with a high resolution tool.
6. For assembly, a perfect supporting of the PCB (all the more on flexible PCB) is recommended during solder paste printing, pick and place and reflow soldering by using optimized tools.

### 3.5 PCB design preference

1. To control the solder paste amount, the closed via is recommended instead of open vias.
2. The position of tracks and open vias in the solder area should be well balanced. A symmetrical layout is recommended, to avoid any tilt phenomena caused by asymmetrical solder paste due to solder flow away.

### 3.6 Reflow profile

Figure 14: ST ECOPACK® recommended soldering reflow profile for PCB mounting



Minimize air convection currents in the reflow oven to avoid component movement.

## 4 Ordering information

Figure 15: Ordering information scheme

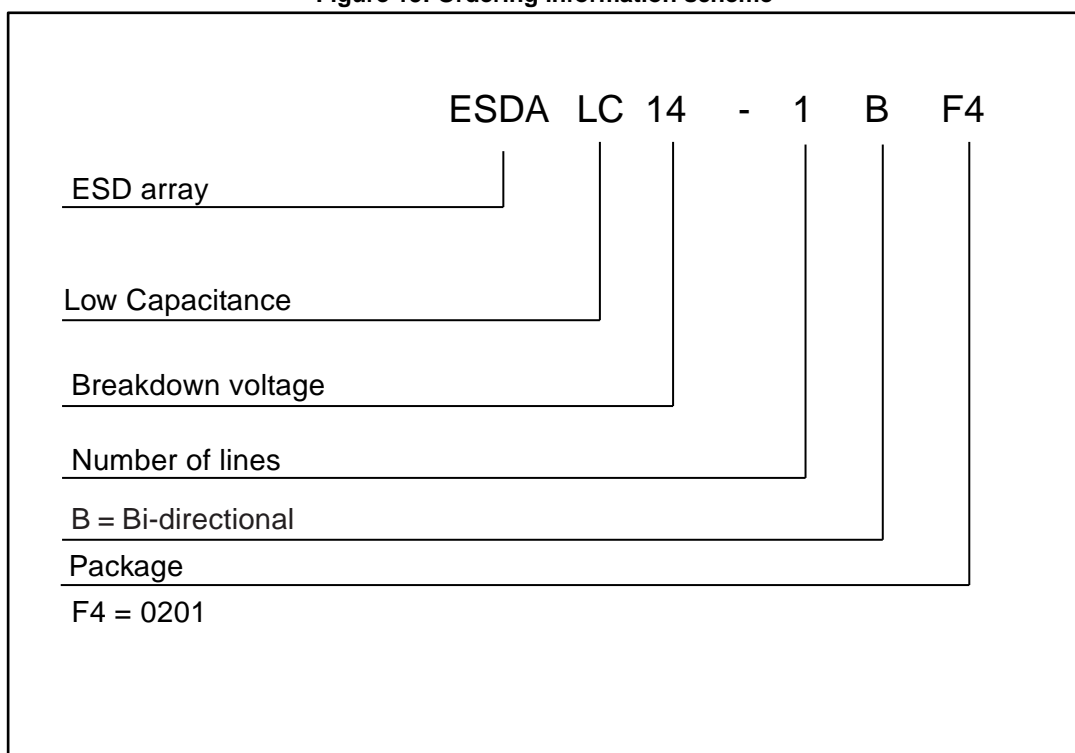


Table 4: Ordering information

| Order code    | Marking          | Package | Weight   | Base qty. | Delivery mode |
|---------------|------------------|---------|----------|-----------|---------------|
| ESDALC14-1BF4 | 1 <sup>(1)</sup> | 0201    | 0.120 mg | 15000     | Tape and reel |

**Notes:**

<sup>(1)</sup>The marking codes can be rotated by 90 °C or 180 °C to differentiate assembly location.

## 5 Revision history

Table 5: Document revision history

| Date        | Revision | Changes                                   |
|-------------|----------|---|
| 11-Oct-2013 | 1        | First issue.                              |
| 03-Sep-2015 | 2        | Updated Table 2.                          |
| 14-Dec-2017 | 3        | Updated weight from 0.116 mg to 0.120 mg. |



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