FL-101

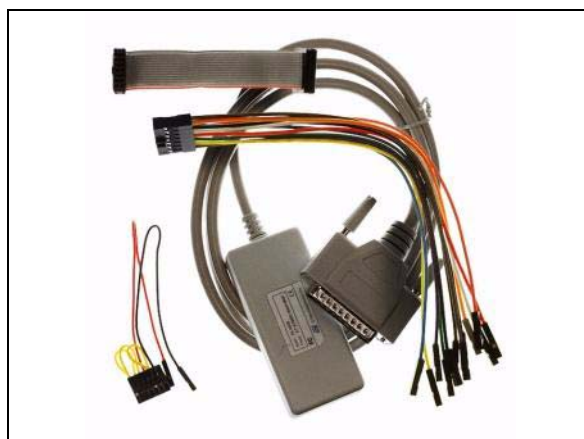## FlashLINK JTAG Programmer

Features

- Allows PC or Notebook parallel port to program UPSD and PSD devices using PSDsoft Express software development tool.

- Supports IEEE 1149.1 JTAG signals (TCK, TMS, TDI, TDO).

- Supports additional signals ($\overline{\text{TERR}}$ and TSTAT) to reduce programming time.

- Single FlashLINK cable assembly supports both 3.3 V and 5 V target devices with no manual configuration. Target device may operate from 2.7 V to 5.5 V.

- Flying Lead cable included to adapt to any target connector using 0.025" square posts.

- Loop Test cable included to validate PC parallel port operation.

Description

The FL-101 is a FlashLINK JTAG programming cable for PSD and UPSD microcontroller families. Driven by a PC through a parallel port, it allows In-System Programming (ISP) of PSD and UPSD devices located on an application board.

Table 1.   Device summary

Reference	Order code
FlashLINK JTAG Programmer	FL-101

www.st.com

Ordering information

The FL-101 can be ordered from your nearest ST distributor or sales office (ST order code: FL-101).

For more information and complete documentation, please refer to the STMicroelectronics microcontroller support site on www.st.com.

Revision history

Table 2.   Document revision history

Date	Revision	Changes
03-Oct-2011	1	Initial release.

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