

2 A DC-DC converter modules

Features

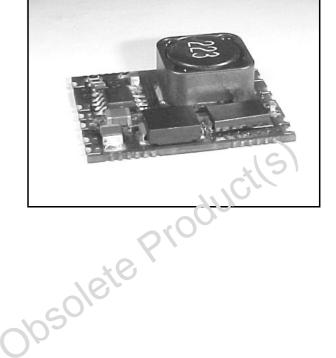
- MTBF 1 000 000 hours $(T_A = 25^{\circ}C)$
- 2 A max output current
- 35 V max input voltage
- 1.5 V max drop-out voltage
- Remote logic inhibit/enable
- Synchronization
- Not-latching overload and short circuit protection
- Thermal shutdown
- Fixed or adjustable output
- No heatsink required
- Operating temperature range -25 °C ÷ 85 °C



The GS-R24F series is a family of high efficiency step down switching voltage regulator, designed to replace linear regulators.

Based on ST L5973 device, these non isolated regulators are suitable for various applications, including telecom, industry, computer and distributed power supply system having a wide range input voltage.





Order nucles	Output voltage [V]	Input voltage [V]	Output ripple [mVpp]	Efficiency [%]	Notes
GS-R24F0182.0	1.8 ± 4%	16 ÷ 35	25	72	Fixed output voltage
GS-R24F0252.0	2.5 ± 4%	16 ÷ 35	25	76	Fixed output voltage
GS-R24F0332.0	3.3 ± 4%	16 ÷ 35	25	82	Fixed output voltage
GS-R24F0502.0	5.0 ± 4%	16 ÷ 35	25	85	Fixed output voltage
GS-R24F0002.0	1.235 ÷ 5.5	16 ÷ 35	25	68 ÷ 85	Progr. output voltage
GS-R24F1201.5	12 ± 4%	16 ÷ 35	100	91	Fixed output voltage
GS-R24F0001.0	3.3 ÷ 24	16 ÷ 35	100 ÷ 250	78 ÷ 96	Progr. output voltage

Contents GS-R24F

Contents

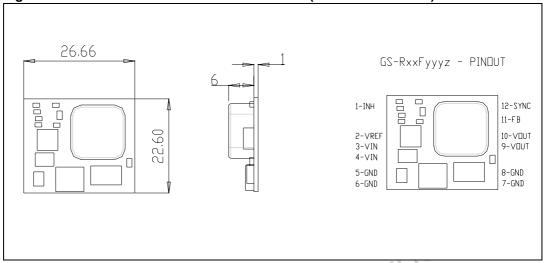
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GS-R24F Pin settings

Pin settings 1

Pin connection 1.1

Figure 1. Pin connection and mechanical data (dimensions in mm)



1.2 Pin description

Table 2. Pin description

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1.2	Pin descri	iption	e de la companya de l
	Table 2. Pi	n description	60/6
	Name	Function	Description
	1	INH	A logic high level disables the device. When the pin is open, an internal pull up disables the device
	2	V_{ref}	3.3 V reference voltage
	3	Input +	DC input voltage
	4	Input +	DC input voltage
	5	Input GND	Return for input voltage source
	6	Input GND	Return for input voltage source
10	7	Output GND	Return for output voltage source
CO11	8	Output GND	Return for output voltage source
0/05	9	V_{out}	Regulated power output
Or	10	V_{out}	Regulated power output
	11	FB	Feedback input, available on adjustable device and on request for additional compensation
	12	Sync	Master/Slave synchronization

GS-R24F **Maximum ratings**

Maximum ratings 2

2.1 **Absolute maximum ratings**

Table 3. **Absolute maximum ratings**

Symbol	Parameter	Value	Unit
V _I	DC input voltage	36	V
I _{OUT}	Maximum output current	internally limit	
V ₁	INH	-0.3 to V _I	V
V ₁₂	Sync	-0.3 to 4	V
V ₁₁	FB	4	V

2.2 Thermal data

Table 4. Absolute maximum ratings

Therma	Thermal data					
Table 4.	Absolute maximum ratings	700				
Symbol	Parameter	Value	Unit			
T _{stg}	Storage temperature range	-40 ÷ 105	°C			
T _{op}	Operating ambient temperature	-25 ÷ 85	°C			
Thermal de-rating						

Thermal de-rating 2.3

Table 5. Thermal de-rating for free air condition (all versions)

	((()					
Symbol	Parameter	Test con	dition	Value	Unit	
	1,100	V _I = 16 ÷ 35 V	T _A = 60 °C	1.95		
	$Q_{Q_{Q_{i}}}$	V _I = 16 ÷ 35 V	T _A = 65 °C	1.85		
I _O O (Output current	V _I = 16 ÷ 35 V	T _A = 70 °C	1.75	A	
10	Output current	V _I = 16 ÷ 35 V	T _A = 75 °C	1.65		
10		V _I = 16 ÷ 35 V	T _A = 80 °C	1.55		
		V _I = 16 ÷ 35 V	T _A = 85 °C	1.40		

3 Electrical characteristics

 T_A = 25 °C, unless otherwise specified

Table 6. Electrical characteristics (all version)

Symbol	Parameter	Test condition	Min	Тур	Max	Unit
V _r	Ripple voltage	V _I =24 V I _o = 2A for GS-R24Fyyy2.0		25	35	mVpp
V _r	Ripple voltage	V _I =24 V I _o = 1.5 A for GS-R24F1201.5			100	mVpp
V _r Ripple voltage		V _I =24V I _o = 1 A for GS-R24F0001.0		100	250	mVpp
	Temperature stability	$V_1 = V_0 + 1.5 \text{ V } I_0 = 2 \text{ A}$				mV/°C
I _o Output current		V _I =16 ÷ 35 V for GS-R24Fyyy2.0	0		2	А
lo	Output current	V _I =16 ÷ 35 V for GS-R24F1201.5	0	111	1.5	Α
I _o Output current		V _I =16 ÷ 35 V for GS-R24F0001.0	00	0,	1	Α
l _{oL}	Current limit	V _I =16 ÷ 35 V		2.5		Α
Iq	Quiescent current	V _I =24 V I _o = 0 A		1.8	2.5	mA
I _{qst-by}	Total stand-by quiescent current	V _{inh} >2.2 V V _i = 35 V		80	150	μΑ
fs	Switching frequency	V _I = 24 V I _o = 2 A	225	250	275	kHz
V	Reference voltage	V _I = 16 ÷ 35 V I _{ref} = 0 ÷ 5 mA	3.234	3.3	3.366	V
V_{ref}	Short circuit current		8	10	30	mA
	INII I thus should visite as	Device ON			0.8	V
INILL	INH threshold voltage	Device OFF	2.2			V
INH	40	201.00 01 1				

4 Application information

4.1 Input voltage

The recommended maximum operating Dc input voltage is 35 V including ripple voltage.

4.2 Reference voltage

No capacitor is required for stability.

4.3 Inhibit function

The inhibit feature allows to put the device in stand-by mode.

With INH pin 1 is higher than 2.2 V the device is disabled and the current consumption is reduced to less than 150 μ A for V_I = 35 V.

With INH pin lower than 0.8 V, the device is enabled.

If the INH pin is left floating, an internal pull up ensures that the voltage at the pin reaches the inhibit threshold and the device is disabled.

The pin can be pulled to V_I to disable the device.

4.4 Multiple units synchronization

Using more than one unit on the same circuit, it is possible to synchronize the switching frequency, connecting all pin 12 together (see *Figure 5*).

The unit with higher frequency becomes the master.

4.5 Current limitation

The device has two current limit protections, pulse by pulse and frequency fold back.

The current is sensed through a resistor and if it reaches the threshold, the on time is reduced and consequently the output voltage, too.

Since the minimum switch ON time (necessary to avoid false overcurrent signal) is not enough to obtain a sufficiently low duty cycle at 250 Hz, the output current could increase again, in strong overcurrent or short circuit conditions.

For this reason the switching frequency is also reduced to keep the inductor current within its maximum threshold limit.

The frequency depends on the feedback voltage.

As the feedback voltage decreases (due to the reduced duty cycle), the switching frequency decrease too.

4.6 Thermal shutdown

The shutdown block generates a signal that turns off the power stage if the temperature of the internal chip goes higher than a fixed internal threshold (150°C min).

The sensing element of the chip is very close to the PDMOS area, so ensuring an accurate and fast temperature detection.

An hysteresis of approximately 20 °C avoids that the devices turns on and off continuously.

4.7 Output voltage programming (GS-R24F0002.0 and GS-R24F0001.0)

The GS-R24F0002.0 output voltage is $5.54 \text{ V} \pm 4 \%$, the GS-R24F0001.0 output voltage is 24.44 V $\pm 4 \%$, to reduce these values connect a resistor between pin 11 (FB) and pin 10 (V_{out}).

The resistor must be located very close to the proper pins, to minimize the injected noise (see figure 2).

The resistor value is calculated using the following formula:

for GS-R24F0002.0 Rv = $[(V_{out} - 1.235) * 11.3] / (5.54 - V_{out})$ $[k\Omega]$;

for GS-R24F0001.0 Rv = [(V_{out} - 1.235) * 62] / (24.44 - V_{out}) [$k\Omega$].

 V_{out} can be adjusted between 1.235 V (Rv = 0 Ω) and 5.54 V/24.44 V (Rv = open).

4.8 Loop compensation (GS-R24F0002.0 and GS-R24F0001.0)

If required by particular load conditions, it is possible to change the feedback loop compensation, adding an external capacitor between pin 11 (FB) and pin 10 (V_{out}), which will act as speed up (see *Figure 4*).

4.9 Soldering

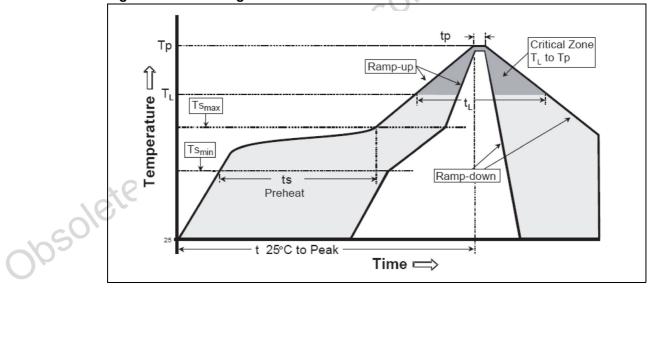
Soldering phase has to be execute with care: in order to avoid undesired melting phenomenon, particular attention has to be take on the set up of the peak temperature.

Here following some suggestions for the temperature profile based on IPC/JEDEC J-STD-020C ,July 2004 recommendations.

Table 7. Soldering

Profile feature	PB free assembly
Average ramp up rate (T _{SMAX} to T _P)	3 °C / sec max
Preheat	
Temperature min (T _S MIN)	150 °C
Temperature max (T _S MAX)	200 °C
Time $(T_S MIN to T_S MAX) (t_S)$	60 – 100 sec
Time maintained above :	
Temperature T _L	217 °C
Time t _L	40 – 70 sec
Peak temperature (T _P)	240 + 0 °C
Time within 5 °C of actual peak temperature (t _P)	10 – 20 sec
Ramp down rate	6 °C / sec
Time from 25 °C to peak temperature	8 minutes max





5 Additional features and protections

5.1 Output overvoltage protection

The overvoltage protection, OVP, is realized by using an internal comparator, whose input is connected to the feedback. It turns off the power stage when the OVP threshold is reached.

This threshold is typically 30 % higher than the feedback voltage.

Figure 3. Output voltage programming Figure 4. Loop compensation

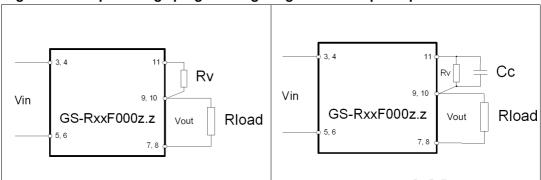
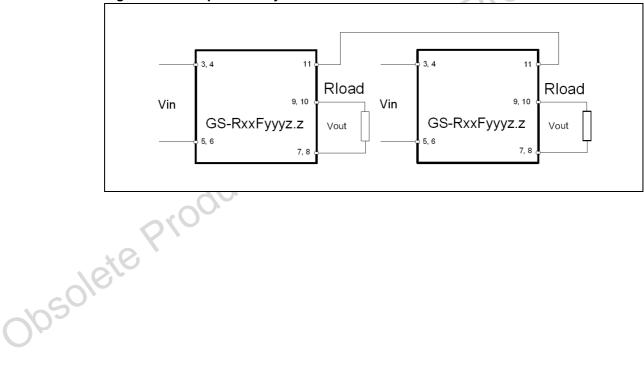


Figure 5. Multiple units synchronization



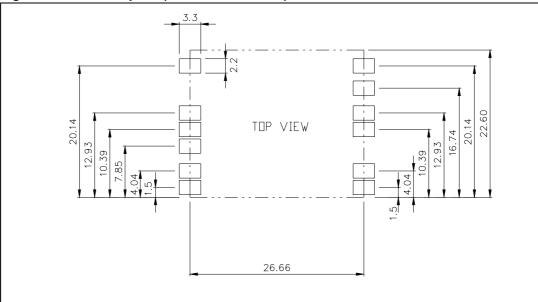


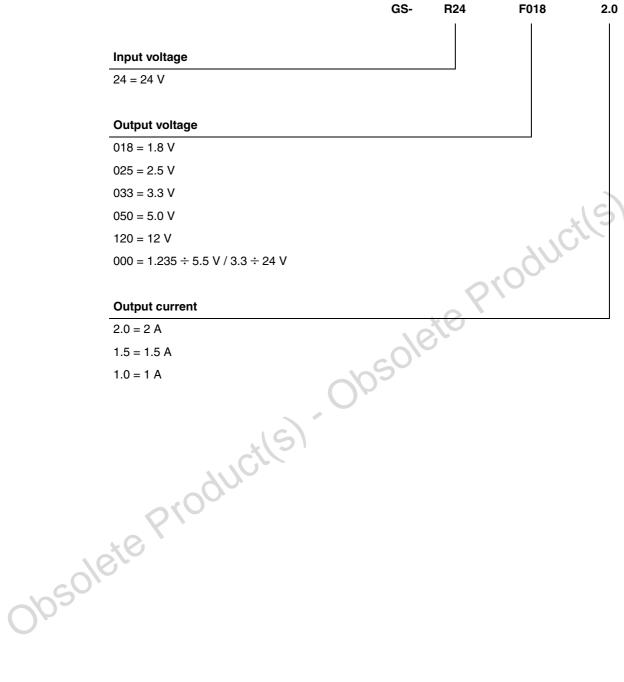
Figure 6. PCB footprint (dimensions in mm)

Obsolete Product(s). Obsolete Product

GS-R24F **Ordering scheme**

Ordering scheme 6

Table 8. Ordering information scheme



577

Revision history GS-R24F

7 Revision history

Table 9. Document revision history

Date	Revision	Changes	
13-Oct-2006	1	Initial release	
27-Feb-2008	2	Added: Section 4.9: Soldering on page 8	

Obsolete Product(s). Obsolete Product(s)

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