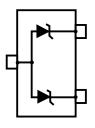


Automotive 2-line ESD protection for high speed lines

3

SOT323-3L (Jedec TO-236)



Functional diagram

Product status link			
HSP051-2W3Y			
Product summary			
Order code	HSP051-2W3Y		
Marking	H5Y		
Package	SOT323-3L		
Packing	Tape and reel		

Features



- · Flow-through routing to keep signal integrity
- Ultra large bandwidth: 3 GHz
- Ultra low capacitance: 0.7 pF
- Extended operating junction temperature range: -40 °C to 150 °C
- RoHS compliant
- Complies with ISO 10605 C = 150 pF, R = 330 Ω exceeds level 4
 - ±12 kV (contact discharge)
 - ±15 kV (air discharge)
- Complies with ISO 10605 C = 330 pF, R = 330 Ω
 - ±8 kV (contact discharge)
 - ±12 kV (air discharge)

Application

The HSP051-2W3Y is designed to protect against electrostatic discharge on automotive circuits such as:

- APIX
- LVDS & digital video interface
- Ethernet and BroadrReach
- USB 2.0 and USB 3.0
- · High speed communication buses

Description

The HSP051-2W3Y is an ESD array designed for high-speed differential lines protection.

The ultralow capacitance variation ensures negligible influence on signal-skew.



1 Characteristics

Table 1. Absolute maximum ratings (T_{amb} = 25 °C)

Symbol		Value	Unit	
		ISO10605 / IEC 61000-4-2 (C = 150 pF, R = 330 Ω):		
	Peak pulse voltage	Contact discharge	12	kV
V _{PP}		Air discharge	15	
У РР		ISO10605 (C = 330 pF, R = 330 Ω)		
		Contact discharge	8	
		Air discharge	12	
P _{PP}	Peak pulse power dissi	20	W	
I _{PP}	Peak Pulse current (8/2	1.8	Α	
T _{stg}	Storage temperature ra	-65 to +150	°C	
T _j	Operating junction temp	-40 to +150	°C	
T _L	Maximum lead temperature for soldering during 10 s			°C

Figure 1. Electrical characteristics - parameter definitions

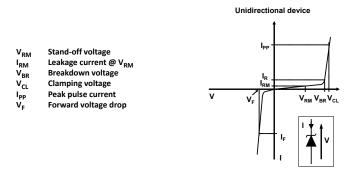


Table 2. Electrical characteristics (values) (T_{amb} = 25° C)

Symbol	Test conditions	Min.	Тур.	Max.	Unit
V _{BR}	I _R = 1 mA	5.3			V
I _R	V _R = 3 V			100	nA
'R	V _R = 5 V			150	ПА
V _{CL}	ISO 10605- C = 150 pF, R = 330 Ω		18		V
• CL	+8 kV contact discharge, measured at 30 ns		10		
C _{I/O-GND}	V _{I/O} = 0 V, f = 1 MHz, V _{OSC} = 30 mV		0.7	1.0	pF
ΔC _{I/O-GND}			0.03		рі
f _C	S ₂₁ = -3 dB		3		GHz

DS12686 - Rev 1 page 2/12



1.1 Characteristics (curves)

Figure 2. Leakage current versus junction temperature

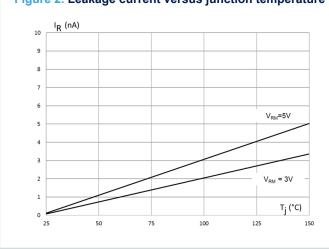


Figure 3. Junction capacitance versus reverse applied voltage

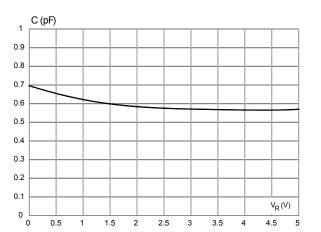


Figure 4. ESD response to ISO 10605 - C = 150 pF, R = 330Ω (+8 kV contact discharge)



Figure 5. ESD response to ISO 10605 - C = 150 pF, R = 330Ω (-8 kV contact discharge)



Figure 6. TLP

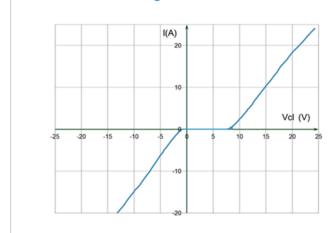
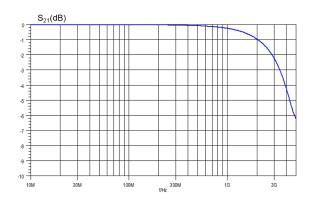
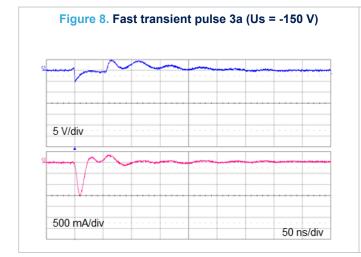


Figure 7. S21 attenuation

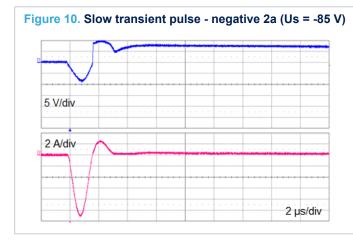


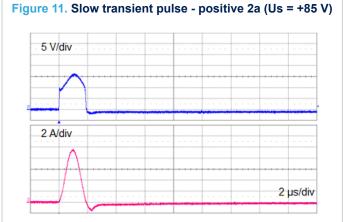
DS12686 - Rev 1 page 3/12





5 V/div
500 mA/div
50 ns/div





DS12686 - Rev 1 page 4/12



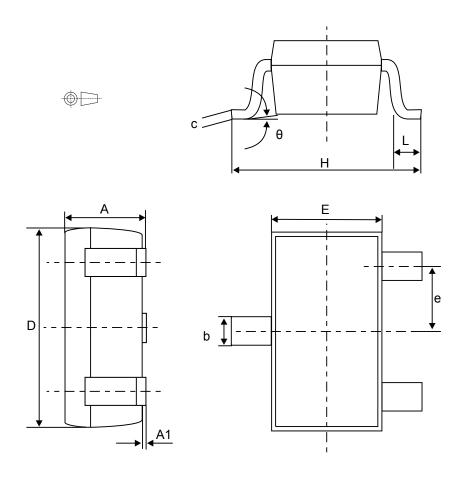
Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK® is an ST trademark.

2.1 SOT323-3L package information

- Epoxy meets UL 94,V0
- Lead-free package

Figure 12. SOT323-3L package outline



DS12686 - Rev 1 page 5/12



	Dimensions						
Ref.	Millimeters			Inches ⁽¹⁾			
	Min.	Тур.	Max.	Min.	Тур.	Max.	
Α	0.8		1.1	0.031		0.043	
A1	0.0		0.1	0.000		0.003	
b	0.25		0.4	0.0098		0.0157	
С	0.1		0.26	0.003		0.0102	
D	1.8	2.0	2.2	0.070	0.078	0.086	
E	1.15	1.25	1.35	0.0452	0.0492	0.0531	
е	0.60	0.65	0.70	0.024	0.026	0.028	
Н	1.8	2.1	2.4	0.070	0.082	0.094	
L	0.1	0.2	0.30	0.004	0.008	0.012	
Θ		0	30°	0		30°	

Table 3. SOT323-3L package mechanical data

Figure 13. SOT323-3L recommended footprint (dimensions in inches)

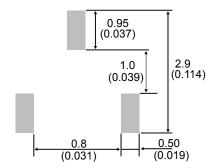
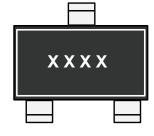


Figure 14. SOT323-3L marking



XXXX: Marking

DS12686 - Rev 1 page 6/12

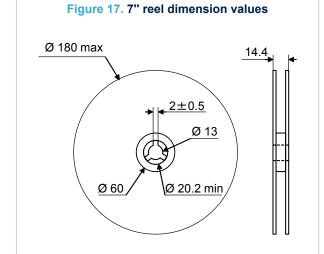
^{1.} Values in inches are converted from mm and rounded to 3 decimal digits



Pin 1 located according to EIA-481

Note: Pocket dimensions are not on scale Pocket shape may vary depending on package





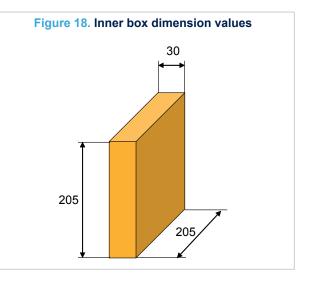
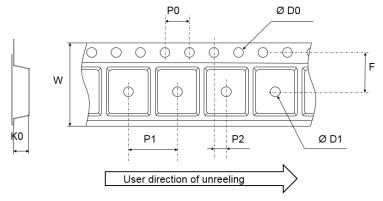


Figure 19. Tape outline



Note: Pocket dimensions are not on scale Pocket shape may vary depending on package

DS12686 - Rev 1 page 7/12



Table 4. Tape dimension values

	Dimensions					
Ref.	Millimeters					
	Min.	Тур.	Max.			
D0	1.45	1.5	1.6			
D1	1					
F	3.45	3.5	3.55			
K0	1.3	1.4	1.5			
P0	3.9	4.0	4.1			
P1	3.9	4.0	4.1			
P2	1.95	2.0	2.05			
W	7.9	8	8.3			

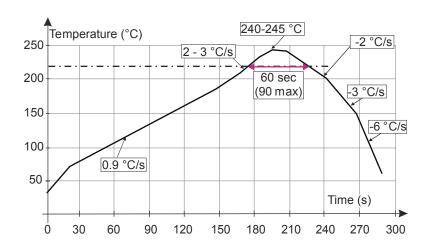
DS12686 - Rev 1 page 8/12



3 Recommendation on PCB assembly

3.1 Reflow profile

Figure 20. ST ECOPACK® recommended soldering reflow profile for PCB mounting



Note: Minimize air convection currents in the reflow oven to avoid component movement.

DS12686 - Rev 1 page 9/12



4 Ordering information

Figure 21. Ordering information scheme

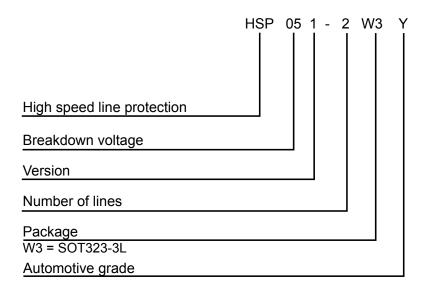


Table 5. Ordering information

Order code	Marking ⁽¹⁾	Package	Weight	Base qty.	Delivery mode
HSP051-2W3Y	H5Y	SOT323-3L	6 mg	3000	Tape and reel

1. The marking can be rotated by multiples of 90° to differentiate assembly location

DS12686 - Rev 1 page 10/12



Revision history

Table 6. Document revision history

Date	Version	Changes
10-Jul-2018	1	Initial release.

DS12686 - Rev 1 page 11/12



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DS12686 - Rev 1 page 12/12

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ESD119B1W01005E6327XTSA1 ESD5V0J4-TP ESD5V0L1B02VH6327XTSA1 ESD7451N2T5G 19180-510 CPDT-5V0USP-HF
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SCM1293A-04SO ESD200-B1-CSP0201 E6327 ESD203-B1-02EL E6327 SM12-7 SMF8.0A-TP SMLJ45CA-TP CEN955 W/DATA
82350120560 82356240030 VESD12A1A-HD1-GS08 CPDUR5V0R-HF CPDUR24V-HF CPDQC5V0U-HF CPDQC5V0USP-HF
CPDQC5V0-HF D1213A-01LP4-7B D1213A-02WL-7 ESDLIN1524BJ-HQ 5KP100A