

LK112S

Low noise and low drop voltage regulator with shutdown function

Datasheet - production data



Features

- Output current up to 200 mA
- Low-dropout voltage (500 mV max. at I_{OUT} = 200 mA)
- Very low quiescent current: 0.1 μ A in OFF mode and max. 250 μ A in ON mode at I_{OUT} = 0 mA
- Low output noise: typ. 30 µV at I_{OUT} = 60 mA and 10 Hz < f < 80 kHz
- Wide range of output voltages
- Internal current and thermal limit
- V_{OUT} tolerance ± 2% (at 25 °C)
- Operative input voltage from: V_{OUT} + 0.5 to 14 V (for V_{OUT} > 2 V) or from 2.5 V to 14 V (for V_{OUT} < 2 V)

Description

The LK112S is a low-dropout linear regulator with shutdown function. The internal switch can be controlled by TTL or CMOS logic levels. The device is ON when the control pin is pulled to a high logic level. An external capacitor can be connected to the noise bypass pin to reduce the output noise level to 30μ Vrms. An internal PNP pass transistor is used to achieve a low-dropout voltage.

The LK112S has a very low quiescent current in ON mode while in OFF mode the I_q is reduced to 100 nA max. The internal thermal shutdown circuitry limits the junction temperature below 150 °C. The load current is internally monitored and in the presence of a short-circuit or overcurrent conditions at the output, the device shuts down.

Table 1. Device summary

Part number	Output voltage
LK112SM18TR	1.8 V
LK112SM33TR	3.3 V
LK112SM50TR	5.0 V

This is information on a product in full production.

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1 Diagram

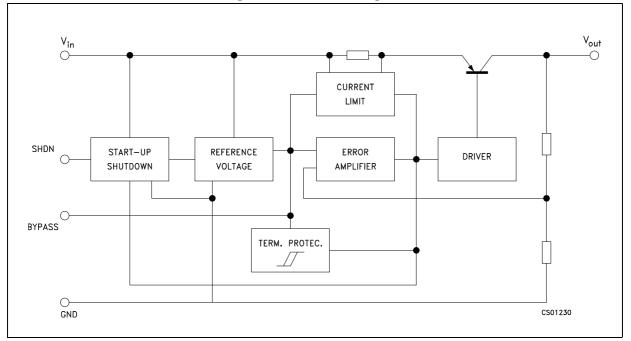
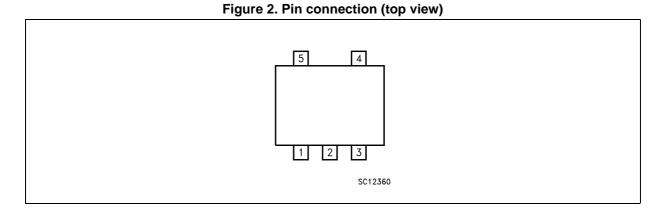


Figure 1. Schematic diagram



2 Pin configuration



Pin n°	Symbol	Note
1	SHDN	Shutdown input disables the regulator when it is connected to GND or to a positive voltage lower than 0.6 V $$
2	GND	Ground pin: internally connected to the die attach flag to decrease the total thermal resistance and increase the package ability to dissipate power
3	Bypass	Bypass pin: 0.1 μ F bypass to improve the thermal noise performance
4	OUT	Output port
5	IN	Input port



3 Maximum ratings

Symbol	Parameter	Value	Unit
VI	DC input voltage	16	V
V _{SHDN}	DC input voltage	16	V
Ι _Ο	Output current	Internally limited	
T _{STG}	Storage temperature range	-55 to 150	°C
T _{OP}	Operating junction temperature range	-40 to 125	°C

Table 4. Thermal data

Symbol	Parameter	SOT23-5L	Unit
R _{thJC}	Thermal resistance junction-case	81	°C/W
R _{thJA}	Thermal resistance junction-ambient	255	°C/W



4 Electrical characteristics

 T_J = 25 °C, V_{IN} = V_{OUT} +1 V, I_{OUT} = 0 mA, V_{SHDN} = 1.8 V, C_I = 1 μ F, C_O = 2.2 μ F, C_{BYPASS} = 0.1 μ F unless otherwise specified.

Table 5. LK 1125 electrical characteristics							
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit	
		ON mode (except I _{SHDN})		175	250	μA	
۱ _d	Quiescent current	OFF mode, $V_I = 8 V$, $V_{SHDN} = 0 V$		0	0.1	μA	
Vo	Output voltage	I _O = 30 mA	-2		+2	%	
A) /	Line regulation	$V_{I} = V_{O}$ +1 V to V_{O} +6 V, $V_{O} \le 5.6$ V		0.7	20	mV	
ΔV_{O}	Line regulation	$V_{I} = V_{O}$ +1 V to V_{O} +6 V, V_{O} > 5.6 V		0.8	40	mV	
A\/	Load regulation	I _O = 1 to 60 mA		15	30	mV	
ΔV_{O}	Load regulation	I _O = 1 to 200 mA		30	90	mV	
V	Dropout voltage	I _O = 60 mA		0.17	0.24	V	
V _d		$I_{O} = 200 \text{ mA}^{(1)}$		0.35	0.5	V	
I _{SC}	Short-circuit current		200			mA	
SVR	Supply voltage rejection	$V_{I} = V_{O}$ +1.5 V, $C_{BYP} = 0.1 \ \mu F$ $C_{O} = 10 \ \mu F$, f = 400 Hz, I _O = 30 mA		55		dB	
eN	Output noise voltage	B= 10 Hz to 80 kHz, $C_{BYP} = 0.1 \ \mu F$ $C_O = 10 \ \mu F$, $V_I = V_O + 1.5 \ V$, $I_O = 60 \ mA$		30		µVrms	
I _{SHDN}	Shutdown input current	V _{SHDN} = 1.8 V, output ON		12	35	μA	
V _{SHDN}	Shutdown input logic	Output ON	1.8				
		Output OFF			0.6	V	
$\Delta V_{O}/T_{J}$	Output voltage temperature coefficient	I _O = 10 mA		0.09		mV/°C	

Table 5.	LK112S	electrical	characteristics
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1. For versions with an output voltage higher than 2.1 V only.

Note: For versions with an output voltage lower than $2 \vee VIN = 2.4 \vee$



l_o=1 to 60mA

50

 $I_0=1$ to 100mA

75

T」(°C)

25

5 Typical characteristics

 $V_{\rm I}$ =3.5 to 8.5V $V_{\rm 0}$ =2.5V

0

25

50

75

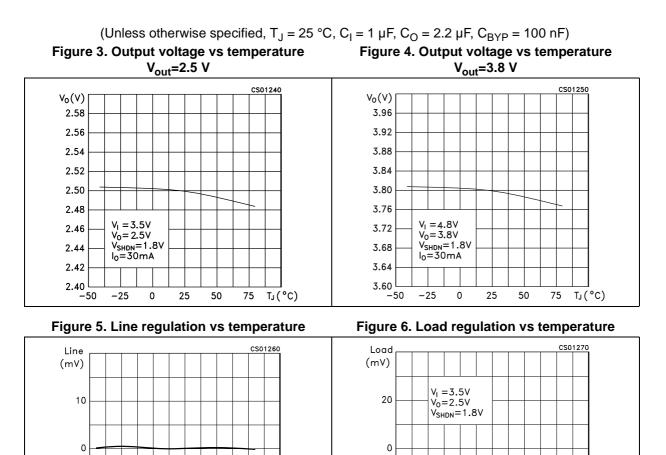
T」(°C)

 $V_{SHDN} = 1.8V$ $I_0 = 5mA$

-25

-10

-20 L



-20

-40 L -50

-25

0

Figure 7. Dropout voltage vs temperature

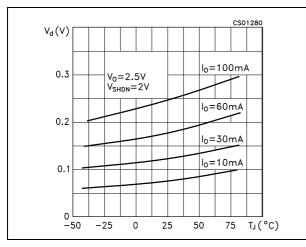


Figure 9. Output voltage vs input voltage

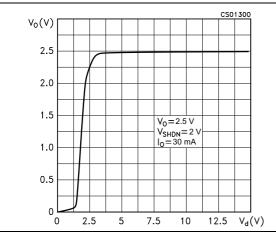


Figure 11. Shutdown current vs shutdown voltage

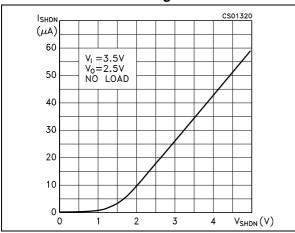
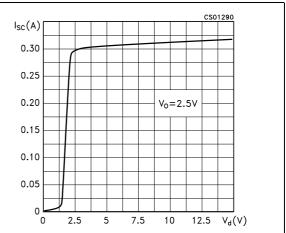
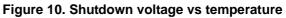


Figure 8. Short-circuit current vs dropout voltage





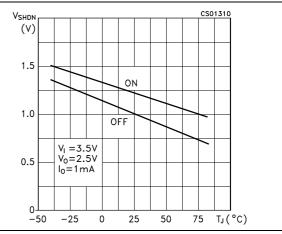


Figure 12. Supply voltage rejection vs temperature

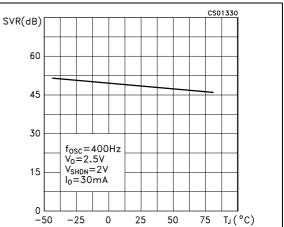




Figure 13. Supply voltage rejection vs output current

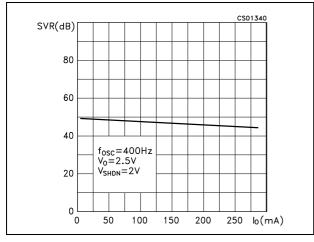


Figure 15. Supply voltage rejection vs temperature

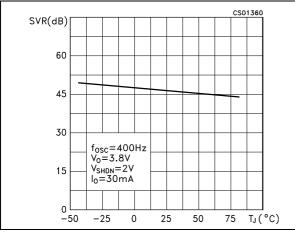


Figure 17. Quiescent current vs input voltage

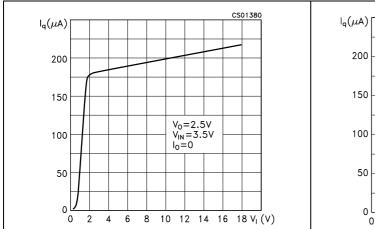


Figure 14. Supply voltage rejection vs frequency

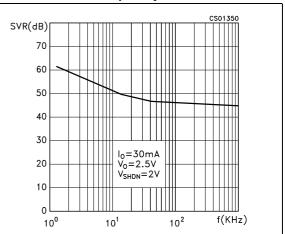


Figure 16. Shutdown current vs temperature

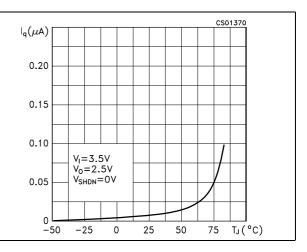
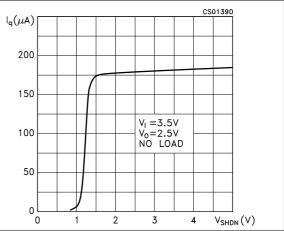


Figure 18. Quiescent current vs shutdown voltage





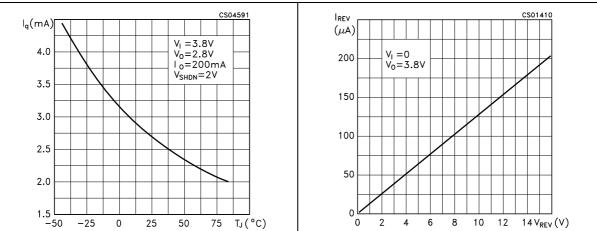
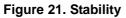


Figure 19. Quiescent current vs temperature



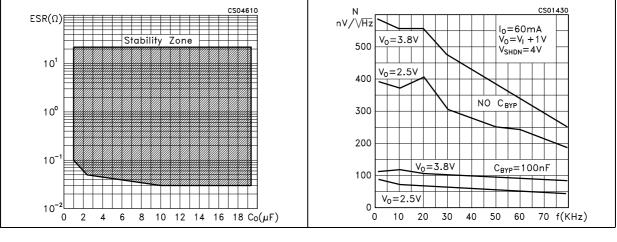


Figure 23. Start-up transient C_{BYP}=10 nF

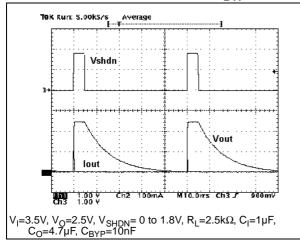
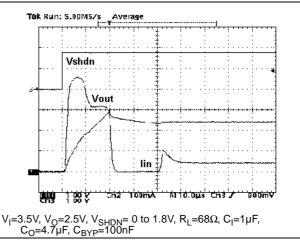


Figure 24. Start-up transient C_{BYP}=100 nF

Figure 22. Noise spectrum

Figure 20. Reverse current vs reverse voltage





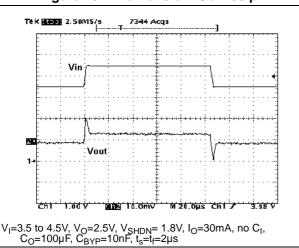
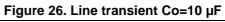


Figure 25. Line transient Co=100 µF



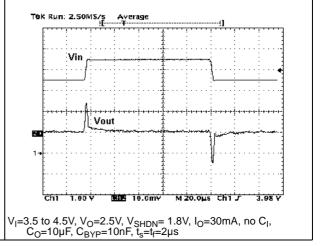


Figure 28. Load transient Vo=2.5 V, Co=2.2 µF

Figure 27. Line transient Co=1 µF

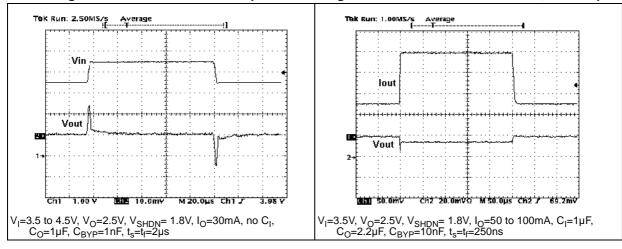
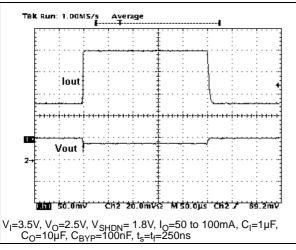
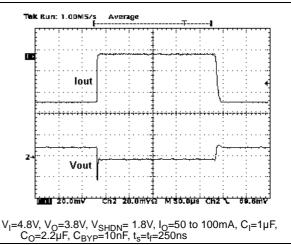


Figure 29. Load transient Vo=2.5 V, Co=10 µF



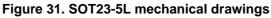


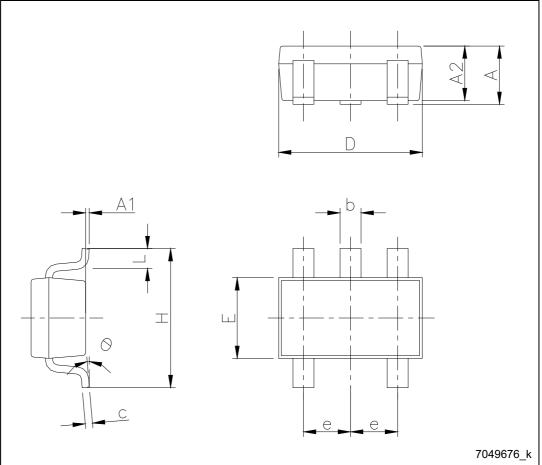




6 Package mechanical data

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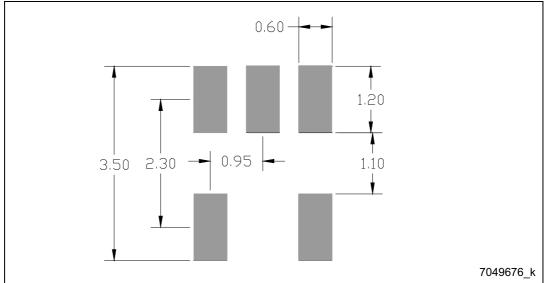




Dim.	mm			
	Min.	Тур.	Max.	
А	0.90		1.45	
A1	0		0.15	
A2	0.90		1.30	
b	0.30		0.50	
С	2.09		0.20	
D		2.95		
E		1.60		
e		0.95		
Н		2.80		
L	0.30		0.60	
θ	0		8	

Table 6. SOT23-5L mechanical data







7 Packaging mechanical data

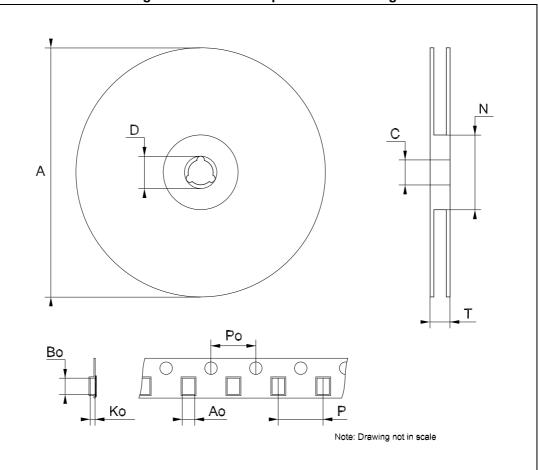


Figure 33.SOT23-5L tape and reel drawings



Figure 34.50123-5L tape and reel mechanical data				
Dim.	mm			
	Min.	Тур.	Max.	
A			180	
С	12.8	13.0	13.2	
D	20.2			
N	60			
Т			14.4	
Ao	3.13	3.23	3.33	
Во	3.07	3.17	3.27	
Ko	1.27	1.37	1.47	
Po	3.9	4.0	4.1	
Р	3.9	4.0	4.1	

Figure 34.SOT23-5L tape and reel mechanical data



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8 Revision history

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	Table 7. Document revision history
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Date	Revision	Changes
31-Aug-2004	3	Mistake on fig. 19.
31-Jan-2005	4	Change maturity code.
12-Jun-2006	5	Order codes updated.
17-Oct-2006	6	The T _{OP} value on table 2 updated.
20-Jul-2007	7	Add Table 1 in cover page.
21-Sep-2007	8	Features updated.
11-Dec-2007	9	Modified: Table 6.
12-Feb-2008	10	Modified: Table 6.
10-Jul-2008	11	Modified: Table 1 and Table 6.
11-Feb-2014	12	Part number LK112Sxx changed to LK112S. Updated the title and the <i>Description</i> in cover page, <i>Table 2: Pin description</i> , <i>Section 5: Typical characteristics</i> and <i>Section 6: Package mechanical data</i> . Added <i>Section 7: Packaging mechanical data</i> . Minor text changes.



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