

M74HC132

Quad 2-input Schmitt NAND gate

Datasheet - production data

- Wide operating voltage range:
 V_{CC} (opr) = 2 V to 6 V
- Pin and function compatible with 74 series 132
- ESD performance
 - HBM: 2 kV
 - MM: 200 V
 - CDM: 1 kV

Description

The M74HC132 is a high-speed CMOS quad 2-input Schmitt NAND gate fabricated with silicon gate C^2 MOS technology.

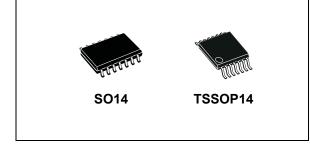
Pin configuration and function are identical to those of the M74HC00. The hysteresis characteristics (around 20 % V_{CC}) of all inputs allow slowly changing input signals to be transformed into sharply defined jitter-free output signals.

All inputs are equipped with protection circuits against static discharge and transient excess voltage.

Order code	Temp. range	Package	Packing	Marking
M74HC132RM13TR	-55 °C to 125 °C	S014		74HC132
M74HC132YRM13TR ⁽¹⁾	-40 °C to 125 °C	SO14 (automotive grade)	Tape and reel	74HC132Y
M74HC132TTR	-55 °C to 125 °C	TSSOP14	Tape and Teel	HC132
M74HC132YTTR ⁽¹⁾	-40 °C to 125 °C	TSSOP14 (automotive grade)		HC132Y

Table 1. Device summary

Qualification and characterization according to AEC Q100 and Q003 or equivalent, advanced screening according to AEC Q001 and Q002 or equivalent.



Features

- High-speed: t_{PD} = 11 ns (typ.) at V_{CC} = 6 V
- Low power dissipation:
 I_{CC} = 1 μA (max.) at T_A = 25 °C
- High noise immunity: V_H(typ) = 0.9 V at V_{CC} = 5 V
- Symmetrical output impedance: |I_{OH}| = I_{OL} = 4 mA (min.)
- Balanced propagation delays: $t_{PLH} \cong t_{PHL}$

This is information on a product in full production.

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1 Pin information

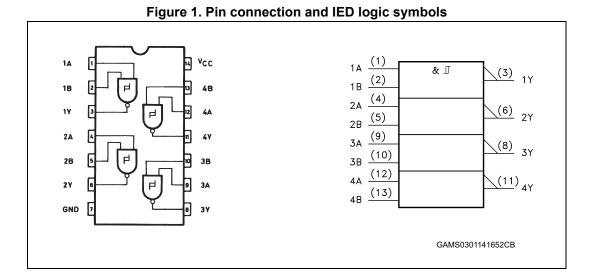


Table 2. Pin description

Pin no	Symbol	Name and function
1, 4, 9, 12	1A to 4A	Data inputs
2, 5, 10, 13	1B to 4B	Data inputs
3, 6, 8, 11	1Y to 4Y	Data outputs
7	GND	Ground (0 V)
14	V _{CC}	Positive supply voltage

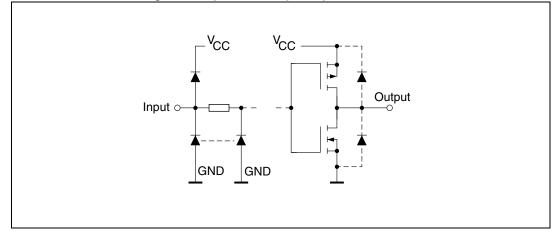


2 Functional description

Table 3. Truth table

A	В	Y
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

Figure 2. Input and output equivalent circuit





3 Electrical characteristics

"Absolute maximum ratings" are those values beyond which damage to the device may occur. Functional operation under these conditions is not implied.

Symbol	Parame	Value	Unit	
V _{CC}	Supply voltage	-0.5 to +7		
VI	DC input voltage			V
Vo	DC output voltage	-0.5 to V _{CC} + 0.5		
Ι _{ΙΚ}	DC input diode current	1.20		
Ι _{ΟΚ}	DC output diode current	±20		
Ι _Ο	DC output current	±25	mA	
I _{CC} or I _{GND}	DC V _{CC} or ground current	±50		
Р	Devuer dissinction	SO14	500 ⁽¹⁾	
PD	Power dissipation	450 ⁽¹⁾	mW	
T _{stg}	Storage temperature	-65 to +150		
ΤL	Lead temperature (10 sec)	300		

1. Power dissipation at 65 °C. Derating from 65 °C to 125 °C: SO14 -7 mW/°C, TSSOP14 -6.1 mW/°C.

Symbol	Parameter	Value	Unit
V _{CC}	Supply voltage	2 to 6	
VI	Input voltage	0 to V	V
V _O	Output voltage	0 to V _{CC}	
T _{op}	Operating temperature	-55 to 125	°C



Electrical characteristics

		٦	lest condition	Value							
Symbol	Parameter	v _{cc}		T,	T _A = 25 °C		-40 to 85 °C		-55 to 125 °C		Unit
		(V)		Min.	Тур.	Max.	Min.	Max.	Min.	Max.	
		2.0		1.0	1.25	1.5	1.0	1.5	1.0	1.5	
V _P	High level threshold voltage	4.5		2.3	2.7	3.15	2.3	3.15	2.3	3.15	V
		6.0		3.0	3.5	4.2	3.0	4.2	3.0	4.2	
		2.0		0.3	0.65	0.9	0.3	0.9	0.3	0.9	
V _N	Low level threshold voltage	4.5		1.13	1.6	2.0	1.13	2.0	1.13	2.0	V
		6.0		1.5	2.3	2.6	1.5	2.6	1.5	2.6	
		2.0		0.3	0.6	1.0	0.3	1.0	0.3	1.0	
V _H	Hysteresis voltage	4.5		0.6	1.1	1.4	0.6	1.4	0.6	1.4	V
		6.0		0.8	1.2	1.4	0.8	1.7	0.8	1.7	
		2.0	I _O = -20 μA	1.9	2.0		1.9		1.9		
		4.5	I _O = -20 μA	4.4	4.5		4.4		4.4		
V _{OH}	High level output voltage	6.0	I _O = -20 μA	5.9	6.0		5.9		5.9		V
		4.5	I _O = -4.0 mA	4.18	4.31		4.13		4.10		
		6.0	I _O = -5.2 mA	5.68	5.8		5.63		5.60		
		2.0	I _O = -20 μA			0.1		0.1		0.1	
		4.5	I _O = -20 μA			0.1		0.1		0.1	
V _{OL}	Low level output voltage	6.0	I _O = -20 μA			0.1		0.1		0.1	V
		4.5	I _O = -4.0 mA		0.17	0.26		0.33		0.40	
		6.0	I _O = -5.2 mA		0.18	0.26		0.33		0.40	
lı	Input leakage current	6.0	$V_{I} = V_{CC}$ or GND			±0.1		±1		±1	μA
I _{CC}	Quiescent supply current	6.0	$V_{I} = V_{CC}$ or GND			1		10		20	μA

Table 6. DC specifications



		Test condition	Value							
Symbol	Parameter	V AA	T _A = 25 °C			-40 to 85 °C		-55 to 125 °C		Unit
		V _{CC} (V)	Min.	Тур.	Max.	Min.	Max.	Min.	Max.	
		2.0		30	75		95		110	
t _{TLH} , t _{THL}	Output transition time	4.5		8	15		19		22	ns
		6.0		7	13		16		19	
		2.0	-	52	105	-	130	-	160	
t _{PLH} , t _{PHL}	Propagation delay time	4.5		13	21		26		32	ns
	une	6.0		11	18		22		27	

Table 7. AC electrical characteristics $(C_{L} = 50 \text{ pF}, \text{ Input } t_{r} = t_{f} = 6 \text{ ns})$

Table 8. Capacitive characteristics

Sym		Test condition	Value							
	Parameter	V _{CC} (V)	T _A = 25°C			-40 to 85 °C		-55 to 125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	
C _{IN}	Input capacitance	5.0		5	10		10		10	
C _{PD}	Power dissipation capacitance ⁽¹⁾		-	29		-		-		pF

1. C_{PD} is defined as the value of the IC's internal equivalent capacitance which is calculated from the operating current consumption without load (refer to test circuit). Average operating current can be obtained by the following equation: $I_{CC(opr)} = C_{PD} \times V_{CC} \times f_{IN} + I_{CC}/4$ (per gate).

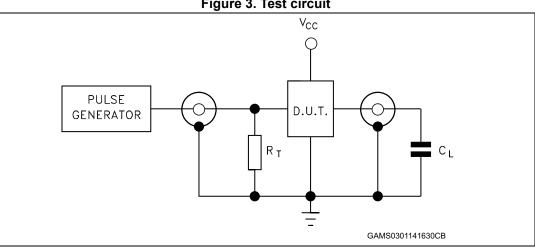


Figure 3. Test circuit

1.

Legend: C_L = 50 pF or equivalent (includes jig and probe capacitance). R_T = Z_{OUT} of pulse generator (typically 50 Ω).



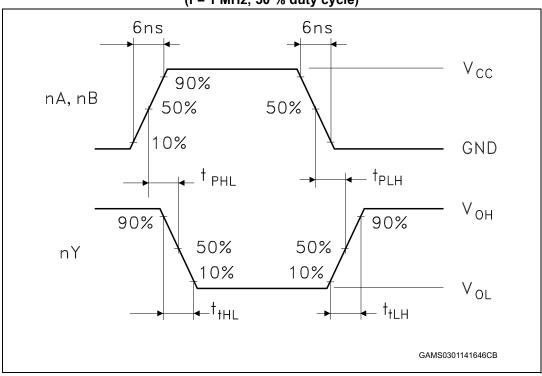


Figure 4. Propagation delay times (f = 1 MHz; 50 % duty cycle)



4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK[®] is an ST trademark.



4.1 SO14 package information

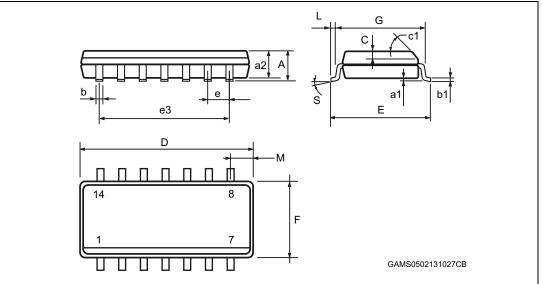


Figure 5. SO14 package mechanical drawing

Table 9. SO14 package mechanical data

	Dimensions									
Ref		Millimeters		Inches						
	Min.	Тур.	Max.	Min.	Тур.	Max.				
А			1.75			0.068				
a1	0.1		0.2	0.003		0.007				
a2			1.65			0.064				
b	0.35		0.46	0.013		0.018				
b1	0.19		0.25	0.007		0.010				
С		0.5			0.019					
c1		45 °			45 °					
D	8.55		8.75	0.336		0.344				
E	5.8		6.2	0.228		0.244				
е		1.27			0.050					
e3		7.62			0.300					
F	3.8		4.0	0.149		0.157				
G	4.6		5.3	0.181		0.208				
L	0.5		1.27	0.019		0.050				
М			0.68			0.026				
S			8 °			8 °				



4.2 TSSOP14 package information

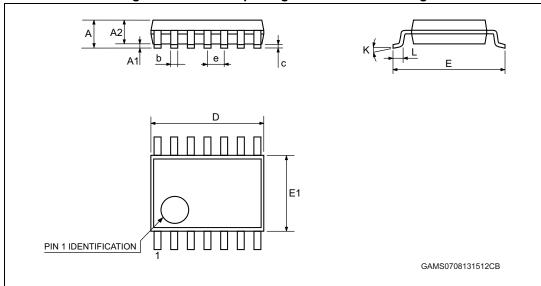


Figure 6. TSSOP14 package mechanical drawing

Table 10. TSSOP14 package mechanical data

-	Dimensions							
Ref	Millimeters			Inches				
	Min.	Тур.	Max.	Min.	Тур.	Max.		
А			1.2			0.047		
A1	0.05		0.15	0.002	0.004	0.006		
A2	0.8	1	1.05	0.031	0.039	0.041		
b	0.19		0.30	0.007		0.012		
С	0.09		0.20	0.004		0.0089		
D	4.9	5	5.1	0.193	0.197	0.201		
Е	6.2	6.4	6.6	0.244	0.252	0.260		
E1	4.3	4.4	4.48	0.169	0.173	0.176		
е		0.65			0.0256			
К	0 °		8 °	0 °		8 °		
L	0.45	0.60	0.75	0.018	0.024	0.030		

5 Ordering information

Order code	Temp. range Package		Packing	Marking				
M74HC132RM13TR	-55 °C to 125 °C	S014		74HC132				
M74HC132YRM13TR ⁽¹⁾	-40 °C to 125 °C	SO14 (automotive grade)	Tape and reel	74HC132Y				
M74HC132TTR	-55 °C to 125 °C	TSSOP14		HC132				
M74HC132YTTR ⁽¹⁾	-40 °C to 125 °C	TSSOP14 (automotive grade)		HC132Y				

Table 11. Order codes

1. Qualification and characterization according to AEC Q100 and Q003 or equivalent, advanced screening according to AEC Q001 and Q002 or equivalent.

6 Revision history

Date	Revision	Changes
10-Jan-2014	3	Removed DIP14 package Added ESD data to <i>Features</i> <i>Table 1: Device summary</i> : added automotive grade order codes, added temperature range and marking details. Added <i>Section 5: Ordering information</i> .

Table 12. Document revision history



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