

MC1489 MC1489A

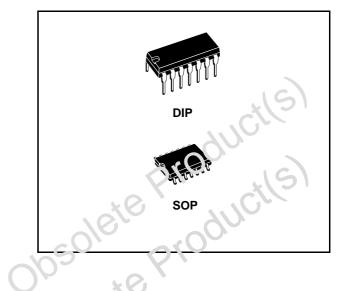
Quad line receivers

General features

- Input resistance –3k to $7k\Omega$
- Input signal range ± 30V
- Input threshold hysteresis built-in
- Response control: a) Logic threshold shifting b) Input noise filtering

Description

Obsolete Product(s) - Obsolete Obsolete Product(s) - Obsolete Obsolete Product(s) - Obsolete Obsolete Product(s) - Obsolete



Order code

Part Number			
DIP14	SO14 (Tape & reel)		
MC1489P	MC1489D1013TR		
MC1489AP	MC1488AD1013TR		

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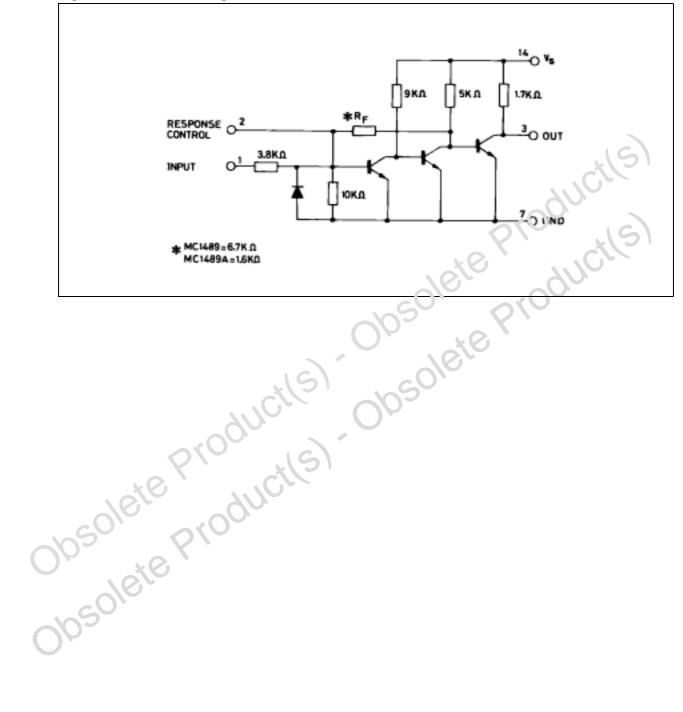
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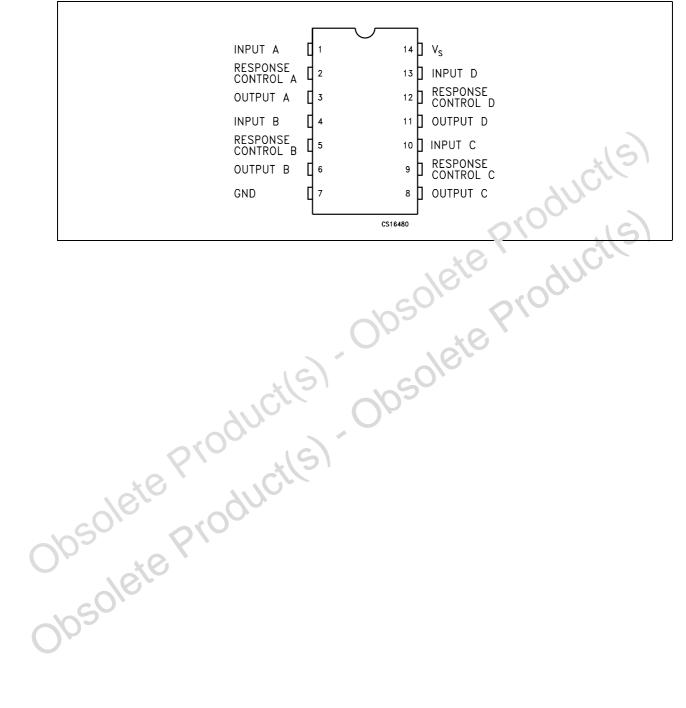
1 Diagram

Figure 1. Schematic diagram



2 Pin description







3 Maximum ratings

Table 1. Absolute maximum ratings

Symbol	Parameter	Value	Unit
VS	Power supply voltage	10	V
VI	Input voltage range	±30	V
I _{OL}	Output load current	20	mA
P _{TOT}	Power dissipation	1	W
T _{amb}	Operating ambient temperature	0 to 75	°C
T _{stg}	Storage temperature range	-65 to 150	0° C

Note: Absolute Maximum Ratings are those values beyond which damage to the Jevice may occur. Functional operation under these condition is not implied.

Symbol	Parameter	Pasic DIP14	SO14	l
R _{thJA}	Thermal resistance junction-ambient Max	200	165	0
	04	201		•
		lete		
	*(5)	5010		
	CL OF) /		
	01000.15)			
	Product(s)			
	ete Product(s)			
6018	ste Product(s)			
psole	ste Product(s)			
05018	ste Product(s)			
psole	ete Product(s) -			
105018	ste Product(s) - C			
ipsole	ete Product(s) - C			

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4 Electrical characteristics

Table 3.Electrical characteristics (Response control pin in open; $V_S = 5V$, $T_{amb} = 0$ to 75°C, unless
otherwise specified)

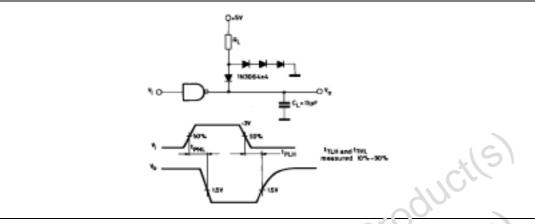
Symbol	Parameter	Test Con	Min.	Тур.	Max.	Unit	
	Dopitivo input ourront	V _{IH} = 25 V		3.6		8.3	mA
IIH	Positive input current	V _{IH} = 3 V		0.43			ШA
IIL	Negative input current	V _{IL} = - 25 V		-3.6		-8.3	mA
٩L	Negative input current	V _{IL} = - 3 V		-0.43		*	
	Input turn-on threshold	$T_{amb} = 25^{\circ}C,$	for MC1489	1		1.5	
V _{IH}	voltage	V _{OL} ≤ 0.45 V I _L = 10 mA	for MC1489A	1.75	1.95	2.25	V
V _{IL}	Input turn-off threshold voltage	$T_{amb} = 25^{\circ}C, V_{OL} \ge 2$.5 V I _L = -0.5 mA	0.75	90	1.25	~
N.	Output voltage high	$V_{IH} = 0.75 V, I_L = -0.5$	mA	2.5	4	5	V
V _{OH}	Output voltage high	I _L = 0.5 mA Input Ope	en Circuit	2.5	4	5	v
V _{OL}	Output voltage low	$V_{IL} = 3 V, I_L = 10 mA$		0.2	0.45	V	
I _{OS}	Output short circuit current		61,	-3	-4	mA	
۱ _S	Power supply current	All gates "ON", . = 0 mA, V _{IH} = 5 V		6	16	26	mA
P _C	Power consumption	V _{IH} = 5 v'	00		80	130	mW

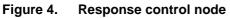
Table 4.Switching characteristics ($V_S = 5V$, $T_{amb} = 25^{\circ}C$)

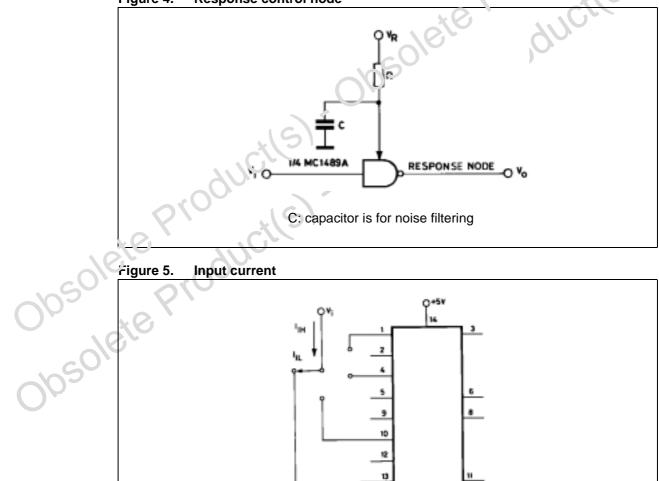
Symbol	Paramoter	Test Conditions	Min.	Тур.	Max.	Unit
t _{PLH}	Propagation oblay time	$R_L = 3.9 \text{ K}\Omega$		25	85	ns
t _{THL}	Pisetime	$R_L = 3.9 \text{ K}\Omega$		120	175	ns
t _{PHL}	Propagation delay time	R _L = 390 Ω		25	50	ns
ίτu _L	Fall time	R _L = 390 Ω		10	20	ns
0050	eter					

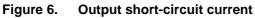
5 Test circuit

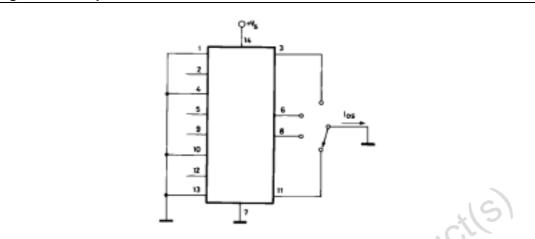


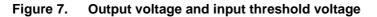


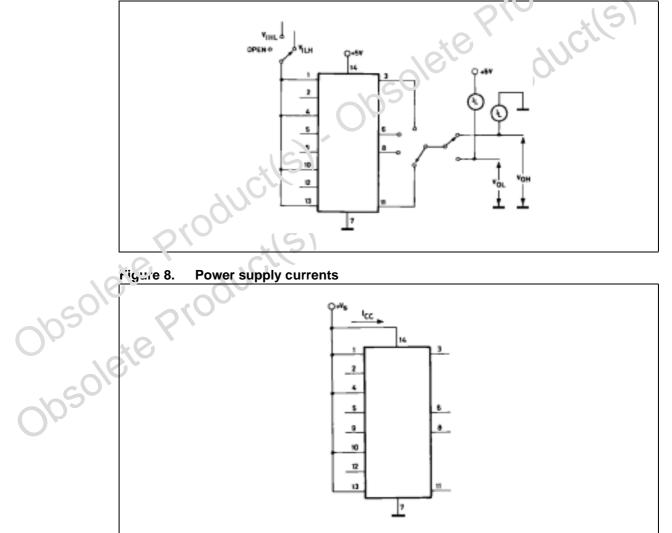






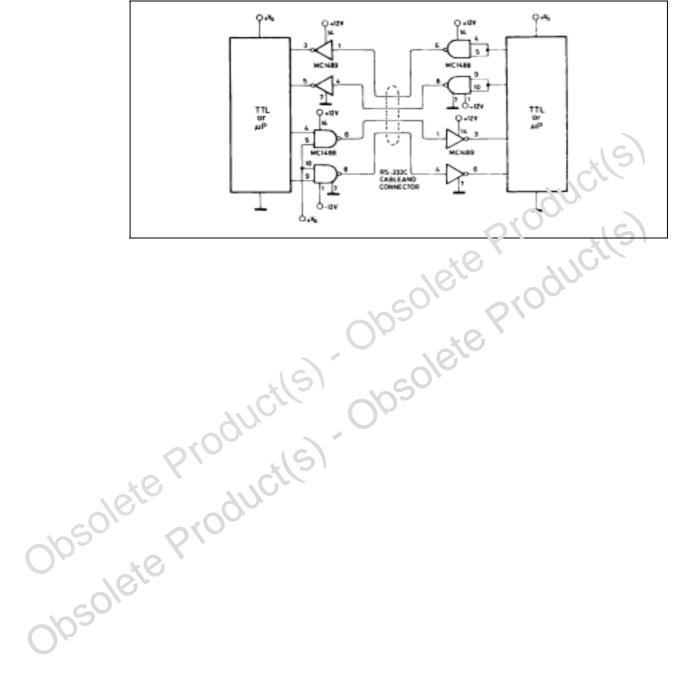






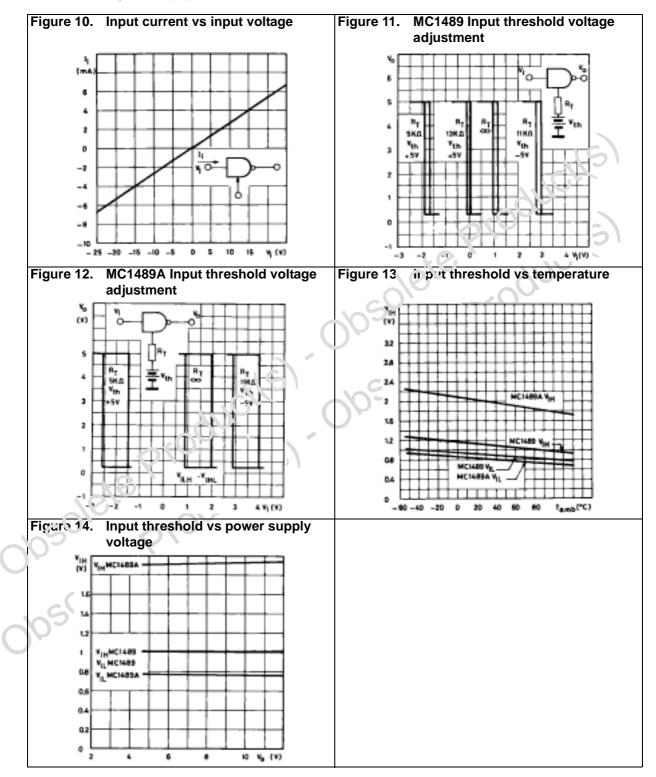
6 Typical applications





7 Typical characteristics

 $(V_{S} = 5V, T_{amb} = 25^{\circ}C)$



8 Application information

The Electronic Industries Association (EIA) has released the RS-232C specification detailing the requirements for the interface between data processing equipment and data communications equipment. This standard specifies not only the number and type of interface leads, but also the voltage levels to be used. The MC1488 quad driver and its companion circuit, the MC1489 quad receiver, provide a complete interface system between DTL or TTL logic levels and the RS-232C defined levels. The RS-232C requirements as applied to receivers are discussed herein.

The required input impedance is defined as between 3000Ω and 7000Ω for input voltages between 3.0 and 25V in magnitude; and any voltage on the receiver input in an open circuit condition must be less than 2.0V in magnitude. The MC1489 circuits meet these requirements with a maximum open circuit voltage of one V_{BE}.

The receiver shall detect a voltage between - 3.0 and - 25V as a Logic "1" and inputs between + 3.0 and + 2.5V as a Logic "0". On some interchange leads, an open circuit of power "OFF" condition (300Ω or more to ground) shall be decoded as an 'OFF" condition or Logic "1". For the reason, the input hysteresis thresholds of the MC1489 circuits are all above ground. Thus an open or grounded input will cause the same output as a negative or Logic "1" input.

8.1 Device characteristics

The MC1489 interface receivers have internal feedback from the second stage to the input stage providing input hysteresis for noise rejection. The MC1489 input has typical turn-on voltage of 1.25V and turn of of 1.0 V for a typical hysteresis of 250mV. The MC1489A has typical turn-on of 1.95V and turn-off of 0.8V for typically 1.15V of hysteresis.

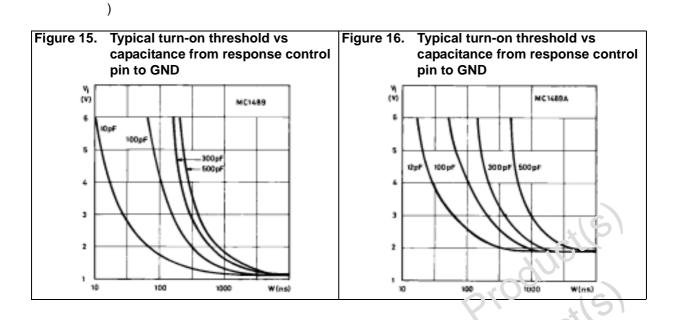
Each receiver section has an external response control node in addition to the input and output pins, the et y allowing the designer to vary the input threshold voltage levels. A resistor can be connected between this node and an external power supply. *Figure 4.*, *Figure 6.* and *Figure 7.* illustrate the input threshold voltage shift possible through this technique.

This response node can also be used for the filtering of the high-frequency, high-energy noise pulses. *Figure 11.* and *Figure 12.* show typical noise-pulse rejection for external capacitors of various sizes.

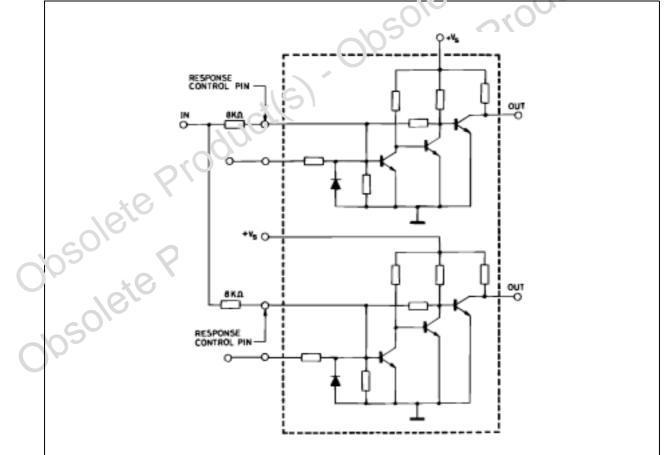
These two operations on the response node can be combined or used individually for may combinations of interfacing applications. The MC1489 circuits are particularly useful for interfacing between MOS circuits and MDTL/MTTL logic systems. In this application, the input threshold voltages are adjusted. (with the appropriate supply and resistor values) to fall in the center of the MOS voltage logic levels (see *Figure 13*.).

The response node may also be used as the receiver input as long as the designer realizes that he may not drive this node with a low impedance source to a voltage greater than one diode above ground or less than one diode below ground. This feature is demonstrated in *Figure 14.* where two receivers are slaved to the same line that must still meet the RS-232C impedance requirement.



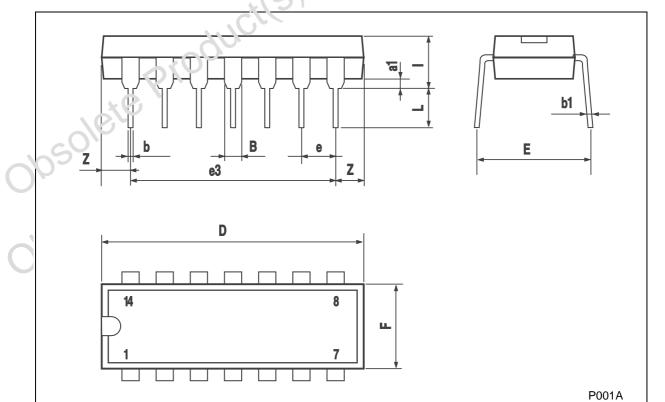






	Plastic DIP-14 MECHANICAL DATA								
DIM	mm.		inch						
DIM.	MIN.	ТҮР	MAX.	MIN.	TYP.	MAX.			
a1	0.51			0.020					
В	1.39		1.65	0.055		0.065			
b		0.5			0.020				
b1		0.25			0.010	16			
D			20			<u>८.787</u>			
E		8.5			0.3?5				
е		2.54			6.100				
e3		15.24		× 0,	0.600				
F			7.1	1610		0.280			
I			5.1	0		0.201			
L		3.3	02		0.130				
Z	1.27		2.54	0.050		0.100			



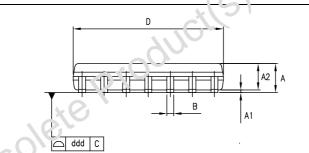


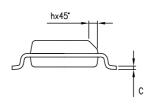
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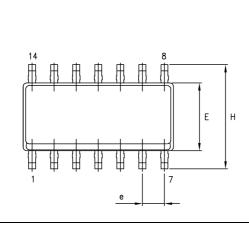
Application information

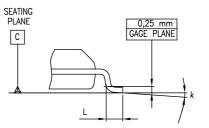
DIM.	mm.					
	MIN.	ТҮР	MAX.	MIN.	TYP.	MAX.
А	1.35		1.75	0.053		0.069
A1	0.1		0.25	0.004		0.010
A2	1.10		1.65	0.043		0.065
В	0.33		0.51	0.013		0.020
С	0.19		0.25	0.007		0.01เ
D	8.55		8.75	0.337	A	ો.344
E	3.8		4.0	0.150	20	0.157
е		1.27			0.90	
Н	5.8		6.2	0.228		0.244
h	0.25		0.50	0.010		0.020
L	0.4		1.27	0.016		0.050
k	0°		8	0°		8°
ddd			0.100			0.004

SO-14 MECHANICAL DATA







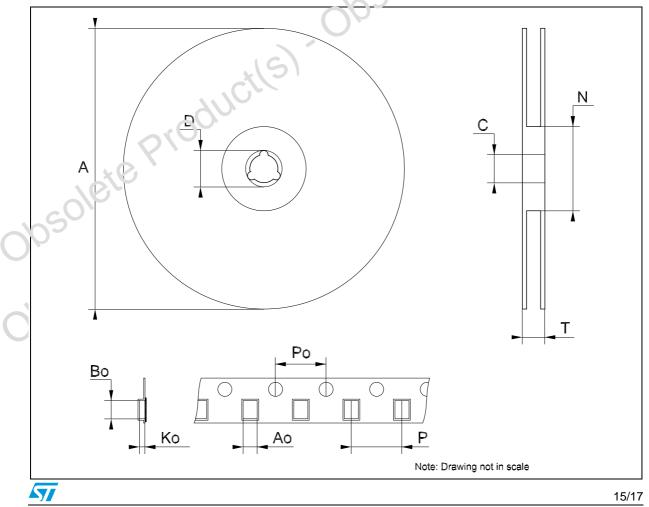


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DIM.	mm.			inch		
	MIN.	ТҮР	MAX.	MIN.	TYP.	MAX.
А			330			12.992
С	12.8		13.2	0.504		0.519
D	20.2			0.795		
Ν	60			2.362		16
Т			22.4		4	ົງ.882
Ao	6.4		6.6	0.252	70	0.260
Во	9		9.2	0.354	010	0.362
Ko	2.1		2.3	0.082		0.090
Po	3.9		4.1	0.15,3		0.161
Р	7.9		8.1	0.311		0.319



Tape & Reel SO-14 MECHANICAL DATA

9 Revision history

Table 5. Revision history

Date	Revision	Changes
13-Apr-2006	4	Order codes has been updated and new template.

Obsolete Product(s) - Obsolete Product(s) Obsolete Product(s) - Obsolete Product(s)



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