

## **SPV1001N**

## Cool bypass switch for photovoltaic applications

#### **Features**

- SPV1001N30 I<sub>F</sub>=12.5 A, V<sub>R</sub>=30 V
- SPV1001N40 I<sub>F</sub>=12.5 A, V<sub>R</sub>=40 V
- Very low forward voltage drop
- Very low reverse leakage current
- 150 °C operating junction temperature



■ Photovoltaic panels

#### **Description**

The SPV1001N is a system-in-package solution for photovoltaic applications to perform cool bypass rectification similar to that of a conventional Schottky diode but with much lower forward voltage drop and reverse leakage current.

The device consists of a power MOSFET transistor which charges a capacitor during the OFF time, and drives its gate during the ON time using the charge previously stored in the capacitor.

The ON and OFF times are set to reduce the average voltage drop across the drain and source terminals, resulting in reduced power dissipation.

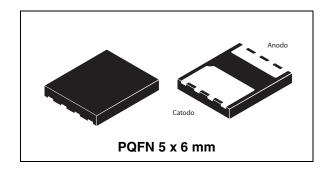


Table 1. Device summary

Order codes	Package	Packaging		
SPV1001N30	PQFN 5 x 6 mm	Topo and rool		
SPV1001N40	FQFN 5 X O IIIIII	Tape and reel		

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Maximum ratings SPV1001N

# 1 Maximum ratings

## 1.1 Absolute maximum ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Val	Unit		
Symbol	Farameter	SPV1001N30	SPV1001N40		
V <sub>R</sub>	Max DC reverse voltage	30	40	V	
I <sub>F</sub>	Max forward current	12.5	12.5	Α	
I <sub>FSM</sub>	Non repetitive peak surge (half-wave, single phase 50-60 Hz)	250	250	Α	
ESD level	Human body level	≥8 k	≥8 k	V	

#### 1.2 Thermal data

Table 3. Thermal data

Symbol	Parameter	Value		Unit
$T_J$	Junction temperature operating range	-40 to 150	-40 to 150	°C
T <sub>STG</sub>	Storage temperature range	-40 to 150	-40 to 150	°C
R <sub>thJC</sub>	Thermal resistance, junction-to-case	4	4	°C/W

## 2 Electrical characteristics

Table 4. Electrical characteristics

Cymhal	Dovemeter	Tost conditions		SPV1001 N30			SPV1001 N40			Unit
Symbol Parameter		Test conditions		Min.	Тур.	Max.	Min.	Тур.	Max.	Onit
	V <sub>F,AVG</sub> AVG forward voltage drop	IF = 10A	$T_J = 25^{\circ}C$	-	120	-	-	140	-	mV
$V_{F,AVG}$		IF = 5A	$T_J = 25^{\circ}C$	-	70	-	-	85	-	mV
			T <sub>J</sub> = 125°C	-	240	-	-	280	-	mV
ı	I <sub>R</sub> Reverse leakage current	VR = 30V	T <sub>J</sub> = 25°C	-	1	-	-	1	-	μA
'R			T <sub>J</sub> = 125°C	-	10	-	-	10	-	μA
D	TON/T ratio	IF = 5A	T <sub>J</sub> = 25°C	-	95%	-	-	95%	-	-
			T <sub>J</sub> = 125°C	-	75%	-	-	75%	-	-
		IF = 5A, T <sub>OFF</sub>	$T_J = 25^{\circ}C$	-	850	-	-	850	-	mV
V <sub>F</sub> Forward voltage drop	Forward voltage drop		T <sub>J</sub> = 125°C	-	600	-	-	600	-	mV
	i orward vollage drop	IF = 5A, T <sub>ON</sub>	$T_J = 25^{\circ}C$	-	35	-	-	40	-	mV
		11 – 5A, 10N	T <sub>J</sub> = 125°C	-	135	-	-	160	-	mV

Note: For correct power dissipation and heatsink sizing, please refer to Figure 1, 2 e 4

Device description SPV1001N

### 3 Device description

A photovoltaic panel consists of a series of PV cells. In optimal conditions, all the cells are equally irradiated and function at the same current level. However, during normal operation some cells may become partially shaded or obscured. These shaded cells limit the current generated by the fully irradiated cells and, in the extreme cases where these cells are totally obscured, the current flow is blocked.

In this case the shaded cells behave like a load, and the current generated from the fully irradiated cells produces overvoltages which can reach the breakdown threshold. This phenomenon, known as a "hot spot", can cause overheating of the shaded cells and, in some cases, even permanent damage resulting in current leakage. To prevent hot spots, therefore, bypass diodes are connected in parallel to the cell strings.

The device described here has the same functionality as a Schottky diode, but with improved performance. It features very low forward voltage drop and reverse leakage current. It consists of a power MOSFET transistor which charges a capacitor during the OFF time, and drives its gate during the ON time using the charge previously stored in the capacitor. The ON and OFF times are set to reduce the average voltage drop across the drain and source terminals, resulting in reduced power dissipation.

SPV1001N Device description

Figure 1. Average forward power dissipation vs average forward current @ 25°C of ambient temperature

Figure 2. Average forward power dissipation vs average forward current @ 75°C of ambient temperature

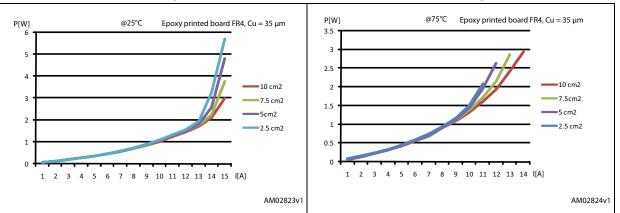
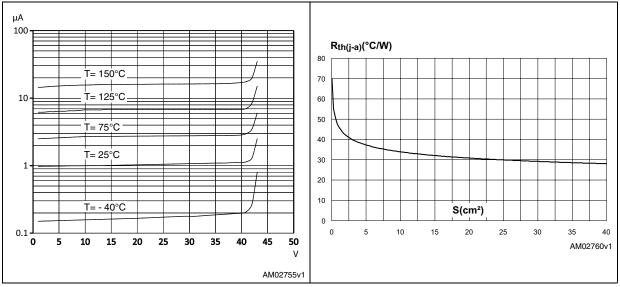


Figure 3. Reverse current

Figure 4. Thermal resistance junction-toambient vs copper surface under tab <sup>(1)</sup>



<sup>1.</sup> Epoxy printed board FR4,  $Cu = 35 \mu m$ 

# 4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: www.st.com. ECOPACK<sup>®</sup> is an ST trademark.

Table 5. PQFN 5 x 6 mm mechanical data

Dim.	mm				
Dilli.	Min.	Тур.	Max.		
А	0.85	0.80	0.95		
A1	0.02	0	0.05		
D	5.00				
D2	4.26	4.16	4.36		
Е	6.00				
E2	2.50	2.40	2.60		
е	1.27				
L	1.20	1.10	1.30		
L1	0.30				
NXb	0.45				

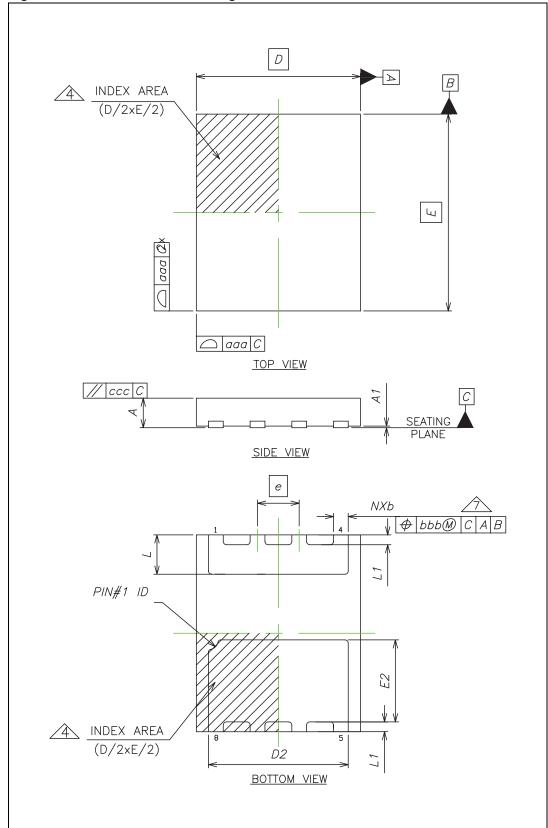


Figure 5. PQFN 5 x 6 mm drawing

Revision history SPV1001N

# 5 Revision history

Table 6. Document revision history

Date	Revision	Changes
20-Jun-2011	1	Initial release
16-Nov-2011	2	Updated Figure 3

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