

High voltage fast-switching NPN power transistor

Features

- High voltage capability
- Low spread of dynamic parameters
- Minimum lot-to-lot spread for reliable operation
- Very high switching speed

Applications

- Compact fluorescent lamps (CFLS)
- SMPS for battery charger

Description

The device is manufactured using high voltage multi epitaxial planar technology for high switching speeds and high voltage capability. It uses a cellular emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA.

The STBV32G and STBV32G-AP are supplied using halogen-free molding compound.

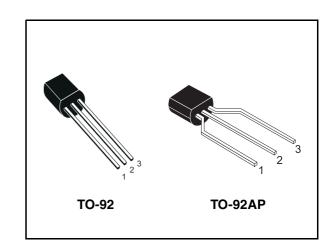


Figure 1. Internal schematic diagram

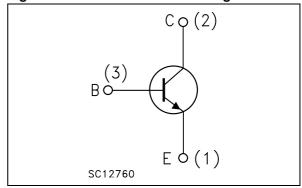


Table 1. Device summary

Order codes	Marking	Package	Packaging	
STBV32	BV32	TO-92	Bulk	
STBV32G	BV32G	TO-92	Bulk	
STBV32-AP	BV32	TO-92AP	Ammopack	
STBV32G-AP	BV32G	TO-92AP	Ammopack	

Electrical ratings STBV32

1 Electrical ratings

Table 2. Absolute maximum rating

Symbol	Parameter	Value	Unit	
V _{CES}	Collector-emitter voltage (V _{BE} = 0)	700	V	
V _{CEO}	Collector-emitter voltage (I _B = 0)	400	V	
V_{EBO}	Collector-base voltage ($I_C = 0$, $I_B = 0.5A$, $t_P < 10$ ms)	V _{(BR)EBO}	V	
I _C	Collector current (f \geq 100 Hz, duty-cycle \leq 50%, T _C = 25 °C)	1.5	А	
I _{CM}	Collector peak current (t _P < 5 ms)	3	Α	
I _B	Base current	0.5	Α	
I _{BM}	Base peak current (t _P < 5 ms)	1.5	Α	
P _{TOT}	Total dissipation at T _c = 25 °C	1.5	W	
T _{stg}	Storage temperature	-65 to 150	°C	
TJ	Max. operating junction temperature	150		

Table 3. Thermal data

Symbol	Parameter		Value	Unit
R _{thi-case}	Thermal resistance junction-case max		83.3	°C/W

2 Electrical characteristics

 $(T_{case} = 25 \, ^{\circ}C; \text{ unless otherwise specified})$

Table 4. Electrical characteristics

Symbol	Parameter	Test co	nditions	Min.	Тур.	Max.	Unit
I _{CES}	Collector cut-off current (V _{BE} = 0)	V _{CE} = 700 V V _{CE} = 700 V	T _C = 125 °C			1 5	mA mA
V _{(BR)EBO}	Emitter-base breakdown voltage ($I_C = 0$)	I _E = 10 mA		9		18	V
V _{CEO(sus)} ⁽¹⁾	Collector-emitter sustaining voltage (I _B = 0)	I _C = 10 mA		400			V
V _{OE} (1)	Collector-emitter saturation voltage	$I_{C} = 0.5 A$ $I_{C} = 1 A$	_			0.5 1	V V
*CE(sat)	voltage	I _C = 1.5 A	_			1.5	V
V _{BE(sat)} (1)	Base-emitter saturation voltage	I _C = 0.5 A I _C = 1 A	I _B = 100 mA I _B = 250 mA			1 1.2	V V
h _{FE}	DC current gain	$I_C = 0.5 \text{ mA}$ $I_C = 0.5 \text{ A}$ $I_C = 1 \text{ A}$		20 8 5		25 25	
t _r t _s	Resistive load Rise time Storage time Fall time	$I_C = 1 \text{ A}$ $I_{B1} = -I_{B2} = 200 \text{ r}$ $V_{CC} = 125 \text{ V}$	mA [']			1 4 0.7	μs μs μs
t _s	Inductive Load Storage time	$I_C = 1 A$ $I_{B1} = 200 \text{ mA}$ L = 50 mH Figure 13.	$V_{clamp} = 300 \text{ V}$ $V_{BE(off)} = -5 \text{ V}$ $R_{BB} = 0$		0.8		μs

^{1.} Pulsed duration = 300 µs, duty cycle ≤1.5%

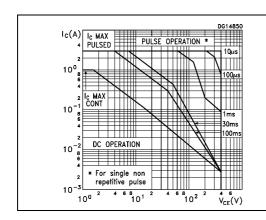
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Electrical characteristics STBV32

2.1 Electrical characteristics (curves)

Figure 2. Safe operating area

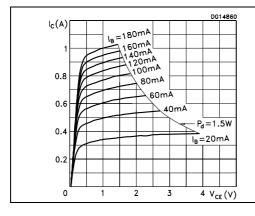
Figure 3. Derating curve



P_{tot} (%)
100
50
P_{tot} (°C)

Figure 4. Output characteristics

Figure 5. Reverse biased safe operating area



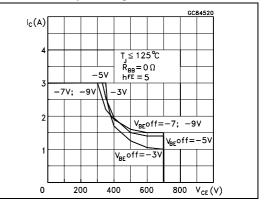
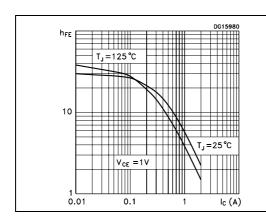
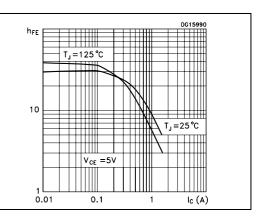


Figure 6. DC current gain

Figure 7. DC current gain



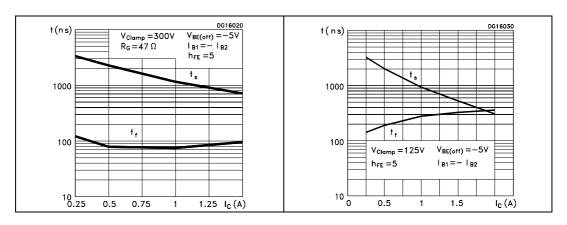


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STBV32 Electrical characteristics

Figure 8. **Collector-emitter saturation** Figure 9. **Base-emitter saturation** voltage voltage DG16010 V_{CE (sat)} (V) V_{BE(sat)} (V) $h_{FE} = 5$ $h_{FE} = 5$ 1.1 T_J =125 °C 1.0 T_J =125 °C 0.9 0.8 0.1 0.7 0.01 0.5 C 0.1 1_c (A) 0.1 $I_{c}(A)$

Figure 10. Inductive load switching time Figure 11. Resistive load switching time



Electrical characteristics STBV32

2.2 Test circuits

Figure 12. Resistive load switching test circuit

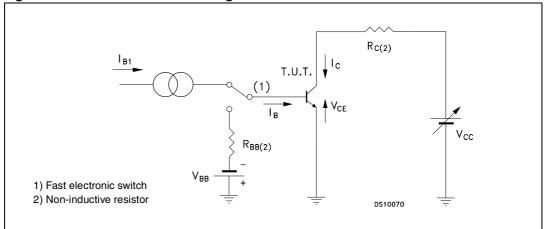
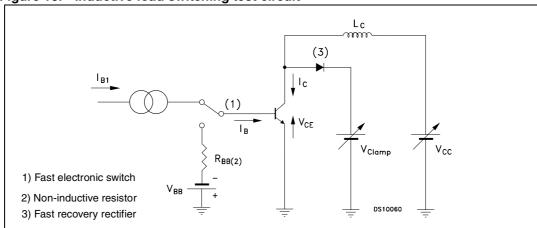


Figure 13. Inductive load switching test circuit

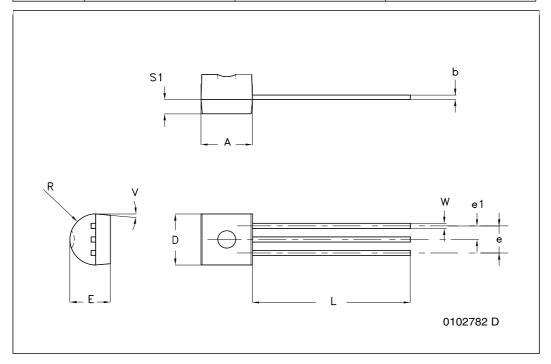


3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect . The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

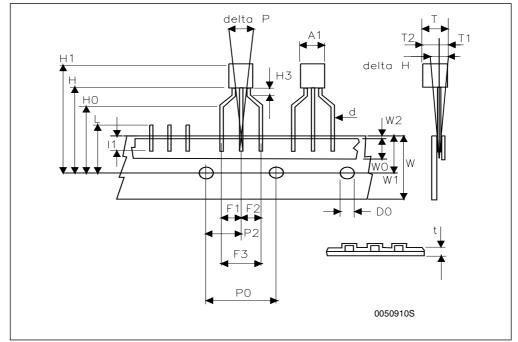
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DIM.	mm.				
	MIN.	ТҮР	MAX.		
Α	4.32		4.95		
b	0.36		0.51		
D	4.45		4.95		
E	3.30		3.94		
е	2.41		2.67		
e1	1.14		1.40		
L	12.70		15.49		
R	2.16		2.41		
S1	0.92		1.52		
W	0.41		0.56		
V		5°			



TO-92 ammopack shipment (suffix"-AP") mechanical data

Dim		mm	
Dim.	Min	Тур	Max
A1			4.80
Т			3.80
T1			1.60
T2			2.30
d			0.48
P0	12.50	12.70	12.90
P2	5.65	6.35	7.05
F1,F2	2.44	2.54	2.94
F3	4.98	5.08	5.48
delta H	-2.00		2.00
W	17.50	18.00	19.00
W0	5.70	6.00	6.30
W1	8.50	9.00	9.25
W2			0.50
Н	18.50		20.50
H3	0.5	1	1.5
H0	15.50	16.00	16.50
H1			25.00
D0	3.80	4.00	4.20
t			0.90
L			11.00
I1	3.00		
delta P	-1.00		1.00



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Revision history STBV32

4 Revision history

Table 5. Document revision history

Date	Revision	Changes
02-Jul-2008	8	Added halogen-free molding compound package.

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