

STBV42D

High voltage fast-switching NPN power transistor

Preliminary data

Features

- High voltage capability
- Low spread of dynamic parameters
- Very high switching speed
- Integrated free-wheeling diode

Application

■ Compact fluorescent lamps (CFLs)

Description

The device is manufactured using high voltage multi epitaxial planar technology for high switching speeds and high voltage capability. It uses a cellular emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA.

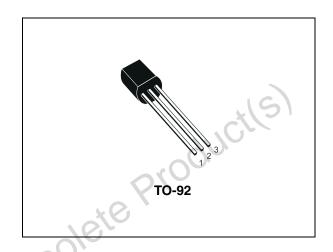


Figure 1. Internal schematic diagram

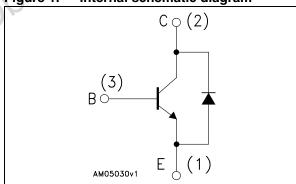


Table 1. Device summary

Order code	Marking	Package	Packaging
STBV42D	BV42D	TO-92	BAG

April 2010 Doc ID 17236 Rev 2 1/8

Electrical ratings STBV42D

1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{CES}	Collector-emitter voltage (V _{BE} = 0)	700	V
V _{CEO}	Collector-emitter voltage (I _B = 0)	400	V
V _{EBO}	Collector-base voltage (I _C = 0)	9	V
I _C	Collector current	1	Α
I _{CM}	Collector peak current (t _P < 5 ms)	2	Α
I _B	Base current	0.5	SA
I _{BM}	Base peak current (t _P < 5 ms)	1 G	Α
P _{TOT}	Total dissipation at T _C = 25 °C	Og/Or	W
T _{STG}	Storage temperature	- 65 to 150	°C
TJ	Max. operating junction temperature	150	

Table 3. Thermal data

	Symbol	Parameter	Value	Unit
	R_{thJC}	Thermal resistance junction-case	125	°C/W
Obsole	H _{thJC} Inermal resistance junction-case			

2 Electrical characteristics

 T_{case} = 25 °C; unless otherwise specified.

Table 4. Electrical characteristics

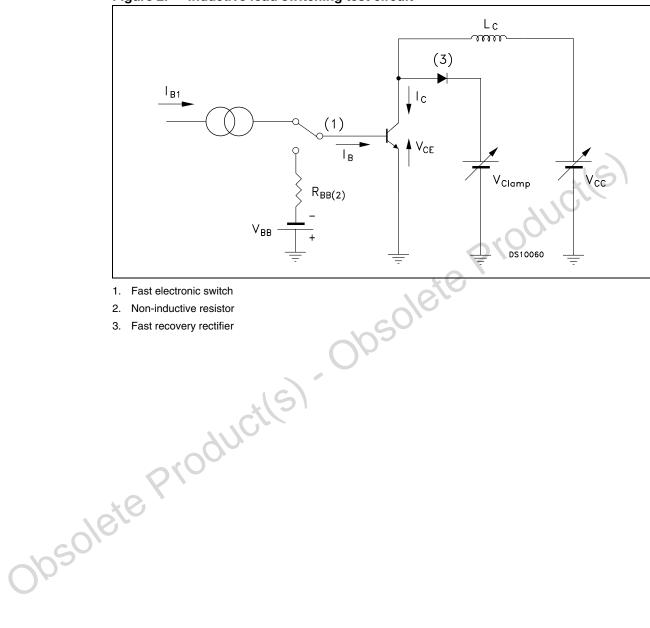
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CES}	Collector cut-off current (V _{BE} = 0)	V _{CE} = 700 V V _{CE} = 700 V T _C = 125 °C			1 5	mA mA
I _{EBO}	Emitter cut-off current (I _C = 0)	V _{EB} = 9 V			1	mA
V _{CEO(sus)}	Collector-emitter sustaining voltage (I _B = 0)	I _C = 1 mA	400	~	5	V
V _{CE(sat)} (1)	Collector-emitter saturation voltage	$\begin{split} & I_{C} = 0.25 \text{ A} & I_{B} = 50 \text{ mA} \\ & I_{C} = 0.5 \text{ A} & I_{B} = 125 \text{ mA} \\ & I_{C} = 0.75 \text{ A} & I_{B} = 250 \text{ mA} \end{split}$	09,	0.2 0.3 0.4	0.5 1 1.5	V V V
V _{BE(sat)} (1)	Base-emitter saturation voltage	$I_C = 0.25 \text{ A}$ $I_B = 50 \text{ mA}$ $I_C = 0.5 \text{ A}$ $I_B = 125 \text{ mA}$			1 1.2	V V
h _{FE} ⁽¹⁾	DC current gain	$\begin{split} & I_{\text{C}} = 5 \text{ mA}, & V_{\text{CE}} = 2 \text{ V} \\ & I_{\text{C}} = 0.4 \text{ A}, & V_{\text{CE}} = 5 \text{ V} \\ & I_{\text{C}} = 0.8 \text{ A} & V_{\text{CE}} = 5 \text{ V} \end{split}$	12 10 5		30 20	
t _f	Inductive Load Fall time	$I_C = 0.25 \text{ A}$ $V_{clamp} = 300 \text{ V}$ $I_{B(on)} = -I_{B(off)} = 50 \text{ mA}$ L = 3 mH Figure 2		0.3		μs
V _F	Diode forward voltage	I _F = 350 mA			1.7	V

^{1.} Pulse test: pulse duration ≤ 300 μs, duty cycle ≤ 2 %

577

2.1 **Test circuit**

Figure 2. Inductive load switching test circuit



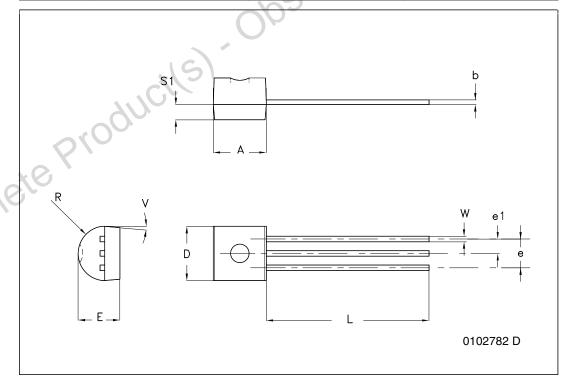
3 Package mechanical data

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Obsolete Product(s). Obsolete Product(s)

TO-92 bulk shipment mechanical data

DIM.	mm.				
	MIN.	ТҮР	MAX.		
А	4.32		4.95		
b	0.36		0.51		
D	4.45		4.95		
E	3.30		3.94		
е	2.41		2.67		
e1	1.14		1.40		
L	12.70		15.49		
R	2.16		2.41		
S1	0.92		1.52		
W	0.41	10,10	0.56		
V		5°			



STBV42D Revision history

4 Revision history

Table 5. Document revision history

Date	Revision	Changes
08-Mar-2010	1	First release.
28-Apr-2010	2	Inserted V _F maximum value <i>Table 4 on page 3</i> .

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577

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