

STD845DN40

Dual NPN high voltage transistors in a single package

Datasheet — production data

Features

- Low V_{CE(sat)}
- Simplified circuit design
- Reduced component count
- Fast switching speed

Applications

- Compact fluorescent lamp (CFL) 220 V mains
- Electronic ballast for fluorescent lighting



The device is a dual NPN high voltage power transistor manufactured using multi-epitaxial planar technology. It is housed in dual-island DIP-8 package with separated terminals to provide a high degree of assembly flexibility.

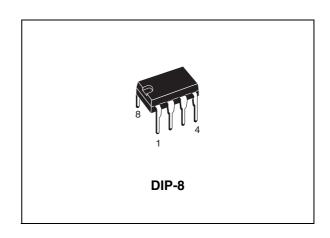


Figure 1. Internal schematic diagram

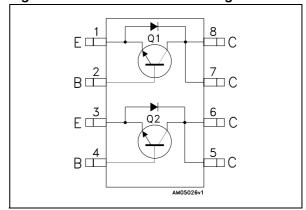


Table 1. Device summary

Order code	Marking	Package	Packaging
STD845DN40	D845DN40	DIP-8	Tube

Electrical ratings STD845DN40

1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{CBO}	Collector-base voltage (I _E = 0)	700	V
V _{CEO}	Collector-emitter voltage (I _B = 0)	400	V
V _{EBO}	Emitter-base voltage ($I_C = 0$, $I_B = 2$ A, $t_p < 10$ ms)	V _{(BR)EBO}	V
I _C	Collector current	4	Α
I _{CM}	Collector peak current (t _P < 5 ms)	8	Α
I _B	Base current	2	Α
I _{BM}	Base peak current (t _P < 5 ms)	4	Α
В	Total dissipation at T _{amb} = 25 °C single transistor	3	W
P _{TOT}	Total dissipation at T _{case} = 25 °C single transistor	45	W
T _{STG}	Storage temperature	-65 to 150	°C
TJ	Max. operating junction temperature	150	°C

Table 3. Thermal data

Symbol	Parameter	Value	Unit
R _{thJA} ⁽¹⁾	Thermal resistance junction-ambient (single transistor)	42	°C/W
R _{thJC}	Thermal resistance junction-case (single transistor)	2.7	°C/W

^{1.} Device mounted on PCB area of 25 mm².

2 Electrical characteristics

 T_{case} = 25 °C unless otherwise specified.

Table 4. Electrical characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I _{CES}	Collector cut-off current (V _{BE} = 0)	V _{CE} = 700 V V _{CE} = 700 V T _c = 125 °C			100 500	μA μA
I _{CEO}	Collector cut-off current (I _B = 0)	V _{CE} = 400 V			250	μА
V _{(BR)EBO}	Emitter-base breakdown voltage (I _C = 0)	I _E = 10 mA	9		18	٧
V _{CEO(sus)} ⁽¹⁾	Collector-emitter sustaining voltage (I _B = 0)	I _C = 100 mA	400			V
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	$\begin{aligned} I_C &= 0.5 \text{ A} & I_B &= 0.1 \text{ A} \\ I_C &= 1 \text{ A} & I_B &= 0.2 \text{ A} \\ I_C &= 2.5 \text{ A} & I_B &= 0.5 \text{ A} \\ I_C &= 4 \text{ A} & I_B &= 1 \text{ A} \end{aligned}$	4	0.5	0.7 1 1.5	> > >
V _{BE(sat)} ⁽¹⁾	Base-emitter saturation voltage	$\begin{split} I_C &= 0.5 \text{ A} & I_B &= 0.1 \text{ A} \\ I_C &= 1 \text{ A} & I_B &= 0.2 \text{ A} \\ I_C &= 2.5 \text{ A} & I_B &= 0.5 \text{ A} \end{split}$	4		1.1 1.2 1.3	V V V
h _{FE} ⁽¹⁾	DC current gain	$I_C = 10 \text{ mA}$ $V_{CE} = 5 \text{ V}$ $I_C = 2 \text{ A}$ $V_{CE} = 5 \text{ V}$			32	
V _F	Diode forward voltage	I _F = 2 A			2.5	٧
t _s	Resistive load Storage time Fall time	$I_C = 2 \text{ A}$ $I_{B(on)} = -I_{B(off)} = 400 \text{ mA}$ $V_{CC} = 125 \text{ V}$ $t_p = 30 \text{ µs}$		2.5 0.2		μs μs
t _s	Inductive load Storage time Fall time	$I_C = 2 \text{ A}, V_{CC} = 200 \text{ V}$ $V_{BE(off)} = -5 \text{ V} I_{B(on)} = 400 \text{ m}$ $R_{BB} = 0, L = 200 \mu\text{H}$	A	0.6 0.1		μs μs

^{1.} Pulse test: pulse duration \leq 300 µs, duty cycle \leq 2 %.

2.1 Electrical characteristics (curves)

Figure 2. DC current gain $(V_{CE} = 1.5 \text{ V})$

Figure 3. DC current gain $(V_{CE} = 5 V)$

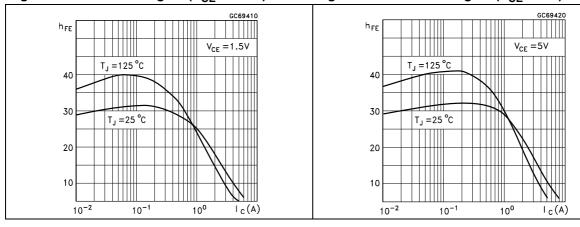


Figure 4. Collector-emitter saturation voltage Figure 5. Base-emitter saturation voltage

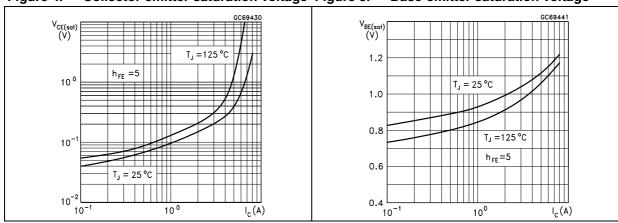


Figure 6. Inductive load fall time

Figure 7. Inductive load storage time

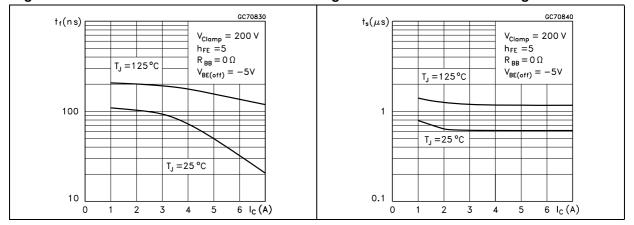


Figure 8. Resistive load fall time

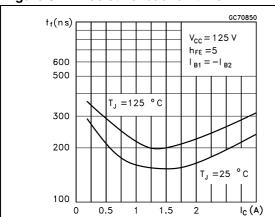


Figure 9. Resistive load storage time

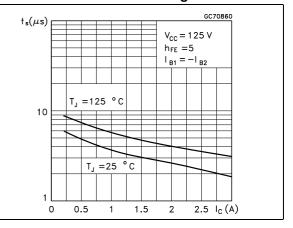
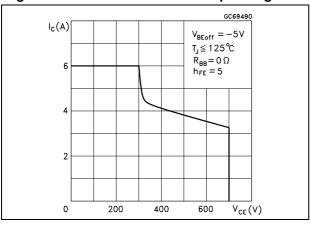


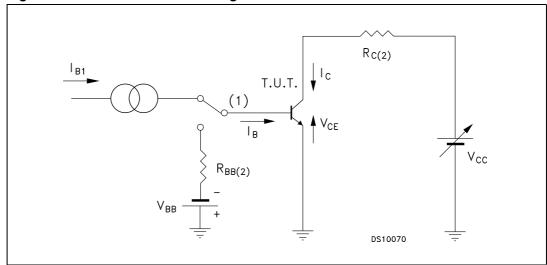
Figure 10. Reverse biased safe operating area



Test circuits STD845DN40

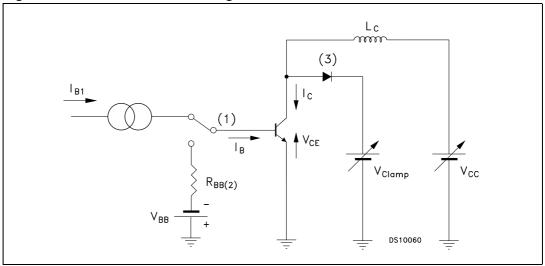
3 Test circuits

Figure 11. Resistive load switching test circuit



- 1. Fast electronic switch
- 2. Non-inductive resistor

Figure 12. Inductive load switching test circuit



- 1. Fast electronic switch
- 2. Non-inductive resistor
- 3. Fast recovery rectifier

4 Package mechanical data

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Table 5. DIP-8 mechanical data

Dim.		mm.	
	Min.	Тур.	Max.
А			4.80
A1	0.50		
A2	3.10		3.50
A3	1.40		1.60
b	0.38		0.55
b1	0.38		0.51
b2	1.47		1.57
b3	0.89		1.09
С	0.21		0.35
c1	0.20		0.30
D	9.10		9.30
D1	0.13		
Е	7.62		8.25
E1	6.25		6.45
е		2.54	
eA		7.62	
eB	7.62		10.90
eC	0		1.52
L	2.92		3.81

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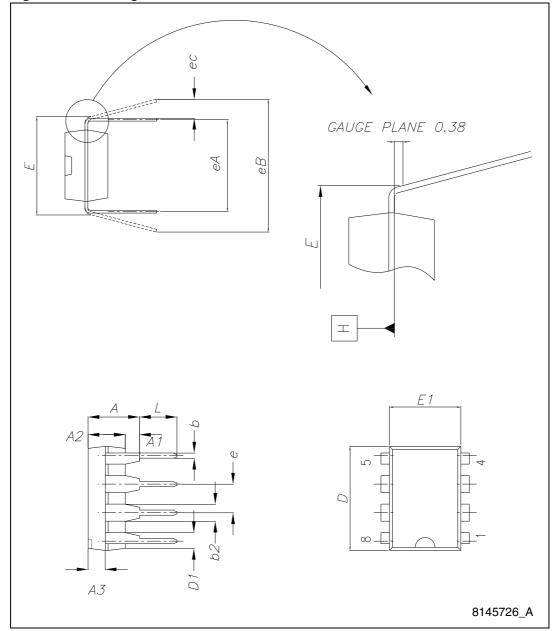


Figure 13. Drawing dimension DIP-8

Doc ID 17211 Rev 3

STD845DN40 Revision history

5 Revision history

Table 6. Document revision history

Date	Revision	Changes
03-Mar-2010	1	Initial release.
16-Apr-2010	2	Inserted P _{TOT} and R _{thJA} values <i>Table 2</i> and <i>Table 3 on page 2</i> .
23-Oct-2012	3	Modified P _{TOT} and R _{thJA} values in <i>Table 2</i> and <i>Table 3 on page 2</i> .

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